**Superscalar out of order architectural simulator (With Memory Hierarchy)**

A project for CSEN702 Microprocessors

Supervised by:

* Dr. Mohamed Taher
* Eng. Jolly Salah

Team Members:

* Ahmed Tarek 31-4177
* Mostafa Ibrahim
* Omar
* Nada Bahaa

How the work was divided:

* Memory Hierarchy: Ahmed Tarek
* Functional Units: Mostafa Ibrahim
* Tomasulo: Mostafa Ibrahim, Nada Bahaa, Omar Yousry
* Utilities: Ahmed Tarek
* Assembler: Ahmed Tarek