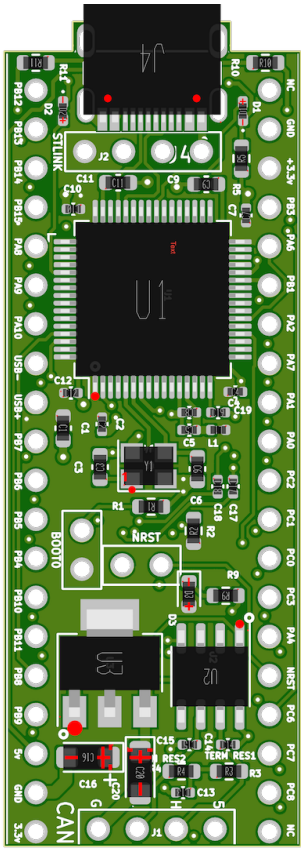
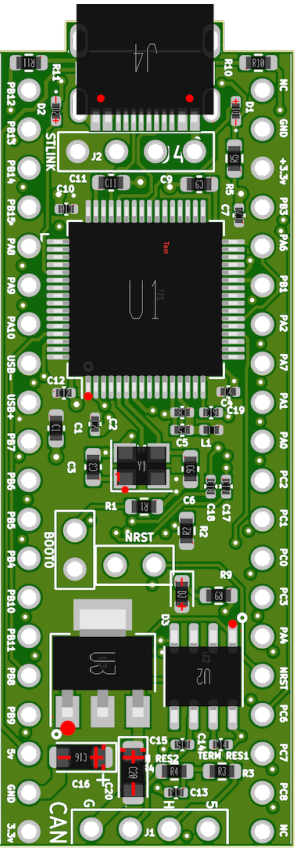


			F405	PIN
			pin#	NAME
SPI			33	PB12
SPI2_NSS			34	PB13
SPI2_SCK			35	PB14
SPI2_MISO			36	PB15
SPI2_MOSI			41	PA8
			42	PA9
			43	PA10
			44	PA11
			45	PA12
			59	PB7
			58	PB6
SPI1_MOSI			57	PB5
SPI3_MISO			56	PB4
SPI2_SCK			29	PB10
			30	PB11
			61	PB8
			62	PB9
				5v
				GND
				3.3v



PIN	F405				
NAME	pin#	ADC	UART	SPI	
NC					
GND					
3.3v					
PB3	55			SPI3_SCK	
PA6	22	ADC		SPI1_MISO	
PB1	27	ADC			
PA2	16	ADC			
PA7	23	ADC		SPI1_MOSI	
PA1	15	ADC	UART4_RX		
PA0	14	ADC	UART4_TX		
PC2	10	ADC		SPI2_MISO	
PC1	9	ADC			
PC0	8	ADC			
PC3	11	ADC		SPI2_MOSI	
PA4	20	ADC		SPI1_NSS	
NRST	7	ADC			
PC6	37				
PC7	38				
PC8	39				
NC					

MP2 Net name	PIN
	NAME
A_OVERLIMIT	PB12
LIN_A	PB13
LIB_B	PB14
LIB_C	PB15
HIN_A	PA8
HIN_B	PA9
HIN_C	PA10
DUSB_DM	PA11
DUSB_DP	PA12
TIM2 CH1	PB7
TIM2 CH2	PB6
TIM3 CH1	PB5
TIM3 CH2	PB4
D_UART1_TX I2C1_SCL TIM4_CH1	PB10
D_UART1_RX I2C1_SDA TIM4_CH2	PB11
DI2C1_SCL TIM4_CH3	PB8
DI2C1_SDA TIM4_CH4	PB9
V5V	5v
GND	GND
V3V3	3.3v



PIN	MP2 Net name
NAME	
NC	NC
GND	GND
3.3v	NC
PB3	PB10 D_GPIO
PA6	PB2 D_GPIO
PB1	JP3
PA2	A_PHC_VSENSE
PA7	JP2
PA1	A_PHB_VSENSE
PA0	A_PHA_VSENSE
PC2	A_SHC_AMP
PC1	A_SHB_AMP
PC0	A_SHA_AMP
PC3	VSENSE
PA4	A_THROTTLE
NRST	JP3
PC6	HALL_C
PC7	HALL_B
PC8	HALL_A
NC	NC