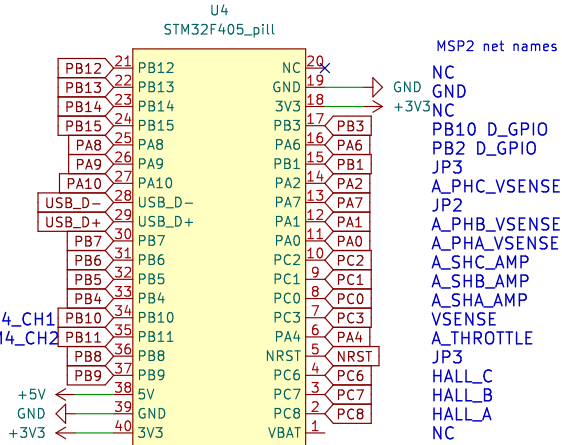


Fig 17  
<https://www.nxp.com/docs/en/supporting-information/AH1014.pdf>

MSP2 net names

A\_OVERLIMIT  
 LIN\_A  
 LIB\_B  
 LIB\_C  
 HIN\_A  
 HIN\_B  
 HIN\_C  
 DUSB\_DM  
 DUSB\_DP  
 TIM2\_CH1  
 TIM2\_CH2  
 TIM3\_CH1  
 TIM3\_CH2  
 D\_UART1\_TX I2C1\_SCL TIM4\_CH1  
 D\_UART1\_RX I2C1\_SDA TIM4\_CH2  
 DI2C1\_SCL TIM4\_CH3  
 DI2C1\_SDA TIM4\_CH4  
 V5V  
 GND  
 V3V3

## PILL HEADERS



## ST-LINK

