SLOS098D - AUGUST 1991 - REVISED MAY 1998

- 1/2 V_I Virtual Ground for Analog Systems
- Self-Contained 3-terminal TO-226AA Package
- Micropower Operation . . . 170 μA Typ,
 V_I = 5 V
- Wide V_I Range . . . 4 V to 40 V
- High Output-Current Capability
 - Source . . . 20 mA Typ
 - Sink . . . 20 mA Typ

description

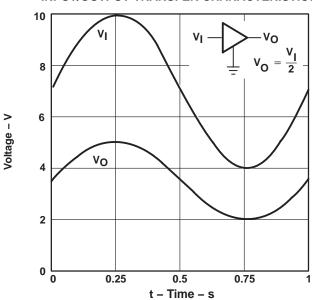
In signal-conditioning applications utilizing a single power source, a reference voltage equal to one-half the supply voltage is required for termination of all analog signal grounds. Texas Instruments presents a precision virtual ground whose output voltage is always equal to one-half the input voltage, the TLE2426 "rail splitter."

The unique combination of a high-performance, micropower operational amplifier and a precision-trimmed divider on a single silicon chip results in a precise $V_{\text{O}}/V_{\text{I}}$ ratio of 0.5 while sinking and sourcing current. The TLE2426 provides a low-impedance output with 20 mA of sink and source capability while drawing less than 280 μA

Excellent Output Regulation

- $-45 \,\mu\text{V}$ Typ at $I_{O} = 0$ to $-10 \,\text{mA}$
- $+15 \mu V$ Typ at $I_0 = 0$ to +10 mA
- Low-Impedance Output . . . 0.0075 Ω Typ
- Noise Reduction Pin (D, JG, and P Packages Only)

INPUT/OUTPUT TRANSFER CHARACTERISTICS



of supply current over the full input range of 4 V to 40 V. A designer need not pay the price in terms of board space for a conventional signal ground consisting of resistors, capacitors, operational amplifiers, and voltage references. The performance and precision of the TLE2426 is available in an easy-to-use, space saving, 3-terminal LP package. For increased performance, the optional 8-pin packages provide a noise-reduction pin. With the addition of an external capacitor (C_{NR}) , peak-to-peak noise is reduced while line ripple rejection is improved.

Initial output tolerance for a single 5-V or 12-V system is better than 1% with 3.6% over the full 40-V input range. Ripple rejection exceeds 12 bits of accuracy. Whether the application is for a data acquisition front end, analog signal termination, or simply a precision voltage reference, the TLE2426 eliminates a major source of system error.

AVAILABLE OPTIONS

	PA	CKAGED DEVICE	S		
T _A	SMALL OUTLINE (D)	CERAMIC DIP (JG)	PLASTIC (LP)	PLASTIC DIP (P)	CHIP FORM (Y)
0°C to 70°C	TLE2426CD	_	TLE2426CLP	TLE2426CP	



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.



TLE2426, TLE2426Y THE "RAIL SPLITTER" PRECISION VIRTUAL GROUND SLOS098D - AUGUST 1991 - REVISED MAY 1998

-40°C to 85°C	TLE2426ID	_	TLE2426ILP	TLE2426IP	TLE2426Y
-55°C to 125°C	TLE2426MD	TLE2426MJG	TLE2426MLP	TLE2426MP	

The D and LP packages are available taped and reeled in the commercial temperature range only. Add R suffix to the device type (e. g., TLC2426CDR). Chips are tested at 25°C.



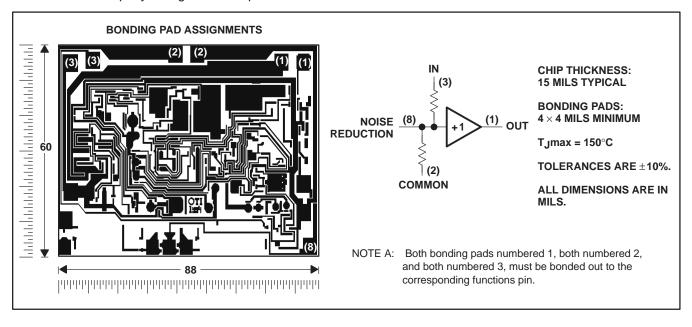
description (continued)

The C-suffix devices are characterized for operation from 0° C to 70° C. The I suffix devices are characterized for operation from -40° C to 85° C. The M suffix devices are characterized over the full military temperature range of -55° C to 125° C.



TLE2426Y chip information

This chip, properly assembled, displays characteristics similar to the TLE2426C. Thermal compression or ultrasonic bonding may be used on the doped aluminum bonding pads. The chips may be mounted with conductive epoxy or a gold-silicon preform.



absolute maximum ratings over operating free-air temperature (unless otherwise noted)†

Continuous input voltage, V _I		
Continuous filter trap voltage		
Output current, IO		±80 mA
Duration of short-circuit current at (or belo	ow) 25°C (see Note 1)	unlimited
Continuous total power dissipation		. See Dissipation Rating Table
Operating free-air temperature range, TA:	C suffix	0°C to 70°C
	I suffix	–40°C to 85°C
	M suffix	–55°C to 125°C
Storage temperature range, T _{stq}		–65°C to 150°C
Lead temperature 1,6 mm (1/16 inch) from	n case for 10 seconds: D or P packa	age 260°C
Lead temperature 1.6 mm (1/16 inch) from	n case for 60 seconds: JG or LP page	ckage 300°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

DISSIPATION RATING TABLE

PACKAGE	$T_{\mbox{A}} \le 25^{\circ}\mbox{C}$ POWER RATING	DERATING FACTOR ABOVE T _A = 25°C	T _A = 70°C POWER RATING	T _A = 85°C POWER RATING	T _A = 125°C POWER RATING
D	725 mV	5.8 mW/°C	464 mW	377 mW	145 mW
JG	1050 mV	8.4 mW/°C	672 mW	546 mW	210 mW
LP	775 mV	6.2 mW/°C	496 mW	403 mW	155 mW
Р	1000 mV	8.0 mW/°C	640 mW	520 mW	200 mW

recommended operating conditions

		C SUFFIX		I SUFFIX		M SUFFIX	
	MIN	MAX	MIN	MAX	MIN	MAX	X UNIT
Input voltage, V _I	4	40	4	40	4	40	V
Operating free-air temperature, TA	0	70	-40	85	-55	125	°C



NOTE 1: The output may be shorted to either supply. Temperature and/or supply voltages must be limited to ensure that the maximum dissipation rating is not exceeded.

electrical characteristics at specified free-air temperature, $V_I = 5 V$, $I_O = 0$ (unless otherwise noted)

DADAMETER	TEST CONDITIO		- +	Τι	LE24260	;		
PARAMETER	TEST CONDITIO	ons .	T _A †	MIN	TYP	300 400 ±160 ±250 ±250 ±250 ±250 ±250	UNIT	
	V _I = 4 V			1.98	2	2.02		
	V _I = 5 V		25°C	2.48	2.5	2.52] ,,	
Output voltage	V _I = 40 V			19.8	20	20.2	V	
	V _I = 5 V		Full range	2.475		2.525	1	
Temperature coefficient of output voltage			Full range		25		ppm/°C	
Our also summed	Madaad	V _I = 5 V	25°C		170	300		
Supply current	No load	V _I = 4 to 40 V	Full range			400	μΑ	
			25°C		-45	±160		
Output voltage regulation (sourcing current)‡	$I_{O} = 0 \text{ to } -10 \text{ mA}$		Full range			±250	μV	
(Sourcing current)+	$I_0 = 0 \text{ to } -20 \text{ mA}$		25°C		-150	±450	1	
	I _O = 0 to 10 mA		25°C		15	±160		
Output voltage regulation sinking current)‡			Full range			±250	μV	
(Sinking current)+	I _O = 0 to 20 mA		25°C		65	±235	1	
Output impedance			25°C		7.5	22.5	mΩ	
Noise-reduction impedance			25°C		110		kΩ	
	Sinking current,	V _O = 5 V	2702		26			
Short-circuit current	Sourcing current,	VO = 0	25°C		-47		mA	
		$C_{NR} = 0$	2702		120		.,	
Output noise voltage, rms	f = 10 Hz to 10 kHz	C _{NR} = 1 μF	25°C		30		μV	
		C _L = 0	0.700		290			
	V_{O} to 0.1%, $I_{O} = \pm 10 \text{ mA}$	C _L = 100 pF	25°C		275		1	
Output voltage current step response	V + 0.040/ 1 + 1/2 1	C _L = 0	0500		400		μs	
	V_{O} to 0.01%, $I_{O} = \pm 10 \text{ mA}$	C _L = 100 pF	25°C		390			
C+	$V_I = 0 \text{ to } 5 \text{ V}, V_O \text{ to } 0.1\%$	C: 400 = 5	0500		20			
Step response	$V_I = 0 \text{ to } 5 \text{ V}, V_O \text{ to } 0.01\%$	C _L = 100 pF	25°C		160		μs	

[†]Full range is 0°C to 70°C.

[‡]The listed values are not production tested.

TLE2426, TLE2426Y THE "RAIL SPLITTER" PRECISION VIRTUAL GROUND SLOS098D - AUGUST 1991 - REVISED MAY 1998

electrical characteristics at specified free-air temperature, V_{I} = 12 V, I_{O} = 0 (unless otherwise noted)

DADAMETED	TEST COMPLETO	NO	- +	TI	LE24260	;		
PARAMETER	TEST CONDITIO	NS	T _A †	MIN	TYP	MAX	UNIT	
	V _I = 4 V			1.98	2	2.02		
	V _I = 12 V		25°C	5.95	6	6.05		
Output voltage	V _I = 40 V			19.8	20	20.2	V	
	V _I = 12 V		Full range	5.945		6.055		
Temperature coefficient of output voltage			Full range		35		ppm/°C	
		V _I = 12 V	25°C		195	300		
Supply current	No load	V _I = 4 to 40 V	Full range			400	μΑ	
		•	25°C		-45	±160		
Output voltage regulation (sourcing current)‡	$I_{O} = 0 \text{ to } -10 \text{ mA}$		Full range			±250	μV	
(Sourcing current)+	$I_0 = 0 \text{ to } -20 \text{ mA}$		25°C		-150	MAX 2.02 6.05 20.2 6.055 pppi 300 400 ±160 ±250 ±450 ±250 ±235 22.5 r		
	I _O = 0 to 10 mA		25°C		15	±160		
Output voltage regulation sinking current)‡			Full range			±250	μV	
(Sinking Current)+	I _O = 0 to 20 mA	25°C		65	±235			
Output impedance			25°C		7.5	22.5	mΩ	
Noise-reduction impedance			25°C		110		kΩ	
	Sinking current,	V _O = 12 V	0500		31			
Short-circuit current	Sourcing current,	VO = 0	25°C		-70		mA	
	4011 4 40111	$C_{NR} = 0$	0500		120		.,	
Output noise voltage, rms	f = 10 Hz to 10 kHz	C _{NR} = 1 μF	25°C		30		μV	
	V + 0.40′ 1 + 40 A	$C_L = 0$	0500		290			
	V_{O} to 0.1%, $I_{O} = \pm 10 \text{ mA}$	C _L = 100 pF	25°C		275			
Output voltage current step response		C _L = 0			400		μs	
	V_{O} to 0.01%, $I_{O} = \pm 10 \text{ mA}$	C _L = 100 pF	25°C		390			
Chan manning	$V_I = 0 \text{ to } 12 \text{ V}, V_O \text{ to } 0.1\%$	C: 400 = 5	0500		20			
Step response	$V_I = 0 \text{ to } 12 \text{ V}, V_O \text{ to } 0.01\%$	C _L = 100 pF	25°C		120		μs	

[†] Full range is 0°C to 70°C.

[‡]The listed values are not production tested.

electrical characteristics at specified free-air temperature, $V_I = 5 V$, $I_O = 0$ (unless otherwise noted)

			_ +	Т	LE2426I				
PARAMETER	TEST CONDITION	ONS	'A'	MIN	TYP	MAX	UNIT		
	V _I = 4 V			1.98	2	2.02			
_	V _I = 5 V		25°C	2.48	2.5	2.52	1		
Output voltage	V _I = 40 V		1	19.8	20	20.2	V		
	V _I = 5 V		No column TYP MAX	2.53	1				
Temperature coefficient of output voltage			Full range		25		ppm/°0		
		V _I = 5 V	25°C		170	300			
Supply current	No load	V _I = 4 to 40 V	Full range			400	μΑ		
		•	25°C		-45	±160			
Output voltage regulation	$I_{O} = 0 \text{ to } -10 \text{ mA}$		Full range			±250	μV		
(sourcing current) [‡]	$I_{O} = 0 \text{ to } -20 \text{ mA}$		25°C		-150	±450			
Output voltage regulation Sinking current)‡	I _O = 0 to 10 mA		25°C		15	±160			
	$I_O = 0$ to 8 mA		Full range			±250	μV		
(Sinking current)+	I _O = 0 to 20 mA		25°C		65	±235	235		
Output impedance			25°C		7.5	22.5	mΩ		
Noise-reduction impedance			25°C		110		kΩ		
	Sinking current,	V _O = 5 V			26				
Short-circuit current	Sourcing current,	V _O = 0	25°℃		-47		mA		
		C _{NR} = 0			120		.,		
Output noise voltage, rms	f = 10 Hz to 10 kHz	C _{NR} = 1 μF	25°℃		30		μV		
		C _L = 0			290				
0	V_{O} to 0.1%, $I_{O} = \pm 10 \text{ mA}$	C _L = 100 pF	25°C		275		1		
Output voltage current step response	V + 0.040/ 1 + + + = 1	C _L = 0	2500		400		μs		
	V_O to 0.01%, $I_O = \pm 10 \text{ mA}$	C _L = 100 pF	25℃		390				
Cton manage	V _I = 0 to 5 V, V _O to 0.1%	- 0 to 5 V Vo to 0 1%			0500		20		
Step response	$V_I = 0 \text{ to } 5 \text{ V}, V_O \text{ to } 0.01\%$	CL = 100 pF	25°C	290 290 275 400 390	μs				

[†] Full range is –40°C to 85°C. ‡ The listed values are not production tested.

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electrical characteristics at specified free-air temperature, V_{I} = 12 V, I_{O} = 0 (unless otherwise noted)

	TEGT 00\IDITIO		- +	Т				
PARAMETER	TEST CONDITIO	INS	T _A †	MIN	TYP	MAX	UNIT	
	V _I = 4 V			1.98	2	2.02		
	V _I = 12 V		25°C	5.95	6	6.05	1	
Output voltage	V _I = 40 V			19.8	20	20.2	V	
	V _I = 12 V		Full range	5.935		6.065	Ī	
Temperature coefficient of output voltage			Full range		35		ppm/°C	
		V _I = 12 V	25°C		195	300		
Supply current	No load	V _I = 4 to 40 V	Full range			400	μΑ	
		•	25°C		-45	±160		
Output voltage regulation	$I_{O} = 0 \text{ to } -10 \text{ mA}$		Full range			±250	μV	
(sourcing current)‡	$I_0 = 0 \text{ to } -20 \text{ mA}$		25°C		-150	±450	1	
	I _O = 0 to 10 mA		25°C		15	±160		
Output voltage regulation sinking current)‡	$I_O = 0$ to 8 mA		Full range			±250	μV	
(sinking current)+	I _O = 0 to 20 mA		25°C		65	±235		
Output impedance			25°C		7.5	22.5	mΩ	
Noise-reduction impedance			25°C		110		kΩ	
	Sinking current,	V _O = 12 V		31				
Short-circuit current	Sourcing current,	VO = 0	25°C		6 6.05 20 20.2 6.065 35 p 195 300 400 -45 ±160 ±250 -150 ±450 15 ±160 ±250 65 ±235 7.5 22.5	mA		
	$\begin{array}{c} \text{No load} \\ \\ \text{Ition} \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\$			120				
Output noise voltage, rms	f = 10 Hz to 10 kHz	C _{NR} = 1 μF	25°C		30	6 6.05 0 20.2 6.065 5 300 400 5 ±160 ±250 0 ±450 5 ±250 1 0 0 0 0 0 0 0 0 0 0 0 0 0 0	μV	
		C _L = 0			290			
	V_{O} to 0.1%, $I_{O} = \pm 10 \text{ mA}$		25°C		275		1	
Output voltage current step response					400		μs	
	V_O to 0.01%, $I_O = \pm 10$ mA	C _L = 100 pF	25°C		390			
01	$V_I = 0 \text{ to } 12 \text{ V}, V_O \text{ to } 0.1\%$	0 400 = 5	0500		20			
Step response	$V_I = 0 \text{ to } 12 \text{ V}, V_O \text{ to } 0.01\%$	C _L = 100 pF	25°C		120		μs	

[†] Full range is –40°C to 85°C.

[‡]The listed values are not production tested.

electrical characteristics at specified free-air temperature, $V_I = 5 V$, $I_O = 0$ (unless otherwise noted)

DADAMETER	TEST SOUDITIO	NO	- +	ΤL	E2426N	Л		
PARAMETER	TEST CONDITIO	NS	T _A †	MIN	TYP	MAX 2.02 2.52 20.2 2.535 300 400 ±160 ±250 ±450 ±255 22.5	UNIT	
	V _I = 4 V			1.98	2	2.02		
	V _I = 5 V		25°C	2.48	2.5	2.52	.,	
Output voltage	V _I = 40 V			19.8	20	20.2	V	
	V _I = 5 V		Full range	2.465		2.535		
Temperature coefficient of output voltage			Full range		25		ppm/°C	
		V _I = 5 V	25°C		170	300		
Supply current	No load	V _I = 4 to 40 V	Full range			400	μΑ	
			25°C		-45	±160		
Output voltage regulation (sourcing current)	$I_O = 0 \text{ to } -10 \text{ mA}$		Full range			±250	μV	
(Sourcing current)+	$I_{O} = 0 \text{ to } -20 \text{ mA}$		25°C		-150	±450		
	I _O = 0 to 10 mA		25°C		15	±160		
Output voltage regulation sinking current)‡	$I_O = 0$ to 3 mA		Full range			±250	μV	
(Sinking current)+	I _O = 0 to 20 mA		25°C		65	±235		
Output impedance			25°C		7.5	22.5	mΩ	
Noise-reduction impedance			25°C		110		kΩ	
0	Sinking current,	V _O = 5 V	2502		26			
Short-circuit current	Sourcing current,	VO = 0	25°C		-47		mA	
0	4011 4 40111	C _{NR} = 0	2502		120		.,	
Output noise voltage, rms	f = 10 Hz to 10 kHz	C _{NR} = 1 μF	25°C		30		μV	
	V + 0.40′ L + 40 A	C _L = 0	2502		290			
	V_{O} to 0.1%, $I_{O} = \pm 10 \text{ mA}$	C _L = 100 pF	25°C		275			
Output voltage current step response	V . 0.0404 140 A	C _L = 0	2502		400		μs	
	V_{O} to 0.01%, $I_{O} = \pm 10 \text{ mA}$	C _L = 100 pF	25°C		390			
Cton reconomic	V _I = 0 to 5 V, V _O to 0.1%	C: 400 pF	25°C		20			
Step response	$V_I = 0 \text{ to } 5 \text{ V}, V_O \text{ to } 0.01\%$	C _L = 100 pF	25.0		120		μs	

[†] Full range is –55°C to 125°C.

[‡]The listed values are not production tested.

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electrical characteristics at specified free-air temperature, V_{I} = 12 V, I_{O} = 0 (unless otherwise noted)

24244555	TEST SOMETIC		- +	Τι	E2426N	И		
PARAMETER	TEST CONDITIO	NS	T _A †	MIN	TYP	MAX	UNIT	
	V _I = 4 V			1.98	2	2.02		
	V _I = 12 V		25°C	5.95	6	6.05] ,,	
Output voltage	V _I = 40 V			19.8	20	20.2	V	
	V _I = 12 V		Full range	5.925		6.075		
Temperature coefficient of output voltage			Full range		35		ppm/°C	
		V _I = 12 V	25°C		195	250		
Supply current	No load	V _I = 4 to 40 V	Full range			350	μΑ	
		•	25°C		-45	±160		
Output voltage regulation	I _O = 0 to – 10 mA		Full range			±250	μV	
(sourcing current) [‡]	$I_0 = 0 \text{ to } -20 \text{ mA}$		25°C		-150	MAX 2.02 6.05 20.2 6.075 ppm/ ^c 250 350 ±160 ±250 μV ±450 ±160 ±250 μV ±235		
	$I_{O} = 0$ to 10 mA		25°C		15	±160		
Output voltage regulation (sinking current)‡	I _O = 0 to 8 mA		Full range			±250	μV	
(Sinking current)+	I _O = 0 to 20 mA		25°C		65	±235		
Output impedance			25°C		7.5	22.5	mΩ	
Noise-reduction impedance			25°C		110		kΩ	
	Sinking current,	V _O = 12 V			31			
Short-circuit current	Sourcing current,	VO = 0	25°C		-70		mA	
		C _{NR} = 0			120		.,	
Output noise voltage, rms	f = 10 Hz to 10 kHz	C _{NR} = 1 μF	25°C		30		μV	
		C _L = 0			290			
	V_{O} to 0.1%, $I_{O} = \pm 10 \text{ mA}$	C _L = 100 pF	25°C		275			
Output voltage current step response		C _L = 0			400		μs	
	V_{O} to 0.01%, $I_{O} = \pm 10 \text{ mA}$	C _L = 100 pF	25°C		390			
0.	V _I = 0 to 12 V, V _O to 0.1%	0 400 -	2502		12		\Box	
Step response	$V_I = 0 \text{ to } 12 \text{ V}, V_O \text{ to } 0.01\%$	C _L = 100 pF	25°C		120		μs	

[†] Full range is –55°C to 125°C.

[‡] The listed values are not production tested.

electrical characteristics at specified free-air temperature, V_I = 5 V, I_O = 0, T_A = 25°C (unless otherwise noted)

DARAMETER	_	EST CONDITION	ıe	TLE2426Y				
PARAMETER	'	EST CONDITION	15	MIN	TYP	Y MAX	UNIT	
Output voltage	V _I = 5 V				2.5		V	
Supply current	No load				170		μΑ	
Outside all the second of the Committee of the Outside of the Outs	$I_{O} = 0 \text{ to } -10 \text{ n}$	nA			-45		\/	
Output voltage regulation (sourcing current)†	$I_0 = 0 \text{ to } -20 \text{ n}$	nA		MIN TYP MAX 2.5 170	μV			
Outside all to an analytic of the line and the	$I_{O} = 0$ to 10 m/	4			15		\/	
Output voltage regulation (sinking current)	$I_0 = 0 \text{ to } 20 \text{ m/s}$	4			65		μV	
Output impedance					7.5		$m\Omega$	
Noise-reduction impedance					110		kΩ	
Short-circuit current	Sinking current	Sinking current,			26		mA	
Short-circuit current	Sourcing curre	nt,	V _O = 0	MIN TYP MAX 2.5 170 -45 -150 15 65 7.5 110 26 -47 120 30 290 275 400 390 20	IIIA			
Output noise voltage, rms	f = 10 Hz to 10	I/U-7	$C_{NR} = 0$		120		μV	
Output hoise voitage, ims	1 = 10 112 to 10	KI IZ	$C_{NR} = 1 \mu F$	MIN TYP MAX 2.5 170 -45 -150 15 65 7.5 110 26 -47 120 30 290 275 400 390 20	μν			
	V _O to 0.1%,	$I_O = \pm 10 \text{ mA}$	$C_L = 0$		290			
Output voltage current step response	VO 10 0.1 76,	10 = ± 10 111A	$C_L = 100 pF$		275			
Output voltage current step response	V _O to 0.01%,	$I_0 = \pm 10 \text{ mA}$	$C_L = 0$		400		μs	
	VO 10 0.01%,	10 = ± 10 111A	$C_L = 100 pF$		390			
Ston roomono	$V_{I} = 0 \text{ to } 5 \text{ V},$	V _O to 0.1%	C: - 100 pE		20			
Step response	$V_{I} = 0 \text{ to } 5 \text{ V},$	V _O to 0.01%	C _L = 100 pF	MIN TYP MAX 2.5 170 -45 -150 15 65 7.5 110 26 -47 120 30 290 275 400 390 20	μs			

[†] The listed values are not production tested.

electrical characteristics at specified free-air temperature, V_I = 12 V, I_O = 0, T_A = 25°C (unless otherwise noted)

DADAMETED	_	EST CONDITION	TLE2426Y					
PARAMETER	1	EST CONDITION	MIN	TYP	MAX	UNIT		
Output voltage	V _I = 12 V				6		V	
Supply current	No load				195		μΑ	
Outside a literature and a literature for a second of the	$I_0 = 0 \text{ to } -10 \text{ m}$	ıA			-45		\/	
Output voltage regulation (sourcing current)†	$I_0 = 0 \text{ to } -20 \text{ m}$	ıA		-150			μV	
Outside the second of the Color of the second of the secon	$I_O = 0$ to 3 mA				15		.,	
Output voltage regulation (sinking current)	$I_0 = 0 \text{ to } 20 \text{ mA}$	I _O = 0 to 20 mA					μV	
Output impedance					7.5		$m\Omega$	
Noise-reduction impedance					110		kΩ	
Short-circuit current	Sinking current,		V _O = 12 V		31		mA	
Short-circuit current	Sourcing currer	nt,	VO = 0		-70		IIIA	
Output noise voltage, rms	f = 10 Hz to 10	レロフ	$C_{NR} = 0$		120		μV	
Output hoise voitage, ims	1 = 10 112 to 10	KI IZ	$C_{NR} = 1 \mu F$		30		μν	
	V _O to 0.1%,	lo - ±10 m∆	C _L = 0		290			
Output voltage current aton regneres	VO 10 0.1%,	$I_O = \pm 10 \text{ mA}$	C _L = 100 pF		275]	
Output voltage current, step response	Vo to 0.019/	1.40 1	C _L = 0		400		μѕ	
	V _O to 0.01%,	$I_O = \pm 10 \text{ mA}$	C _L = 100 pF		390			
Chara seesana	$V_{I} = 0 \text{ to } 12 \text{ V},$	V _O to 0.1%	0. 400 = 5		12			
Step response	$V_{I} = 0 \text{ to } 12 \text{ V},$	V _O to 0.01%	C _L = 100 pF		120		μs	

[†] The listed values are not production tested.

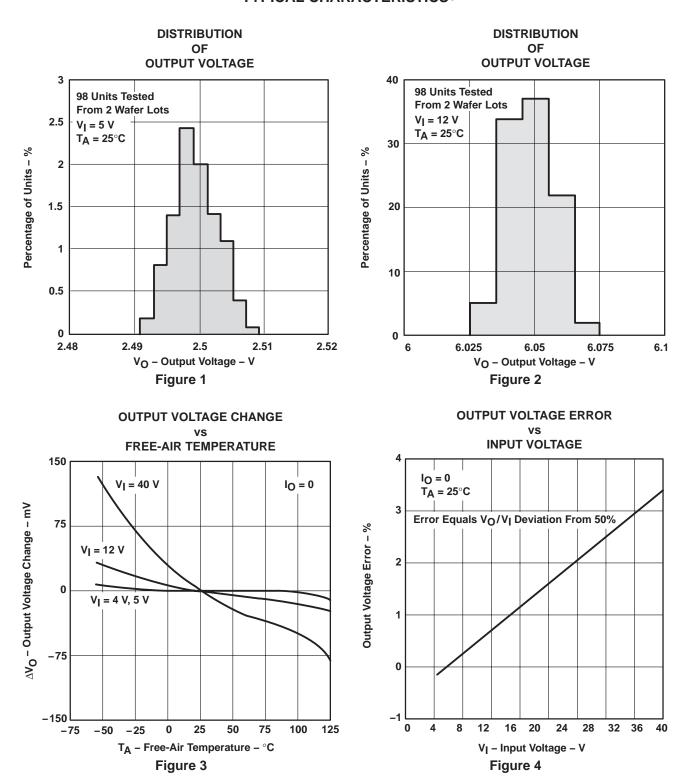


TYPICAL CHARACTERISTICS

Table Of Graphs

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Output voltage	Distribution	1,2
Output voltage change	vs Free-air temperature	3
Output voltage error	vs Input voltage	4
Land Manager	vs Input voltage	5
Input bias current	vs Free-air temperature	6
Output voltage regulation	vs Output current	7
Output impedance	vs Frequency	8
Ob and administration of a summer	vs Input voltage	9,10
Short-circuit output current	vs Free-air temperature	11,12
Ripple rejection	vs Frequency	13
Spectral noise voltage density	vs Frequency	14
Output voltage response to output current step	vs Time	15
Output voltage power-up response	vs Time	16
Output current	vs Load capacitance	17

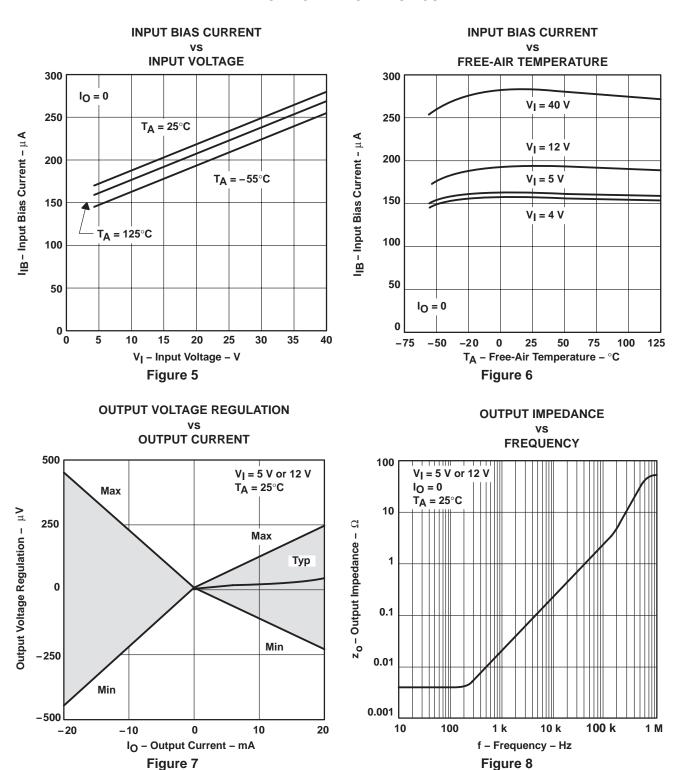
TYPICAL CHARACTERISTICS[†]



[†] Data at high and low temperatures are applicable within the rated operating free-air temperature ranges of the various devices.



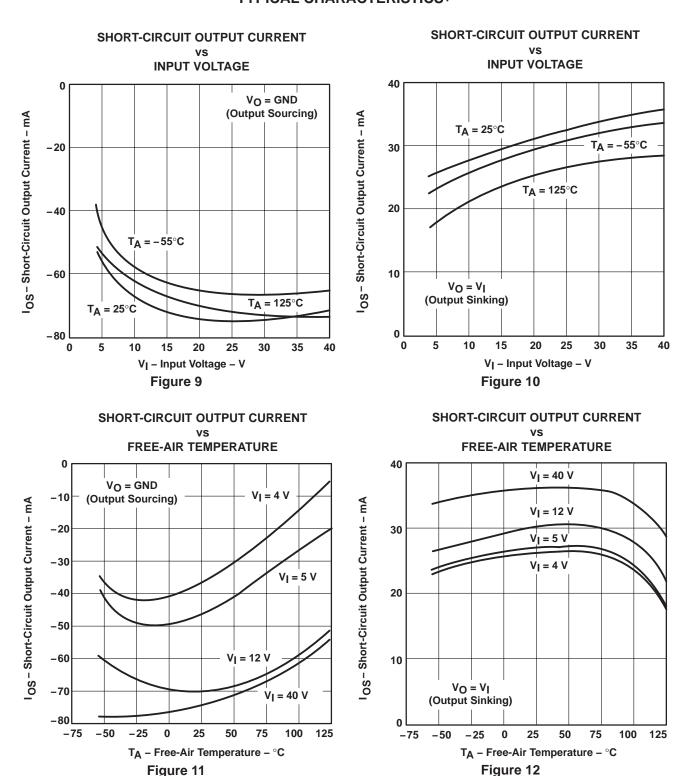
TYPICAL CHARACTERISTICS[†]



[†] Data at high and low temperatures are applicable within the rated operating free-air temperature ranges of the various devices.



TYPICAL CHARACTERISTICS[†]



[†] Data at high and low temperatures are applicable within the rated operating free-air temperature ranges of the various devices.



TYPICAL CHARACTERISTICS

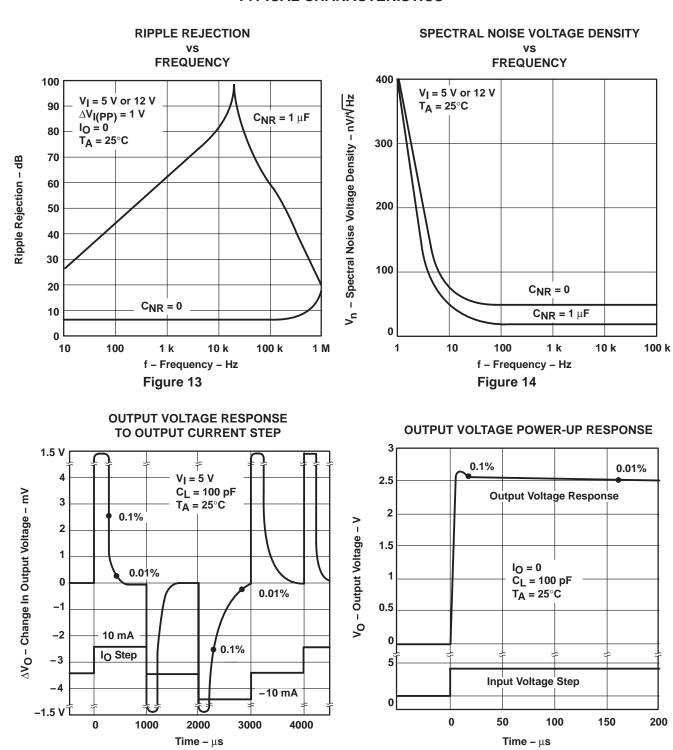




Figure 15

Figure 16

TYPICAL CHARACTERISTICS

STABILITY RANGE OUTPUT CURRENT VS LOAD CAPACITANCE

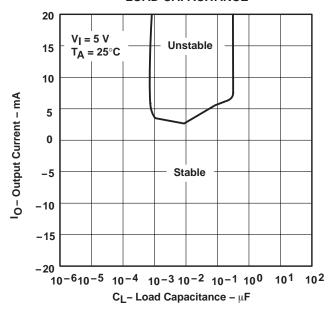


Figure 17

SLOS098D - AUGUST 1991 - REVISED MAY 1998

MACROMODEL INFORMATION

```
C1
          11 12 21.66E-12
   C2
              7 30.00E-12
   C3
          87
              0 10.64E-9
          85 86 15.9E-9
   CPSR
   DCM+
          81 82 DX
   DCM-
          83
             81 DX
   DC
          5
             53 DX
   DE
          54
              5 DX
          90 91 DX
   DLP
   DLN
          92
             90
                DX
   DΡ
              3 DX
          4
   ECMR
          84 99 (2,99) 1
                           (3,0) (4,0) (3,4) -16.2
              0 POLY(2)
   EGND
          99
                                        0
                                            .5 .5
                                 -16.22E-6 3.24E-6
          85
   EPSR
              0 POLY(1)
   ENSE
          89
              2 POLY(1)
                           (88,0) 120E-61
   FΒ
          7
             99 POLY(6)
                           VB VC VE VLPVLNVPSR 0 74.8E6 - 10E6 10E6 10E6 - 10E6 74E6
   GA
           6
              0
                11 12 320.4E-6
              6 10 99 1.013E-9
   GCM
           0
          85 86 (85,86)
   GPSR
                          100E-6
   GRC1
          4
             11
                 (4,11) 3.204E-4
   GRC2
          4
             12 (4,12) 3.204E-4
   GRE1
          13 10 (13,10)
                          1.038E-3
   GRE2
          14 10 (14,10)
                          1.038E-3
              0 VLIM 1K
   HT.TM
          90
   HCMR
          80
             1 POLY(2)
                          VCM+
                                 VCM- 0 1E2
   TRP
          3
              4 146E-6
   IEE
          3 10 DC 24.05E-6
   IIO
             0 .2E-9
             0 1E - 21
   T 1
          88
   Q1
          11
             89 13 QX
   02
          12 80 14 QX
   R2
          6
              9
                100.0E3
   RCM
          84
             81 1K
   REE
          10 99
                 8.316E6
   RN1
          87
              0
                2.55E8
   RN2
          87 88 11.67E3
   RO1
          8
              5
                63
           7
             99 62
   RO2
   VCM+
          82 99 1.0
   VCM-
          83
             99
                -2.3
              0 DC 0
   VB
          9
   VC
          3
             53 DC 1.400
   VE
          54
              4 DC
                   1.400
          7
              8 DC 0
   VLIM
   VLP
          91
              0 DC 30
          0 92 DC 30
   VLN
            86 DC
   VPSR
          0
                    0
   RFB
          5
             2 1K
   RTN1
              1 220K
          3
   RIN2
           1
              4 220K
.MODEL DX D(IS=800.OE-18)
.MODEL QX PNP(IS=800.OE-18BF=480)
.ENDS
```







10-Jun-2014

PACKAGING INFORMATION

Orderable Device	Status	Package Type		Pins		Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Samples
	(1)		Drawing		Qty	(2)	(6)	(3)		(4/5)	
5962-9555602Q2A	OBSOLETE	LCCC	FK	20		TBD	Call TI	Call TI	-55 to 125		
5962-9555602QPA	OBSOLETE	CDIP	JG	8		TBD	Call TI Call TI -55 to 1:		-55 to 125		
TLE2426CD	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM		2426C	Samples
TLE2426CDG4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM		2426C	Samples
TLE2426CDR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM		2426C	Samples
TLE2426CDRG4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM		2426C	Samples
TLE2426CLP	ACTIVE	TO-92	LP	3	1000	Pb-Free (RoHS)	CU SN	N / A for Pkg Type		2426C	Samples
TLE2426CLPE3	ACTIVE	TO-92	LP	3	1000	Pb-Free (RoHS)	CU SN	N / A for Pkg Type		2426C	Samples
TLE2426CLPR	ACTIVE	TO-92	LP	3	2000	Pb-Free (RoHS)	CU SN	N / A for Pkg Type		2426C	Samples
TLE2426CP	ACTIVE	PDIP	Р	8	50	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type		TLE2426CP	Samples
TLE2426ID	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM		24261	Samples
TLE2426IDG4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM		24261	Samples
TLE2426IDR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM		24261	Samples
TLE2426IDRG4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM		24261	Samples
TLE2426ILP	ACTIVE	TO-92	LP	3	1000	Pb-Free (RoHS)	CU SN	N / A for Pkg Type		24261	Samples
TLE2426ILPR	ACTIVE	TO-92	LP	3	2000	Pb-Free (RoHS)	CU SN	N / A for Pkg Type		24261	Samples
TLE2426IP	ACTIVE	PDIP	Р	8	50	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type		TLE2426IP	Samples
TLE2426IPE4	ACTIVE	PDIP	Р	8	50	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type		TLE2426IP	Samples



PACKAGE OPTION ADDENDUM

10-Jun-2014

Orderable Device	Status	Package Type	_	Pins	_	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Samples
	(1)		Drawing		Qty	(2)	(6)	(3)		(4/5)	
TLE2426MD	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-55 to 125	2426M	Samples
TLE2426MDG4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-55 to 125	2426M	Samples
TLE2426MFKB	OBSOLETE	LCCC	FK	20		TBD	Call TI	Call TI	-55 to 125		
TLE2426MJGB	OBSOLETE	CDIP	JG	8		TBD	Call TI	Call TI	-55 to 125		
TLE2426MLP	OBSOLETE	TO-92	LP	3		TBD	Call TI	Call TI	-55 to 125		
TLE2426MP	OBSOLETE	PDIP	Р	8		TBD	Call TI	Call TI	-55 to 125		

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

- (3) MSL, Peak Temp. The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.
- (4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
- (5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.
- (6) Lead/Ball Finish Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.



PACKAGE OPTION ADDENDUM

10-Jun-2014

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OTHER QUALIFIED VERSIONS OF TLE2426:

Automotive: TLE2426-Q1

● Enhanced Product: TLE2426-EP

NOTE: Qualified Version Definitions:

- Automotive Q100 devices qualified for high-reliability automotive applications targeting zero defects
- Enhanced Product Supports Defense, Aerospace and Medical Applications

PACKAGE MATERIALS INFORMATION

www.ti.com 26-Jan-2013

TAPE AND REEL INFORMATION





	Dimension designed to accommodate the component width
	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

7 til dillionololio aro nomina												
Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TLE2426CDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
TLE2426IDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1

www.ti.com 26-Jan-2013



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)	
TLE2426CDR	SOIC	D	8	2500	367.0	367.0	35.0	
TLE2426IDR	SOIC	D	8	2500	367.0	367.0	35.0	

JG (R-GDIP-T8)

CERAMIC DUAL-IN-LINE



NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- C. This package can be hermetically sealed with a ceramic lid using glass frit.
- D. Index point is provided on cap for terminal identification.
- E. Falls within MIL STD 1835 GDIP1-T8

FK (S-CQCC-N**)

LEADLESS CERAMIC CHIP CARRIER

28 TERMINAL SHOWN



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. This package can be hermetically sealed with a metal lid.
- D. Falls within JEDEC MS-004



P (R-PDIP-T8)

PLASTIC DUAL-IN-LINE PACKAGE



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. Falls within JEDEC MS-001 variation BA.



D (R-PDSO-G8)

PLASTIC SMALL OUTLINE



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0,15) each side.
- Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0,43) each side.
- E. Reference JEDEC MS-012 variation AA.





NOTES: A. All linear dimensions are in inches (millimeters).

B. This drawing is subject to change without notice.

Lead dimensions are not controlled within this area.

Falls within JEDEC TO−226 Variation AA (TO−226 replaces TO−92).

E. Shipping Method:

Straight lead option available in bulk pack only.

Formed lead option available in tape & reel or ammo pack.

Specific products can be offered in limited combinations of shipping mediums and lead options.

Consult product folder for more information on available options.





- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. Tape and Reel information for the Formed Lead Option package.

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