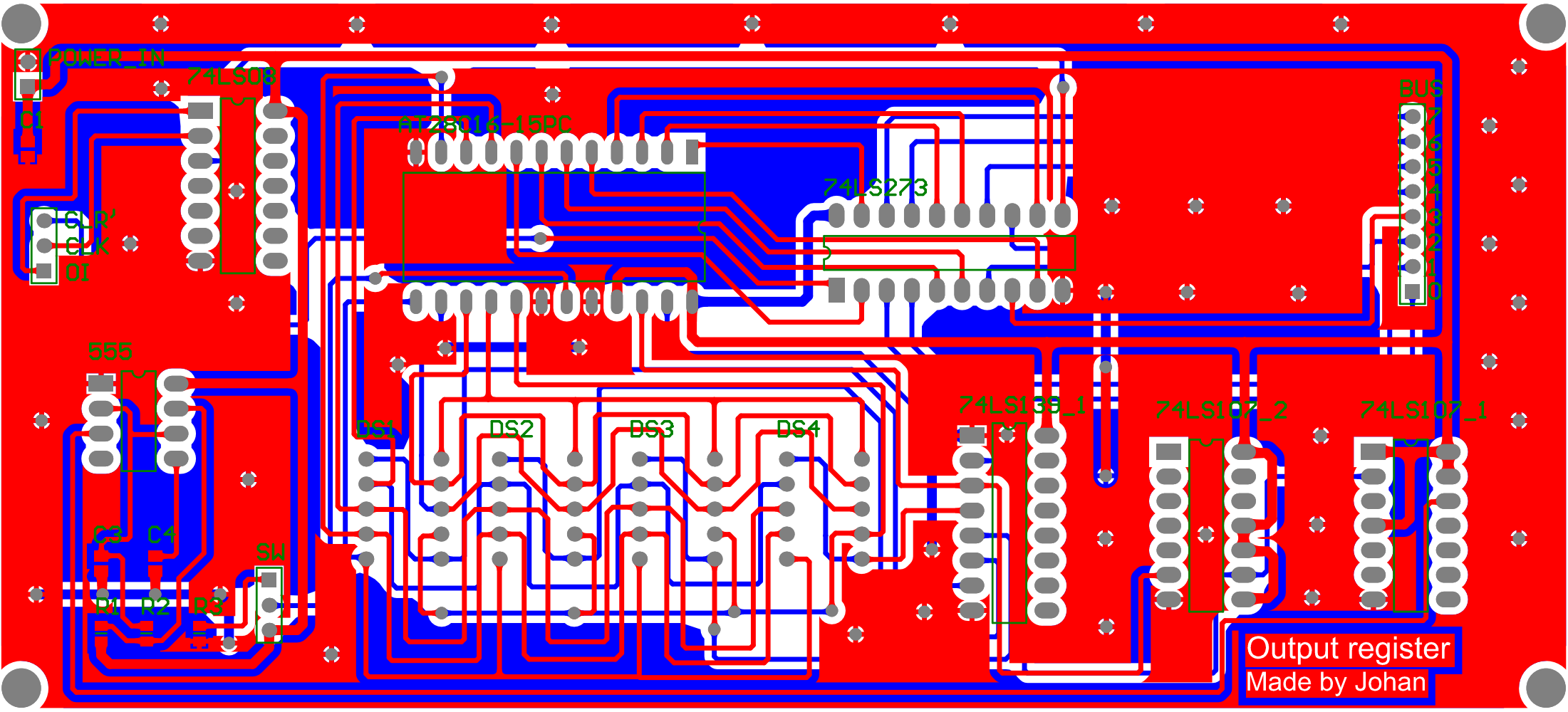
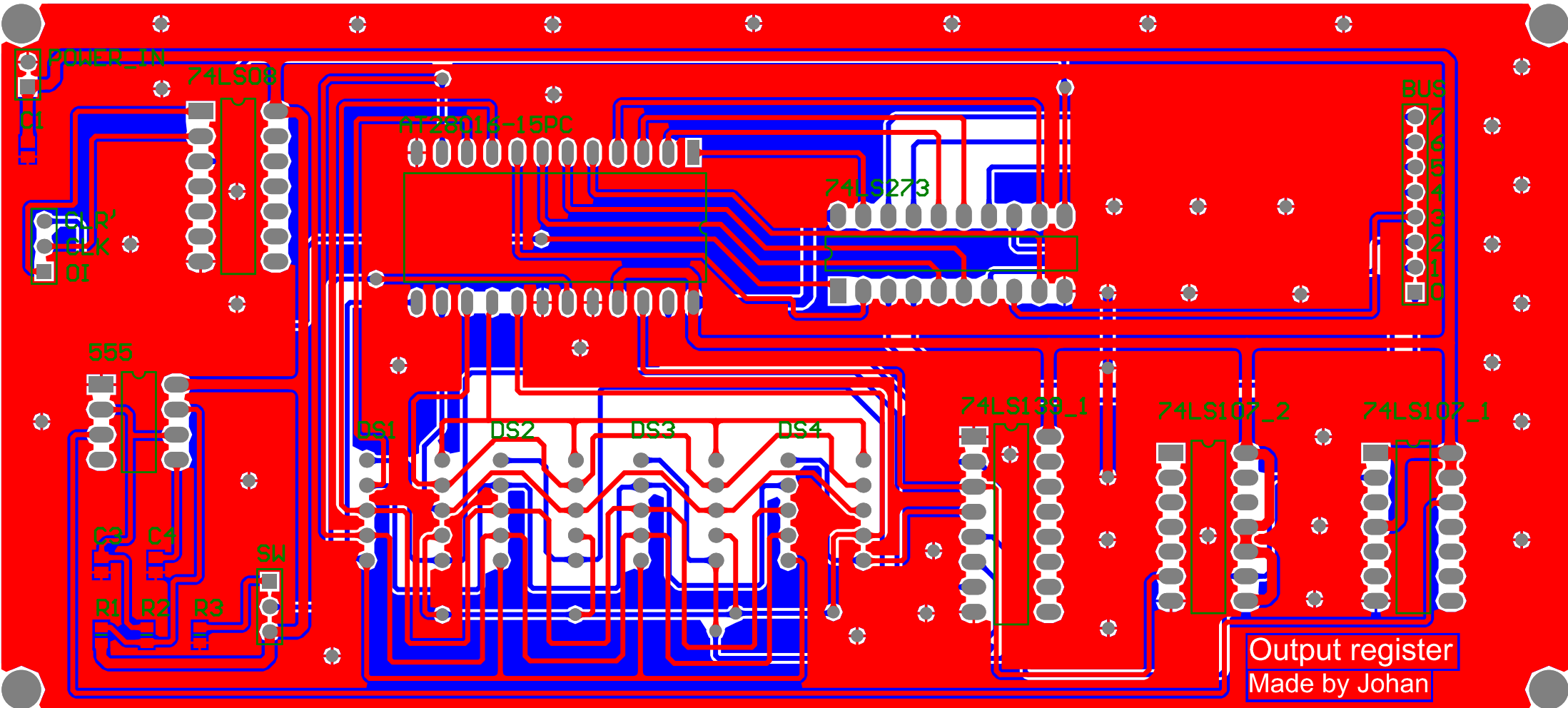


Title		
Size	Number	Revision
A4		
Date:	3-04-2020	Sheet of
File:	C:\Users\...\Sheet1.SchDoc	Drawn By:





Part List

Bill of Materials for BOM Document [output_register.BomDoc]

Source Data From:

output_register.BomDoc

Project:

output_register.PrjPcb

Variant:

None

Report Date: 04-03-2020

21:19

Print Date: 04-Mar-20

9:19:34 PM

Designator	Name	LibRef	Footprint	Description
74LS08	DM74LS08N	DM74LS08N	SN74LS08N	IC GATE AND 4CH 2-INP 14-DIP
74LS107_1, 74LS107_2	SN74LS107AN	SN74LS107AN	DM74LS107	Flip Flop JK-Type Neg-Edge 2-Element 14-Pin PDIP Tube
74LS139_1	SN74LS139AN	SN74LS139AN	SN74LS139AN	TEXAS INSTRUMENTS SN74LS139AN IC, DECODER/DEMUX, 74LS139
74LS273	SN74LS273N	SN74LS273N	SN74LS273N	IC D-TYPE POS TRG SNGL 20DIP
555	NE555P	NE555P	LM555	TEXAS INSTRUMENTS NE555P TIMER SINGLE PRECISION,DIP8 ,0.5MHZ
AT28C16-15PC	AT28C16-15PC	AT28C16-15PC	AT28C16-15PC	
BUS	Header 8	Header 8	HDR1X8	Header, 8-Pin
C1	0.1uF	Cap Pol3	C0805	Polarized Capacitor (Surface Mount)
C3, C4	0.01uF	Cap Pol3	C0805	Polarized Capacitor (Surface Mount)
CLOCK, SW	Header 3	Header 3	HDR1X3	Header, 3-Pin
DS1, DS2, DS3, DS4	Dpy Red-CA	Dpy Red-CA	7-segment	7.62 mm Black Surface HER 7-Segment Display: CA, RH DP
POWER_IN	Header 2	Header 2	HDR1X2	Header, 2-Pin
R1, R3	1k	Res3	6-0805_L	Resistor
R2	100k	Res3	6-0805_L	Resistor