20.11.2015

Istanbul Technical University
Department of Computer Engineering

BLG 231E - Digital Circuits
Assignment 4

Due Date: 26.11.2015, **Thursday,** 17.00.

- **Consequences of plagiarism:** Disciplinary regulations of The Council of Higher Education and of the university are applied.
- **No late submissions** will be accepted.

Submission:

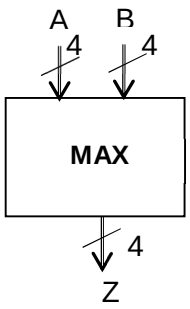
➢ Implement the circuit using the simulation program and submit the *.circ* file to Ninova.

Assignment:

The combinational circuit MAX compares two signed integers and forwards the greater number to the output.

Design and implement the circuit MAX using one parallel adder and other necessary logic units and gates.

Implement and test your design using the simulator program.



A, B 4-bit signed integers.

- If A >= B, then Z=A
- If A < B, then Z=B