

Device

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graph BT; Device[Device] --> Bus[Bus]; Bus[Bus] --> PCI[PCI];
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A hierarchical diagram showing three components in a vertical stack. The top component is labeled 'Device', the middle component is labeled 'Bus', and the bottom component is labeled 'PCI'. A vertical arrow points from the 'Bus' component up to the 'Device' component. Another vertical arrow points from the 'PCI' component up to the 'Bus' component. All components are enclosed in rectangular boxes with thick black borders.

Bus

PCI