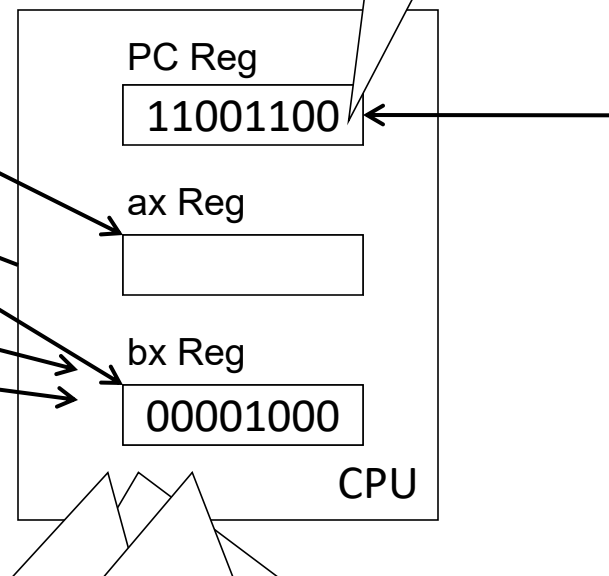


Address	Content	Meaning
11000000	00000010	?
11000001	00001000	?
11000010	00001010	?
.....		
11001100	00000001	
11001101	00000001	
11001110	11000000	
11001111	00000001	
11010000	00000010	
11010001	11000001	
11010010	00000100	
11010011	00000001	
11010100	00000010	
11010101	11000010	

Find next instruction from this address

① fetch  
③ Read from Memory



② Inter  
000000  
source  
bytes st  
compos  
So the r  
110011

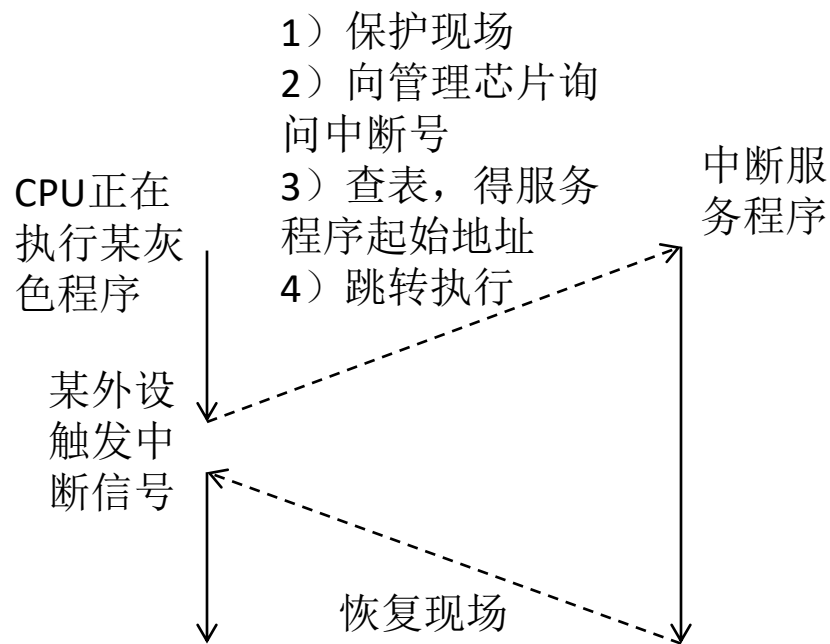
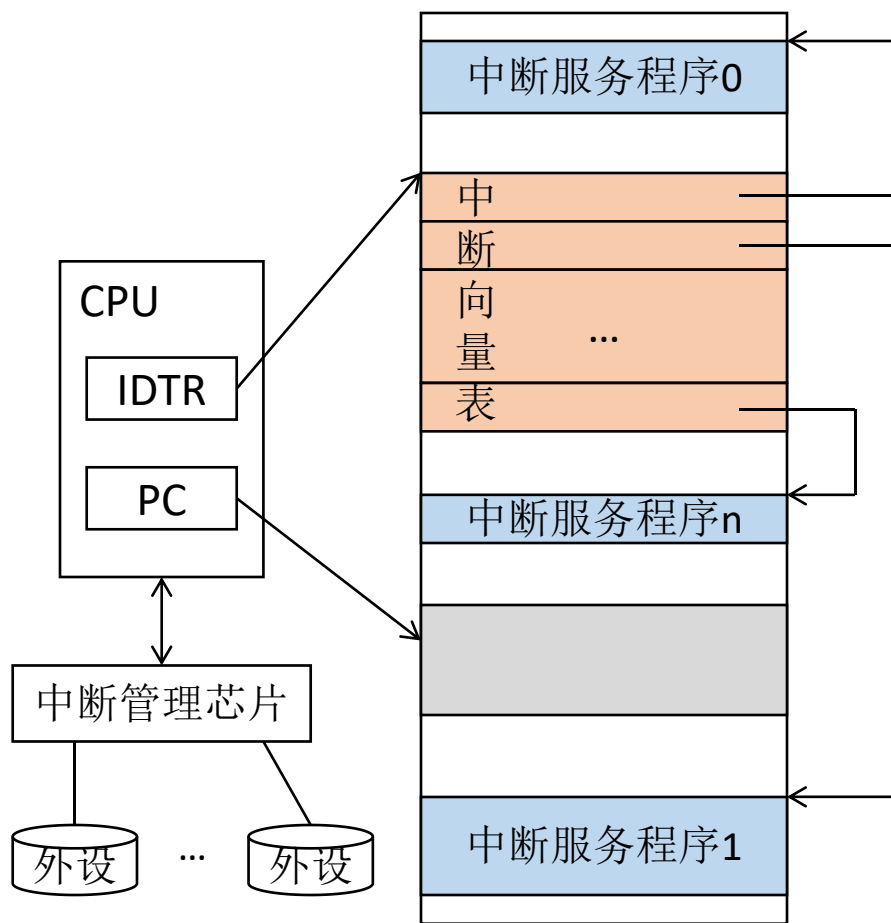
Next:

Fetch the 3rd instruction: 00000100, which is an ADD instruction and should have 3 parameters, so

- 1) The 4th instruction is stored at 11010010+4, i.e. 11010110
- 2) The full instruction is 00000100 00000001, 00000010, 11000010

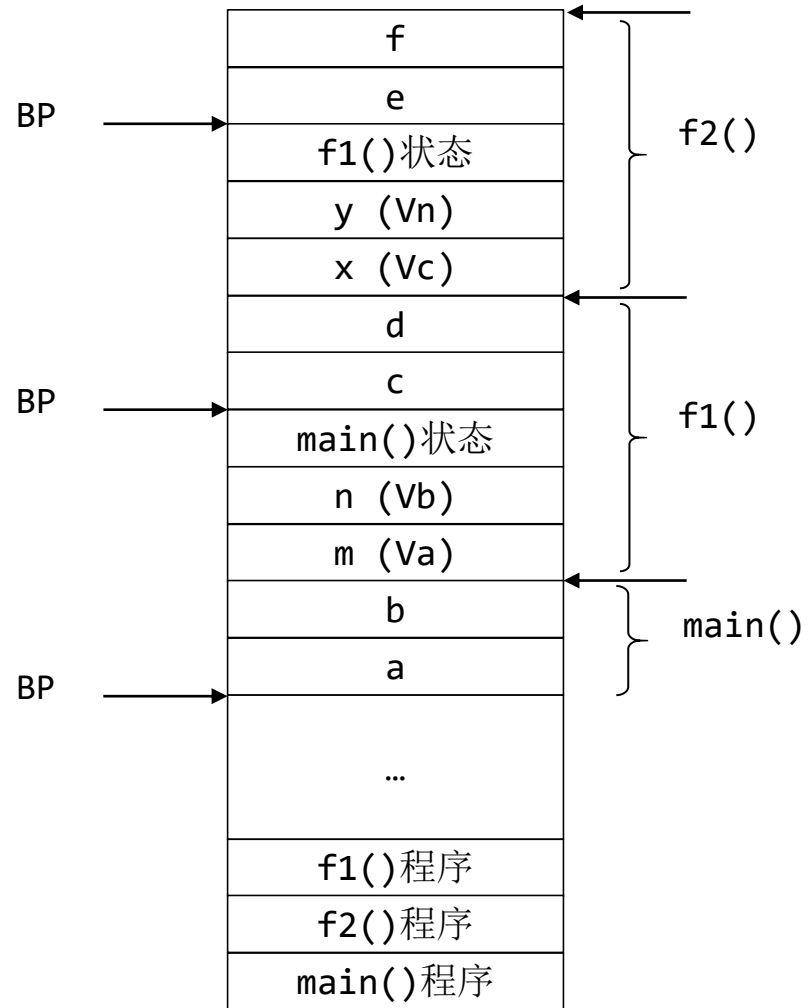
After execution, the numbers in ax and bx are sent to ALU for calculation, and the result is sent to memory address 11000010

# 中断



# 栈

```
void main() {  
    int a, b;  
    ...  
    f1(a, b);  
}  
void f1(int m, int n) {  
    int c, d;  
    ...  
    f2(c, n);  
}  
void f2(int x, int y) {  
    int e, f;  
    ...  
}
```



# 操作系统历史及类型

