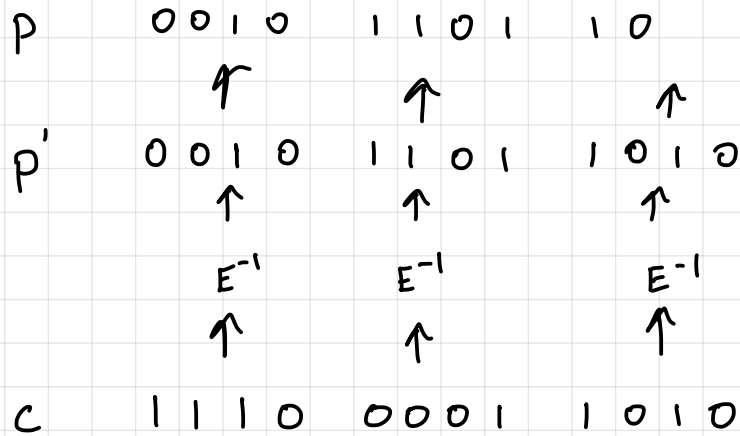


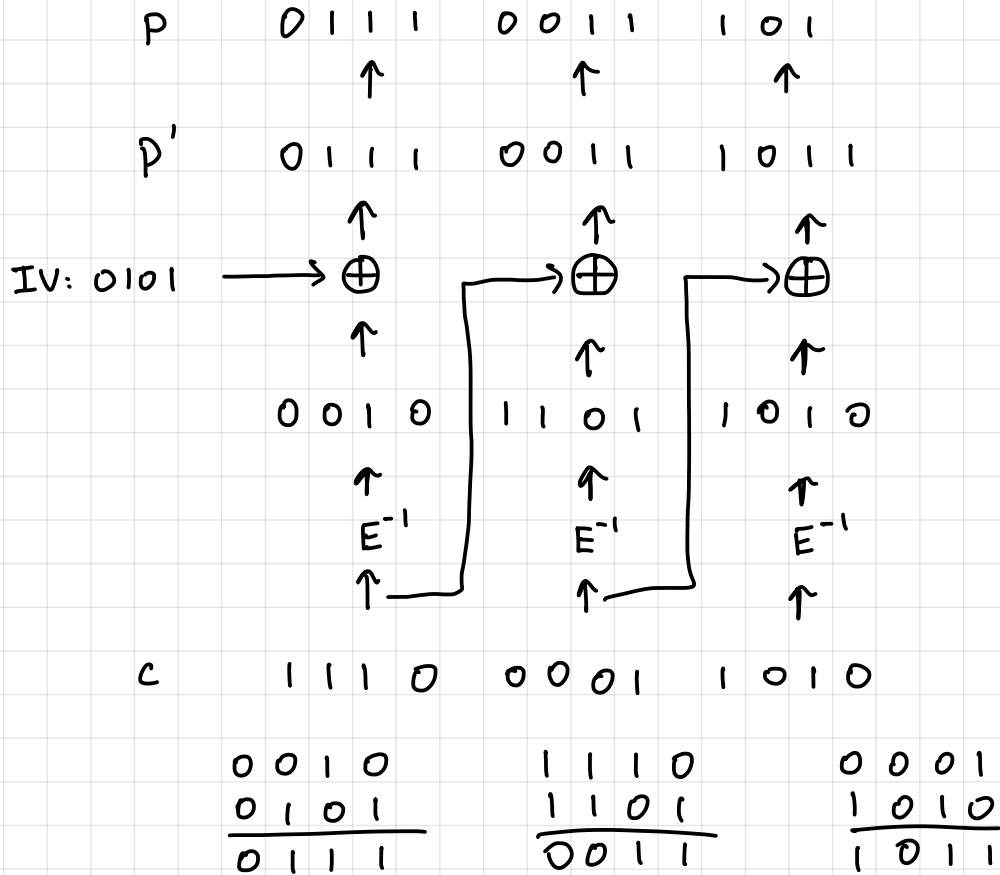
You are to *decrypt* a ciphertext that was *encrypted* using the permutation  $p$  :  $\{0,1\}^4 \rightarrow \{0,1\}^4$  defined as  $p(x) = \sim(x \ggg 1)$ , ie, rotate  $x$  RIGHT 1 bit and then negate all the bits. For example  $p(0011) = 0110$  because  $0011 \ggg 1 = 1001$  and  $\sim 1001 = 0110$ . Thus  $p^{-1}(x) = (\sim x \lll 1)$ . If you need an IV use 0101. If you need a nonce use 01. If the mode uses padding to handle arbitrary plaintext lengths, remove 10\* padding. If you need a counter, begin at 1.

Decrypt the ciphertext 1110 0001 1010 given that it was produced using ECB mode. Write four bits per box, with the final box possibly having fewer bits.



You are to *decrypt* a ciphertext that was *encrypted* using the permutation  $p$  :  $\{0,1\}^4 \rightarrow \{0,1\}^4$  defined as  $p(x) = \sim(x \ggg 1)$ , ie, rotate  $x$  RIGHT 1 bit and then negate all the bits. For example  $p(0011) = 0110$  because  $0011 \ggg 1 = 1001$  and  $\sim 1001 = 0110$ . Thus  $p^{-1}(x) = (\sim x \lll 1)$ . If you need an IV use 0101. If you need a nonce use 01. If the mode uses padding to handle arbitrary plaintext lengths, remove 10\* padding. If you need a counter, begin at 1.

Decrypt the ciphertext 1110 0001 1010 given that it was produced using CBC mode. Write four bits per box, with the final box possibly having fewer bits.

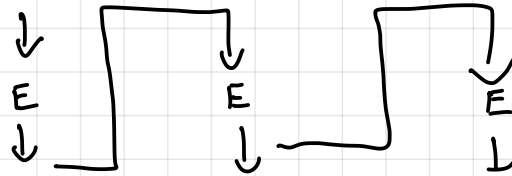


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negate all the bits. For example  $p(0011) = 0110$  because  $0011 \ggg 1 = 1001$  and  
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a nonce use 01. If the mode uses padding to handle arbitrary plaintext lengths,  
remove 10\* padding. If you need a counter, begin at 1.  
Decrypt the ciphertext 1110 0001 1010 given that it was produced using CTR  
mode. Write four bits per box, with the final box possibly having fewer bits.

$p$	0	1	0	1	0	1	1	0	0	1	1	1
		↓				↓				↓		
		E				E				E		
		↓				↓				↓		
keystream	0	1	0	1	1	1	0	0	0	1	0	0
		⊕				⊕				⊕		
$c$	1	1	1	0	0	0	0	1	1	0	1	0
<hr/>												
$p$	1	0	1	1	1	1	0	1	1	1	1	0

You are to *decrypt* a ciphertext that was *encrypted* using the permutation  $p$  :  $\{0,1\}^4 \rightarrow \{0,1\}^4$  defined as  $p(x) = \sim(x \ggg 1)$ , ie, rotate  $x$  RIGHT 1 bit and then negate all the bits. For example  $p(0011) = 0110$  because  $0011 \ggg 1 = 1001$  and  $\sim 1001 = 0110$ . Thus  $p^{-1}(x) = (\sim x \lll 1)$ . If you need an IV use 0101. If you need a nonce use 01. If the mode uses padding to handle arbitrary plaintext lengths, remove 10\* padding. If you need a counter, begin at 1.

IV: 0 1 0 1



key S	0 1 0 1	0 1 0 1	0 1 0 1
	$\oplus$	$\oplus$	$\oplus$
C	1 1 1 0	0 0 0 1	1 0 1 0
	<hr/>		
	1 0 1 1	0 1 0 0	1 1 1 1

GF(8) is defined like GF(256) except the polynomials all have degree less than 3 and the modulus is  $x^3 + x + 1$ . Calculate the following. Give each of your answers as exactly three binary digits.

$$011 \times 010$$

$$011 \times 010$$

$$(x^1 + x^0)(x^1)$$

$$x^2 + x$$

$$110$$

$$100 \times 010$$

$$210$$

$$100 \times 010$$

$$x^2 \times x^1$$

$$x^3 \bmod x^3 + x + 1 = 011$$

$$1000$$

$$1011$$

$$\begin{array}{r} x^3 + x + 1 \overline{) x^3 + 0x^2 + 0x + 0} \\ \underline{x^3 + \phantom{0x^2} + x + 1} \\ \phantom{x^3} + \phantom{0x^2} + x + 1 \end{array}$$

$$110 \times 100$$

$$110 \times 100$$

$$(x^2 + x^1)(x^2)$$

$$x^4 + x^3$$

$$11000 \bmod 1011 = 101$$

$$\begin{array}{r} x + 1 \overline{) x^4 + x^3 + 0x^2 + 0x + 0} \\ \underline{-x^4 + \phantom{0x^3} + x^2 + x} \\ \phantom{x^4} + \phantom{0x^3} + x^2 + x \\ \underline{x^3 + x^2 + x} \\ \phantom{x^4} + \phantom{0x^3} + \phantom{x^2} + x + 1 \\ \phantom{x^4} + \phantom{0x^3} + \phantom{x^2} + 1 \end{array}$$

$$011 + 010$$

$$\begin{array}{r} 011 \\ \oplus 010 \\ \hline 001 \end{array}$$

Let's say you are designing a secure communication system that has two AES units in it (ie, it can compute AES or  $\text{AES}^{-1}$  on two blocks at the same time). Also, let's say that security, encryption speed, and decryption speed are all equally important to you. Which mode-of-operation would you select? Explain your answer in one or two sentences.

CTR is the only mode that both secure  
and allows parallel encryption and decryption