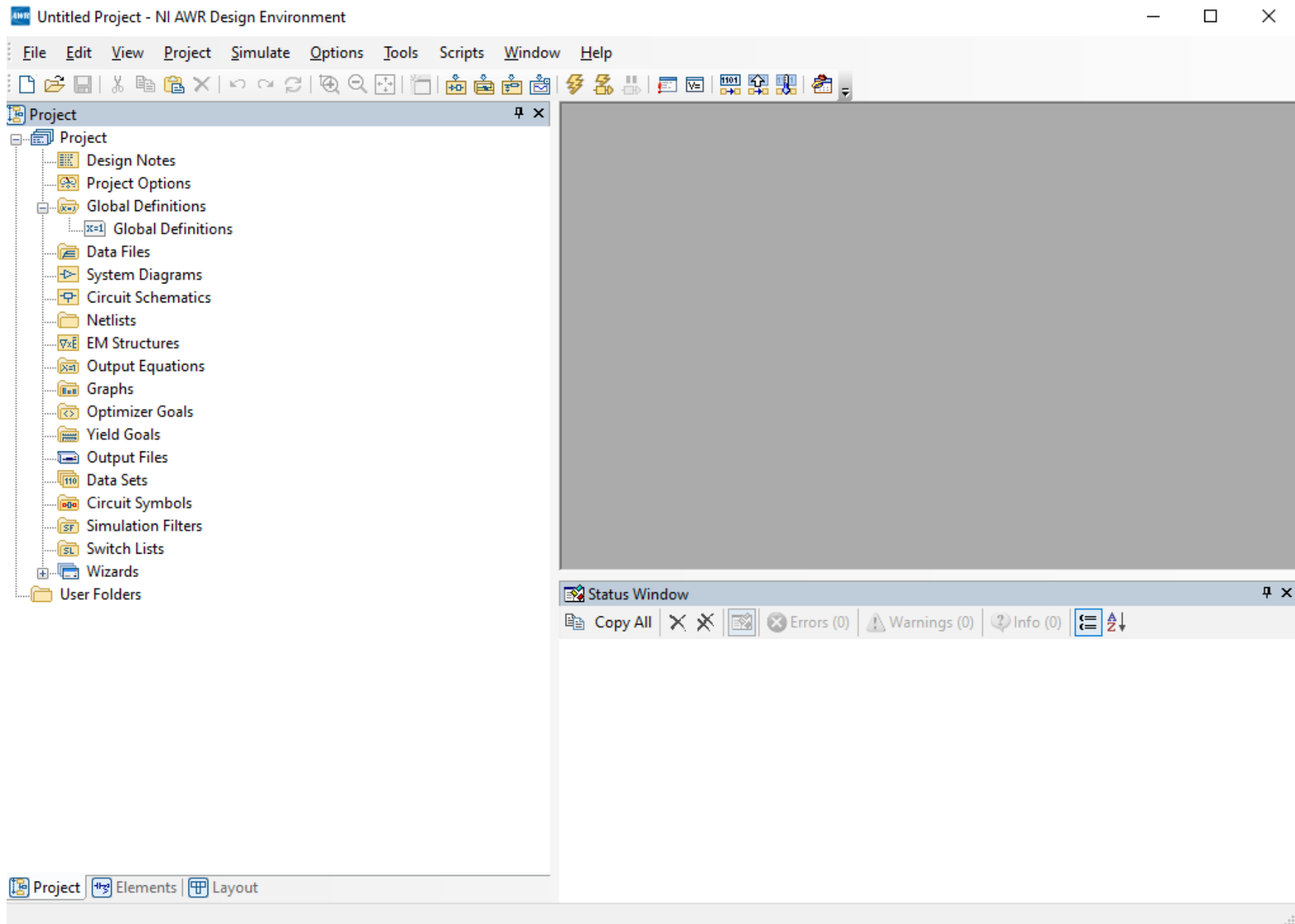


Introdução

NI AWR Design Environment





Project

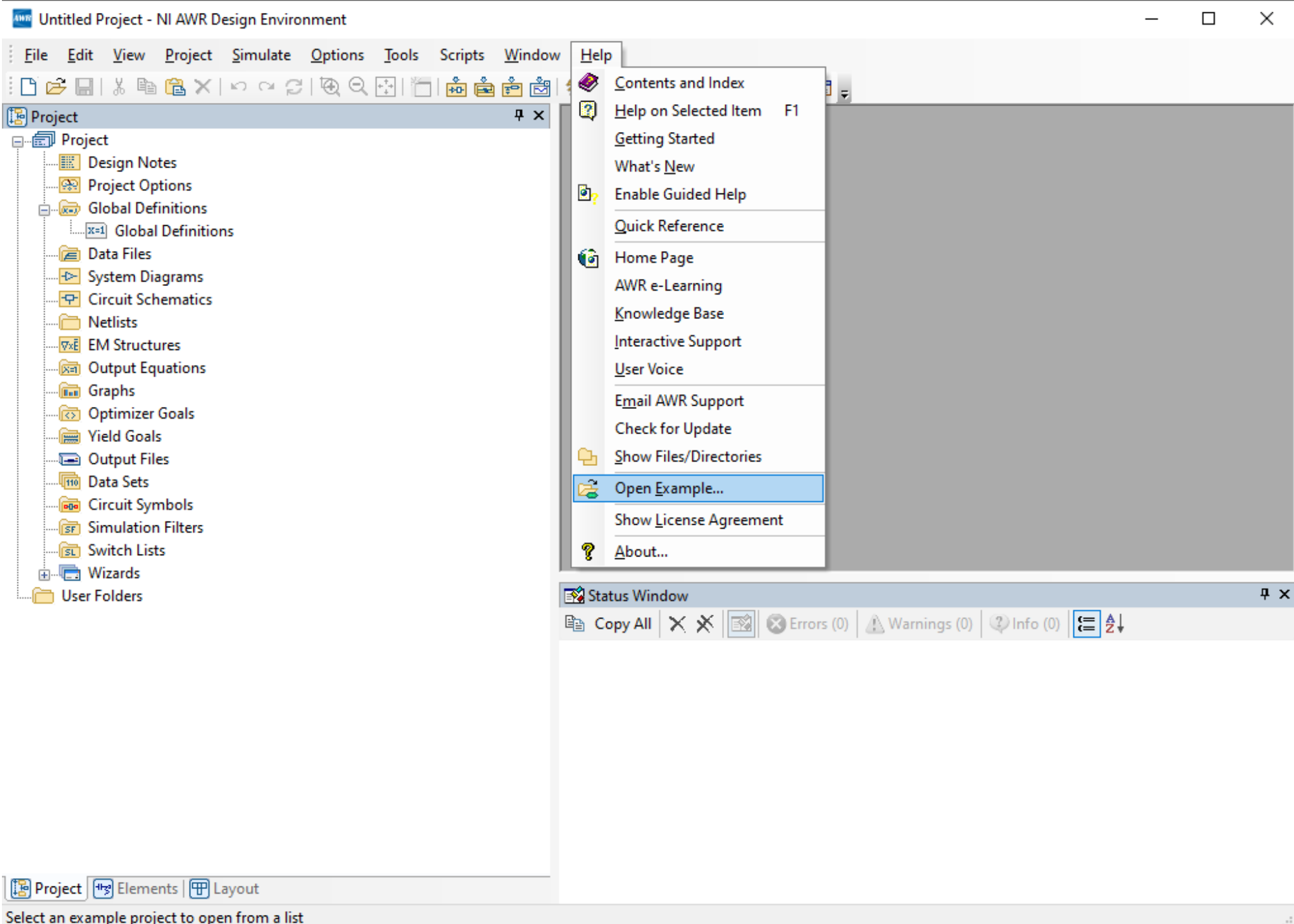
- Project
 - Design Notes
 - Project Options
 - Global Definitions
 - Global Definitions
 - Data Files
 - System Diagrams
 - Circuit Schematics
 - Netlists
 - EM Structures
 - Output Equations
 - Graphs
 - Optimizer Goals
 - Yield Goals
 - Output Files
 - Data Sets
 - Circuit Symbols
 - Simulation Filters
 - Switch Lists
 - Wizards
 - User Folders

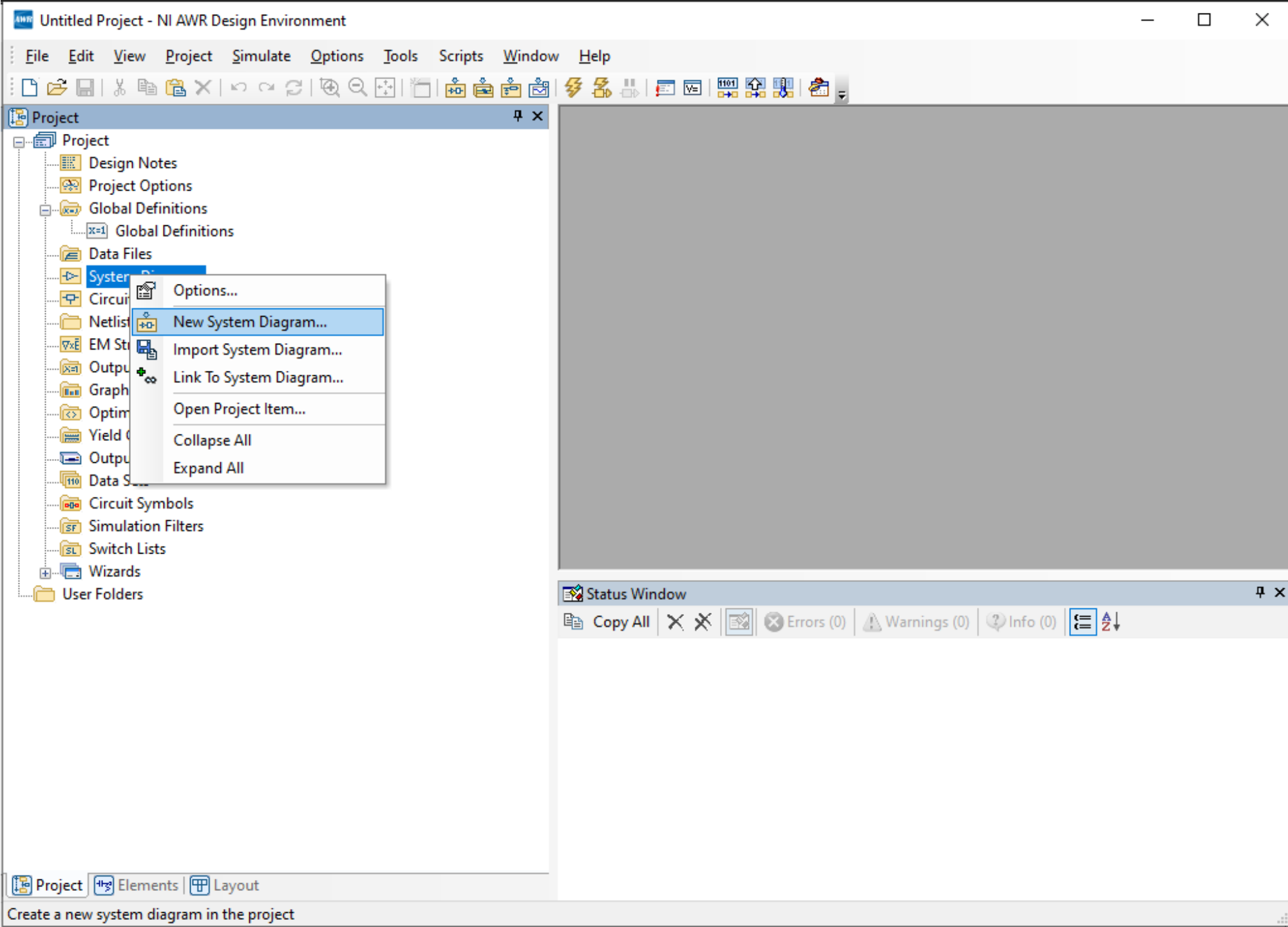
Help

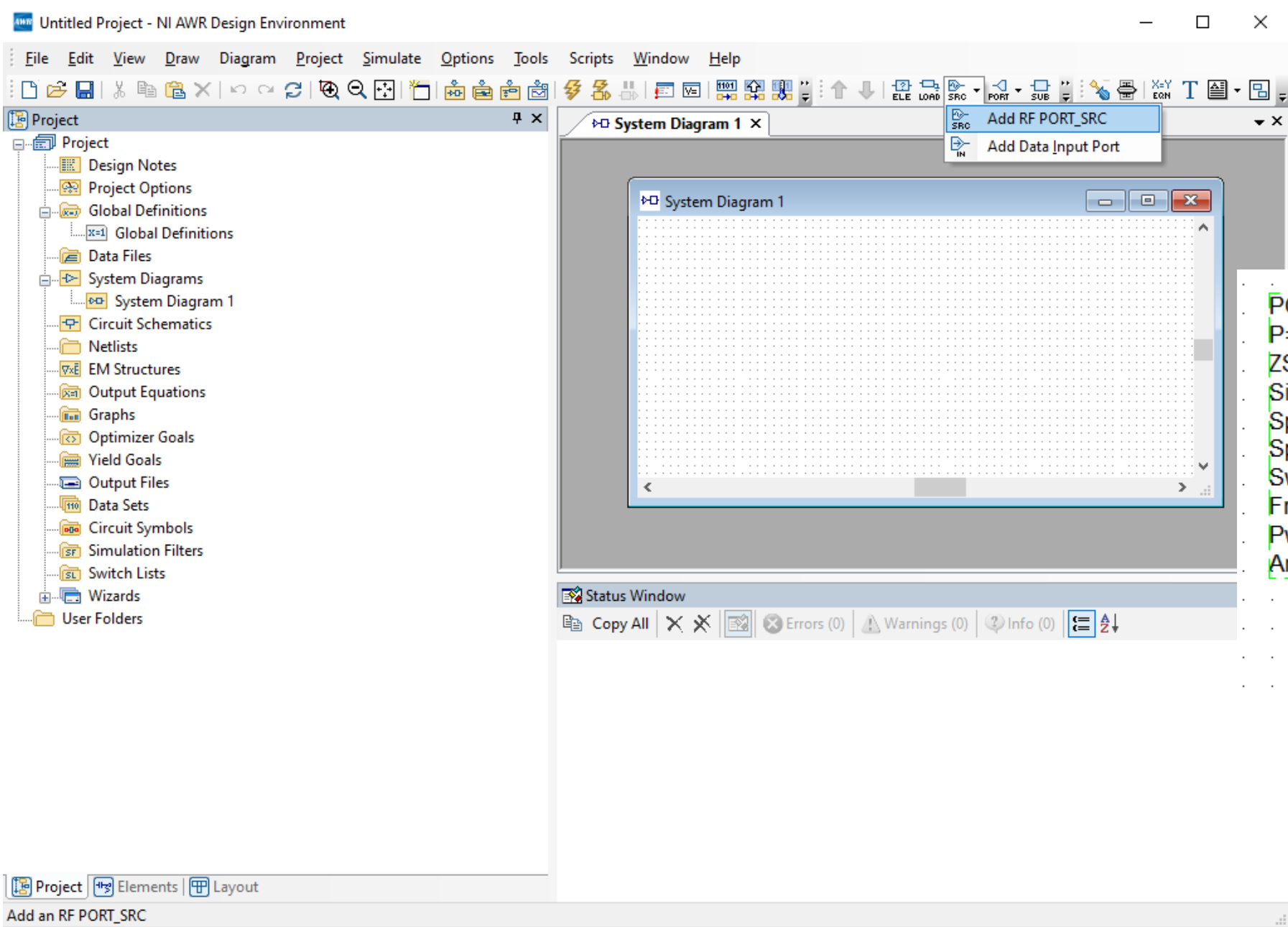
- Contents and Index
- Help on Selected Item F1
- Getting Started
- What's New
- Enable Guided Help
- Quick Reference
- Home Page
- AWR e-Learning
- Knowledge Base
- Interactive Support
- User Voice
- Email AWR Support
- Check for Update
- Show Files/Directories
- Open Example...
- Show License Agreement
- About...

Status Window

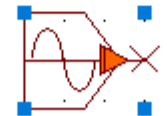
Copy All [X] [X] [X] Errors (0) Warnings (0) Info (0) [Z] [Z]







PORT_SRC
P=1
ZS=_Z0 Ohm
Signal=Sinusoid
SpecType=Specify freq
SpecBW=Use doc freq span
Sweep=None
Freq=1000.MHz
Pwr={0}.dBm
Ang={0}.Deg



Untitled Project - NI AWR Design Environment

FileEditViewDrawDiagramProjectSimulateOptionsToolsScriptsWindowHelp

Project

Project

Design Notes

Project Options

Global Definitions

Global Definitions

Data Files

System Diagrams

System Diagram 1

Circuit Schematics

Netlists

EM Structures

Output Equations

Graphs

Optimizer Goals

Yield Goals

Output Files

Data Sets

Circuit Symbols

Simulation Filters

Switch Lists

Wizards

User Folders

System Diagram 1

Element

System Diagram 1

Add System Block

Type to filter the list. Ctrl+Click the column header to change the column to filter on.

| Name | Description | Path |
|-----------------|---------------------------------|----------------------------------|
| 2ND_ACT | 2nd order active loop filter | PLL\Filters\ |
| 2ND_PASS | 2nd order passive loop filter | PLL\Filters\ |
| 3RD_PASS | 3rd order passive loop filter | PLL\Filters\ |
| 5G_CHANNEL | 5G channel model | Channels\ |
| 802.11b_01_SRC | 802.11b Source for 1Mbps | Libraries\WLAN 802.11b\1 Mbps\ |
| 802.11b_01_TX | 802.11b Transmitter for 1Mbps | Libraries\WLAN 802.11b\1 Mbps\ |
| 802.11b_02_SRC | 802.11b Source for 2Mbps | Libraries\WLAN 802.11b\2 Mbps\ |
| 802.11b_02_TX | 802.11b Transmitter for 2Mbps | Libraries\WLAN 802.11b\2 Mbps\ |
| 802.11b_5.5_SRC | 802.11b Source for 5.5Mbps | Libraries\WLAN 802.11b\5.5 Mbps\ |
| 802.11b_5.5_TX | 802.11b Transmitter for 5.5Mbps | Libraries\WLAN 802.11b\5.5 Mbps\ |
| 802.11b_11_SRC | 802.11b Source for 11Mbps | Libraries\WLAN 802.11b\11 Mbps\ |
| 802.11b_11_TX | 802.11b Transmitter for 11Mbps | Libraries\WLAN 802.11b\11 Mbps\ |
| ABS | Absolute Value | Math Tools\ |
| ABS_FP | Fixed-point Absolute Value | Fixed Point\Math Tools\ |
| ACOS | Arccosine | Math Tools\ |
| ACOSH | Inverse Hyperbolic Cosine | Math Tools\ |

OK

Cancel

Project

Elements

Layout

Untitled Project - NI AWR Design Environment

File Edit View Draw Diagram Project Simulate Options Tools Scripts Window Help

Elements

- Circuit Elements
- System Blocks
 - ADI Analog Devices
 - Channels
 - Coding/Mapping
 - Communication Standards
 - Converters
 - External Applications
 - Filters
 - Fixed Point
 - Interconnects
 - Math Tools
 - Meters
 - Miscellaneous
 - Modulation
 - National Instruments

Models Description

System Diagram 1

System Diagram 1

```
PORT_SRC
P=1
ZS=_Z0 Ohm
Signal=Sinusoid
SpecType=Specify.freq
SpecBW=Use doc freq span
Sweep=None
Freq=1000.MHz
Pwr={0}.dBm
Ang={0}.Deg
```

Status Window

Copy All Errors (0) Warnings (0) Info (0)

Project Elements Layout

The screenshot displays the NI AWR Design Environment interface. The top menu bar includes File, Edit, View, Draw, Diagram, Project, Simulate, Options, Tools, Scripts, Window, and Help. Below the menu is a toolbar with various icons for file operations, simulation, and diagram editing.

The left sidebar contains a tree view of the "Elements" panel, listing various components and blocks such as Coding/Mapping, Communication Standards, Converters, External Applications, Filters, Fixed Point, Interconnects, Math Tools, Meters, Miscellaneous, Modulation, National Instruments, PLL, PRE_RELEASE, Ports, RF Ports, RF Blocks, Signal Processing, Simulation Control, Sources, Testing, _Obsolete, Subcircuits, Libraries, and 3D EM Elements.

The main workspace shows a "System Diagram 1" window. It contains a schematic diagram of a system. The diagram includes a signal source (sinusoid) connected to a transmission line. A test point (TP) is marked on the line. The diagram also shows a load (LOAD) and a port (PORT) connected to the system. The parameters for the components are listed in the diagram's text area:

```

PORT_SRC
P=1
ZS=_Z0 Ohm
Signal=Sinusoid
SpecType=Specify.freq
SpecBW=Use doc.freq span
Sweep=None
Freq=1000.MHz
Pwr={0}.dBm
Ang={0}.Deg
LOAD
ID=S1
Z=_Z0 Ohm
PORT
P=2
Z=_Z0 Ohm

```

The bottom status window shows the "Status Window" with tabs for Copy All, Errors (0), Warnings (0), and Info (0).

Untitled Project - NI AWR Design Environment

File Edit View Draw Diagram Project Simulate Options Tools Scripts Window Help

Project

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 - Global Definitions
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 - System Diagram 1
 - Circuit Schematics
 - Netlists
 - EM Structures
 - Output Equations
 - Graphs
 - New Graph...
 - Edit Measurements...
 - Simulate Open Graphs
 - Open Project Item...
 - Collapse All
 - Expand All
 - Op...
 - View...
 - Output...
 - Data...
 - Circuit...
 - Simulation...
 - Wizard...
- User Folders

System Diagram 1

PORT_SRC
P=1
ZS=_Z0 Ohm
Signal=Sinusoid
SpecType=Specify.freq
SpecBW=Use doc.freq span
Sweep=None
Freq=1000.MHz
Pwr={0}.dBm
Ang={0}.Deg

TP
ID=TP1

LOAD
ID=S1
Z=_Z0 Ohm

PORT
P=2
Z=_Z0 Ohm

Status Window

Copy All Errors (0) Warnings (0) Info (0)

Project Elements Layout

Add a graph to the project

New Graph

Enter a name for the Graph

Graph 1

Select the desired type:

- ☒ Rectangular
- ☐ Smith Chart
- ☐ Polar
- ☐ Histogram
- ☐ Antenna Plot
- ☐ Tabular
- ☐ Constellation
- ☐ 3D Plot

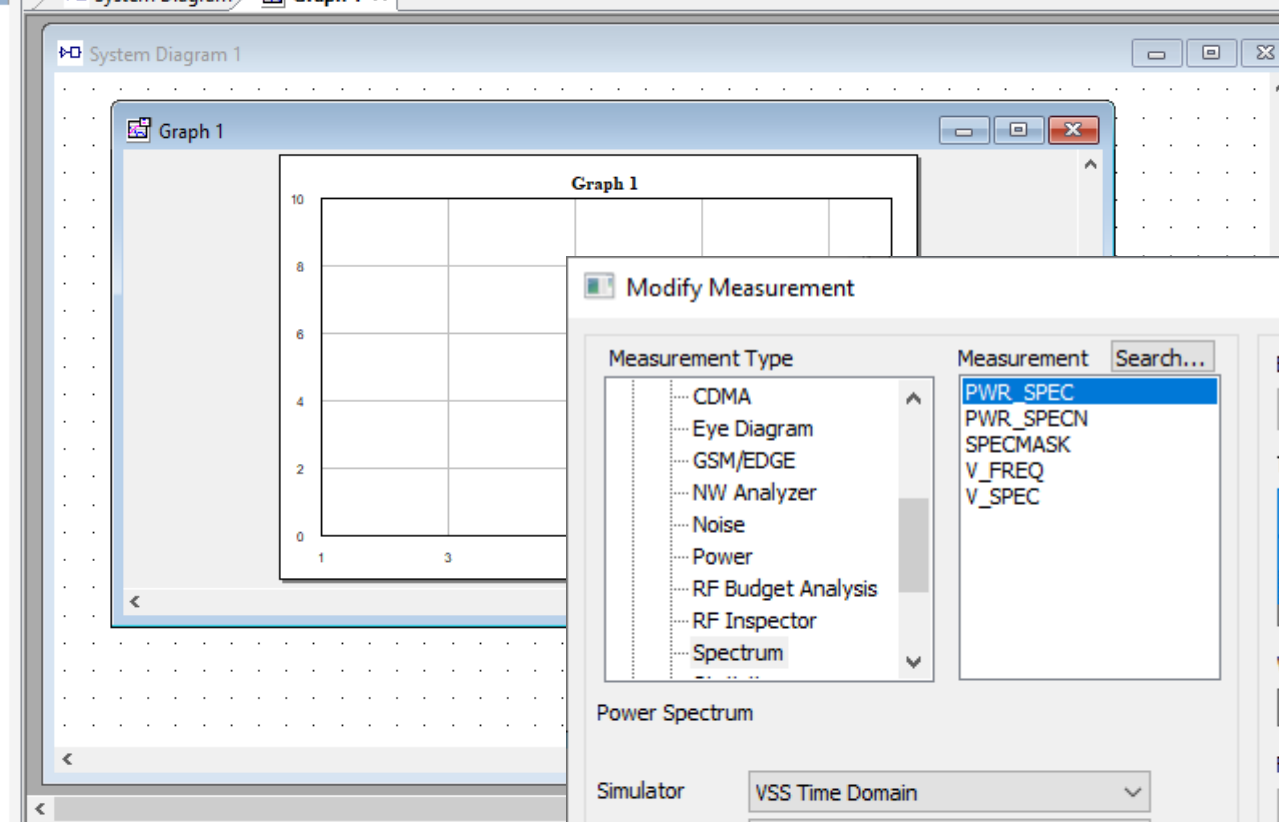
Create Cancel Help



Project

- Project
 - Design Notes
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 - Global Definitions
 - Global Definitions
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 - System Diagram 1
 - Circuit Schematics
 - Netlists
 - EM Structures
 - Output Equations
 - Graphs
 - Graph 1
- Options...
- Add Measurement...
- Simulate for Measurements
- Edit Measurements...
- Rename...
- Duplicate
- Delete
- Duplicate As
- Change Type To
- Open Project Item...
- Collapse All
- Expand All

System Diagram Graph 1



Status Window

Copy All Errors (0) Warnings (0)

Modify Measurement

Measurement Type

- CDMA
- Eye Diagram
- GSM/EDGE
- NW Analyzer
- Noise
- Power
- RF Budget Analysis
- RF Inspector
- Spectrum

Measurement Search...

- PWR_SPEC
- PWR_SPECN
- SPECMASK
- V_FREQ
- V_SPEC

Block Diagram

System Diagram 1

Test Point

TP.TP1

PORT_1

TP.TP1

1000 Auto

VBW/#Avg. Type

10 Auto

Frequency Axis Scaling

Absolute

Frequencies Displayed

Spectrum Analyzer style

Y-Axis Output

Spectrum Analyzer Spectrum

Show Secondary

Power Spectrum

Simulator VSS Time Domain

Configuration Default

Complex Modifier

Real Imag. Mag. Angle AngleU

Complex Conjugate ☒ dBm

Use Vars >>

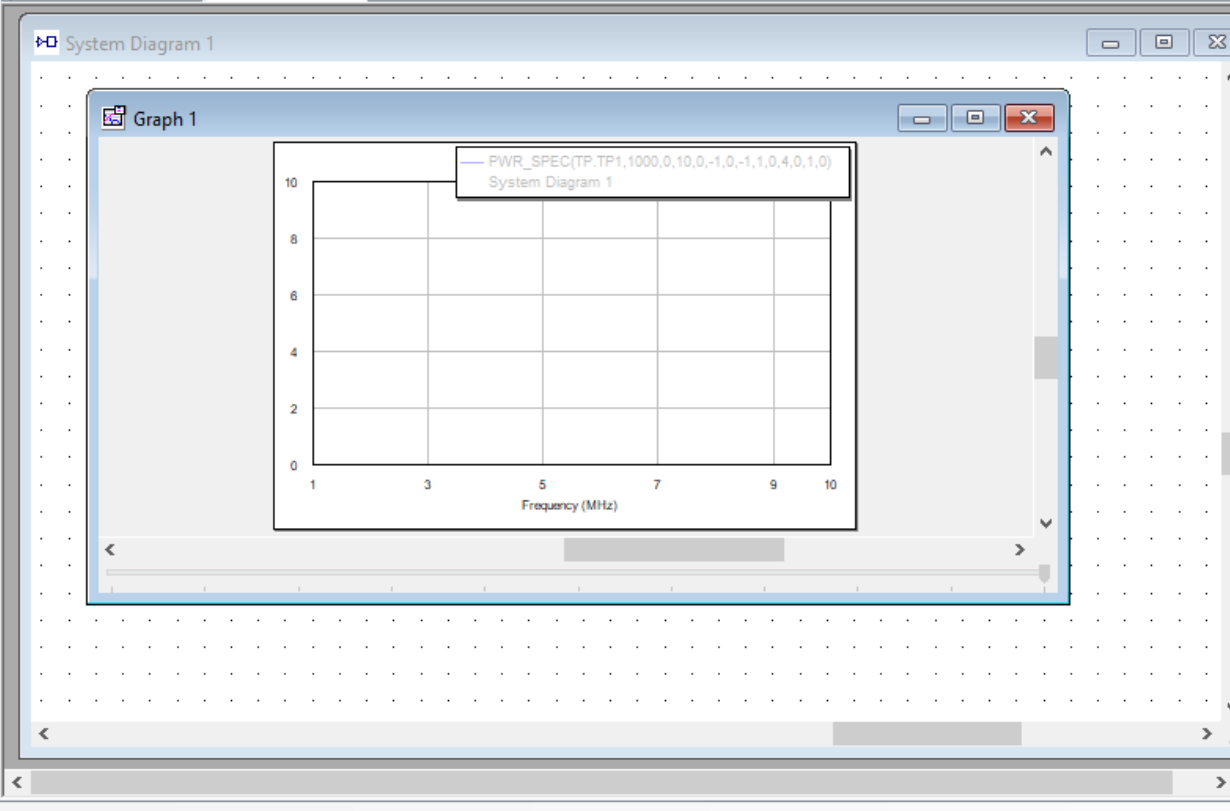
OK Cancel Help Favorite Meas. Help



Project

- Project
 - Design Notes
 - Project Options
 - Global Definitions
 - Global Definitions
 - Data Files
 - System Diagrams
 - System Diagram 1
 - Circuit Schematics
 - Netlists
 - EM Structures
 - Output Equations
 - Graphs
 - Graph 1
 - System Diagram 1: PWR_SPEC(TP.TP1,1000,0,10,0,-1,0,-1,1,0,4,0,1,0)
 - Optimizer Goals
 - Toggle Enable
 - Duplicate
 - Delete
 - Simulate for Measurement
 - View Source Document
 - Add Optimization Goal...
 - Add Yield Goal...
 - Move Up
 - Move Down
 - Properties...
 - Open Project Item...
 - Yield Goals
 - Output Files
 - Data Sets
 - Circuit Symbols
 - Simulation Filters
 - Switch Lists
 - Wizards
 - User Folders

System Diagram 1 Graph 1



Status Window

Copy All X Errors (0) Warnings (0) Info (0)

