

A 6502 Disassembler from Apple

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DESCRIPTION

This subroutine package is used to display single or sequential 6502 instructions in mnemonic form. The subroutines are tailored to disassemblers and debugging aids, but tables with more general usage (assemblers) are included. The subroutines occupy one page (256 bytes) and tables most of another. Seven page zero locations are used.

FEATURES

Four output fields are generated for each disassembled instruction: 1) Address of instruction, in hexadecimal (hex); 2) Hex code listing of instruction, 1 to 3 bytes; 3) 3-character mnemonic, or "???" for invalid ops (which assume a length of 1 byte); and 4) Address field, in one of the following formats.

Format	Address Mode
(empty)	Invalid, Implied, Accumulator
\$12	Page zero
\$1234	Absolute, Branch (<i>target</i> printed)
#\$12	Immediate
\$12.X	Zero page, indexed by X
\$12.Y	Zero page, indexed by Y
\$1234.X	Absolute, indexed by X
\$1234.Y	Absolute, indexed by Y
(\$1234)	Indirect
(\$12.X)	Indexed Indirect
(\$12.Y)	Indirect Indexed

Note that unlike MOS TECHNOLOGY assemblers, which use "A" for accumulator addressing, the APPLE disassembler outputs an empty field to avoid confusion and facilitate byte counting.

USAGE

The following subroutine entries are useful.

DSMBL	Disassembles and displays 20 sequential instructions beginning at the address specified by the page zero variables PCL and PCH. For example, if called with \$D2 in PCL and \$38 in PCH, 20 instructions beginning at address \$38D2 will be disassembled. PCL and PCH are updated to contain the address of the last disassembled instruction. Must be called with 6502 in hexadecimal mode ('D' status bit clear). All processor registers are altered (except S—stack pointer). Uses INSTDSP and PCADJ.
INSTDSP	Disassembles and displays a single instruction whose address is specified by PCL and PCH. Must be called in hexadecimal mode. All processor registers (except S) are altered. Uses PCADJ3, PRPC, PRBLNK, PRBL2, PRNTAX, PRBYTE, and CHAROUT.
PRPC	Outputs a carriage return, 4 hex digits corresponding to PCH and PCL, a dash, and 3 blanks. Alters A, clears X. Uses PRNTAX and CHAROUT.
PRNTX	Outputs the contents of X as two hex digits. Alters A. Uses CHAROUT.
PRNTAX	Outputs two hex digits for the contents of A,

then two hex digits for the contents of X. A is altered. Uses CHAROUT.

PRNTYX	Same as PRNTAX except that Y and X are output. Alters A. Uses CHAROUT.
PRBLNK	Outputs 3 blanks. Alters A, clears X. Uses CHAROUT.
PRBL2	Outputs the number of blanks specified by the contents of X (0 for 256 blanks). Alters A, clears X. Uses CHAROUT.
PRBL3	Outputs a character from the A register followed by X-1 blanks. In other words, X specifies the total number of characters output. (0 for 256 blanks). Alters A, clears X. Uses CHAROUT.
PCADJ	(PCL,PCH) + 1 + (contents of page zero variable LENGTH) → Y & A (low order byte in Y). For example, if PCL = \$D2, PCH = \$38, and LENGTH = 1 (corresponding to a 2 byte instruction), PCADJ will leave Y = \$D4 and A = \$38. X is always loaded with PCH.
PCADJ2	Same as PCADJ except that A is used in place of LENGTH.
PCADJ3	Same as PCADJ2 except that the increment (+1) is specified by the carry (set = +1, clear = +0).

RUNNING AS A PROGRAM

The following program will run a disassembly.

Supplied on APPLE-1 { 9F0 200 8 JSR DSMBL
cassette tapes. { 9F3 4C1FFF JMP MONITOR

First, put the starting address of code you want disassembled in PCL (low order byte) and PCH (high order byte). Then type 9F0 R CR (on APPLE-1 system). 20 instructions will be disassembled. Hitting R CR again will give the next 20, etc.

Cassette tapes supplied for the ACI-1 (APPLE Cassette Interface) are intended to be loaded from \$800 to \$9FF.

NON-APPLE SYSTEMS

Source and object code supplied occupies pages 8 and 9. All code is on page 8, tables are on page 9. These tables may be relocated at will: MODE, MODE2, CHAR1, CHAR2, MNEML, and MNEMR. The code may also be relocated. Be careful if you use pages 0 or 1. Page 1 is the subroutine return stack and page 0 must contain 7 variables (to use DSMBL). These may be relocated on page 0 but PCL must always immediately precede PCH for (Z-page), Y addressing.

	\$40	FORMAT	Used
locations	\$41	LENGTH	} by
used	\$42	LMNEM	} INSTDSP,
by	{ \$43	RMNEM	DSMBL
supplied	\$44	PCL	} Used by PCADJ,
code	\$45	PCH	} INSTDSP, DSMBL
	\$46	COUNT	} Used by DSMBL only

MODIFICATIONS

- To change '#' to '=' for immediate mode change location \$955 (on code enclosed) from a \$A3 to a \$BD.
- To skip the '\$' (meaning hex) preceding disassembled values make the following changes:

Dr. Dobb's Journal of Computer Calisthenics & Orthodontia, Box 310, Menlo Park, CA 94025

```
* OF CODE.
MASK IT FOR 1XXX1010 TEST.
* SAVE IT.
```

September, 1976

- * Z=0, LEFT HALF-BYTE
- * Z=1, RIGHT HALF-BYTE

MHEML

XXXXX000 .INSTRS.

MEME

18881010 INSTRS.

XXXYYY'10 INSTRS.

8887701 INSTRS.

00000000 INSTRS

X227100 INSTRS

LOSS REFERENCE TABLE

CHAR1	0950	0230
CHAR2	0956	0238
CHAR3	0962	0246
CHAR4	0968	0254
CHAR5	0974	0262
CHAR6	0980	0270
CHAR7	0986	0278
CHAR8	0992	0286
CHAR9	0998	0294
CHAR10	1004	0302
CHAR11	1010	0310
CHAR12	1016	0318
CHAR13	1022	0326
CHAR14	1028	0334
CHAR15	1034	0342
CHAR16	1040	0350
CHAR17	1046	0358
CHAR18	1052	0366
CHAR19	1058	0374
CHAR20	1064	0382
CHAR21	1070	0390
CHAR22	1076	0398
CHAR23	1082	0406
CHAR24	1088	0414
CHAR25	1094	0422
CHAR26	1100	0430
CHAR27	1106	0438
CHAR28	1112	0446
CHAR29	1118	0454
CHAR30	1124	0462
CHAR31	1130	0470
CHAR32	1136	0478
CHAR33	1142	0486
CHAR34	1148	0494
CHAR35	1154	0502
CHAR36	1160	0510
CHAR37	1166	0518
CHAR38	1172	0526
CHAR39	1178	0534
CHAR40	1184	0542
CHAR41	1190	0550
CHAR42	1196	0558
CHAR43	1202	0566
CHAR44	1208	0574
CHAR45	1214	0582
CHAR46	1220	0590
CHAR47	1226	0598
CHAR48	1232	0606
CHAR49	1238	0614
CHAR50	1244	0622
CHAR51	1250	0630
CHAR52	1256	0638
CHAR53	1262	0646
CHAR54	1268	0654
CHAR55	1274	0662
CHAR56	1280	0670
CHAR57	1286	0678
CHAR58	1292	0686
CHAR59	1298	0694
CHAR60	1304	0702
CHAR61	1310	0710
CHAR62	1316	0718
CHAR63	1322	0726
CHAR64	1328	0734
CHAR65	1334	0742
CHAR66	1340	0750
CHAR67	1346	0758
CHAR68	1352	0766
CHAR69	1358	0774
CHAR70	1364	0782
CHAR71	1370	0790
CHAR72	1376	0798
CHAR73	1382	0806
CHAR74	1388	0814
CHAR75	1394	0822
CHAR76	1400	0830
CHAR77	1406	0838
CHAR78	1412	0846
CHAR79	1418	0854
CHAR80	1424	0862
CHAR81	1430	0870
CHAR82	1436	0878
CHAR83	1442	0886
CHAR84	1448	0894
CHAR85	1454	0902
CHAR86	1460	0910
CHAR87	1466	0918
CHAR88	1472	0926
CHAR89	1478	0934
CHAR90	1484	0942
CHAR91	1490	0950
CHAR92	1496	0958
CHAR93	1502	0966
CHAR94	1508	0974
CHAR95	1514	0982
CHAR96	1520	0990
CHAR97	1526	0998
CHAR98	1532	1006
CHAR99	1538	1014
CHAR100	1544	1022

DfB	174
DfB	172
DfB	144
DfB	168
DfB	162
DfB	132
DfB	162
DfB	10
DfB	122
DfB	10
DfB	118
DfB	118
DfB	126
DfB	10
DfB	172
DfB	172
DfB	128
DfB	118
DfB	114
DfB	114
DfB	126
DfB	148
DfB	144
DfB	144
DfB	144
DfB	142
DfB	138

46 SYMBOLS DEFINED

0108					
0110					
0090	0109	0112	0126	0131	0134
0013	0018				
0019					
0026	0028				
0045	0099	0106			
0040					
0024					
0014					
0047	0070	0097	0138		
0079	0085				
0078					
0080					
0064					
0061					
0054	0056				
0033					
0044					
0015					
0116					
0142					
0017	0127	0140			
0016	0128	0144	0021	0066	0101
0114					
0105					
0096	0098				
0107	0111				
0069	0136				
0093					
0067	0103	0122	0124		
0092					
0088					
0129					
0119					
0072					
0075					
0020					
0102					
0081	0084				
0034					
0145					