Lecture 13

Bitwise operators

- · These operators work on individual bits of operands.
- They work at binary levels and used in the scenarios where we need to manipulate data efficiently such as in system programming, explography and low-level operations.
- · Java is not typically used for system programming, but languages like C & assembly heavily rely on low-level operations.
- · Mostly used in embedded systems applications which work in resource constrained environments. As these things have limited memor and data has to be manipulated very efficiently and bit wise operators play an important sole here.
- There are following bitwise operators in Jara! -
 - (Bitwise AND (&)
 - @ Pitwise OR (1)
 - 3 Bitwise NOT (~)
 - @ Bitwhe XOR (N) Cerclusive OR)
 - Bit Shift Operators

 (in Left Shift (<<))

 in Rykt Shift (>>)

 in Onsigned Right Shift (>>>)

Bitwise AND (&): - it returns 1 if both input bits one 1.

$$Q' = Q = 10 & b = 5$$
 $Q = 0$
 $Q = 0$

@ Bitwise OR (1) '- it returns I if any of the input

3 Bitwise xoR(N):- it returns 1 if both bits are different, otherwise

it returns 1 if any of the input bit is 1, but not both.

$$e:g: \alpha=10, b=5$$

We can apply these bitwise operators (&, 1, 1) on integers & Char values not on floating point numbers. There operators (&, 1, 1) can also be applied on boolean values.

boolean b2 = false, bl&b2 - fake b1 | b2 - true

But the operators de, 1, ^ are referred to as bitwise only when they are applied on integer operands

There is no short-circuiting

in Bitwee & and 1

these are non-short-circuit operators

e9 !-

int a = 10, b = 20, C=30, d=40, if ((9<b) 1 (++c <d)) {

System out pointln (" Both conditions are torre");

System.out. pointln ("c="+c); -> 31

Here even if (a<b) is true but offl second condition would be evaluated & that's why c value becomes 31

(4) Bitwise NOT (~):- It inverts bits, 0 becomes 1 & 1 becomes 0

int a = log ~ = -11

0000000 0000000 00001010 (9) 11111111 1111111 (tilolo) (~14)

Ly this is -11 in 2's complement

NOTE-Bitwise Not (~) can not be applied to boolean values.

Compound Bitwise Hosignment: -

Combining bitwise operations with assignment.

operand 1 = operand 1 operator operand2 operand 1 operator = operand 2

a = 10, b=5;

Canbewriter as a &= b -> this performs Bitwise AND on a & b, then assigns the result to a.

Same we can write!

Practice Time: -

Bit Shift operation :- Shifts the bits.

Left shift; the bits to the left by a specified number of positions, given on right.

eg: - int a = 5; a << 1

00001010 -> [10] 00000000 ∞ 5<1 00000000

- This operation inserts as from the oight and discards any bits that more out of the left side

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Here a=5
                   a \ll 1 = \frac{10}{2} (SX2^{\prime})
      3 So the answer is same as multiplication by powers of two (assuming
                                                            overflow
                   S<= = SX2 = SX4 = 20
                                                            occus)
                   5 << 3 = 5 \times 2^3 = 40
 IMP
   int result = a << 31 = ? = -2147483648 [because of overflow]
 Suppose: - inta=1;
           long a = IL;
           long a = 11;
long result = a << 31; > 2147483648 [No overflow] here.
Unsigned Right Shift! - shifts the bits to the right, by the specified
     number of positions.
   . Right shift the left operand by the specified number of positions
     given on light.
         e-g1 - int a = lo ;
                    Q>>>1
                                       00002020
  00000101 => 5
                  00000000
10>>>1 => 00000000
     . Inserts zeros at leftmost bils (higher-order bils) & ignores the
        syn bit.
      . This is useful for unsigned operations
      . same as division by powers of 2
                     10>>>1 = 10/21 = 5
                      10>>>2 = 10/22 = 2
```

e.g!int a = -10;
int result = 9 >>> 2;

System, out.pnntln (result); \Rightarrow 1073741821

Signed Right-Shift Operators (>>):>

Ly shifts bits to the right while preserving the sign of the original value. It means that if the value is negative, the sign bit (MSB) is filled with 1s and if the value is positive it is filled with 0s.

e.g:- int a = 10;] . (conser's tre)

 $-10 \Rightarrow 11111111 | 11111111 | 11111111 | 111110110$ $-10 \Rightarrow 11111111 | 11111111 | 11111011 | 11111011 | 3 -5$ inserted 1.

2) We can say that >> is same as >>> but padded with MSB

Compound Bit Shift Operators!
Combining bit shift operators with arrighment.

eg! - int $q = lo_{g'}$ Interest = $a \ll = l_{g'}$ and $a = lo_{g'}$ int $a = lo_{g'}$ $a \gg = l_{g'}$ some as $a = a \gg l \Rightarrow s$ int $a = lo_{g'}$ $a \gg = l_{g'}$ $a \gg = l_{g'}$

Applications:

- 1 Embedded Systems | Embedded Programming
- D Games Programming
- Performance optimization (Faster Computations)

 Linstead of using * or /, shifting bits left or night
 can speed up the calculation.
- (I) Network Programming (IP Address Manipulation)
 La used to manipulate & compare IP addresses & subnet masks
- © Cryptography Ly Encyption algorithms (AES, DES) use bitwise operations
- @ Graphius Programming (Color Manipulation)
- 1) Huffman Encoding Data Compression Algorithms

-> Bituise operators have lower precedence than Arithmetic operators.

Operator Precedence & Associativity Chart:

1-41.	the ce injus		
Precedence	Opentos	Type	Associativity
1	[]	Parantheses Array Subscript Member Selection	Left-to-Right
2.	++	Unary Post increment. Unary Pre increment.	L-R (Left-to-Right)
3	++ + -! ~ (type)	Unary Pre-increment Unary Pre-decrement Unary plus Unary minus Logical Not Bitwise NOT Unary type Cest	R-L (Right-to-left)
4.	*/*	Multiplication Division Modulus	L-R
5.	+	Addition Subtraction	L-R
6.	<< >>> >>>	Bitwise Left Shift Signed Right Shift Unsigned Right Shift	L-R
7.	<= >> >= înstanceo	Relational less than less than equal to greater than greater than equal to Type Comperison (abjects only)	L-R

8.	==	Relational is equal to is not equal to	L-R
9	&	Bitwhe AND	L-R
[0	Λ	Bitwuse XOR	L-R.
11		Bitwise OR	L-R
12	. & &	Logical AND	L-R
13.	11	Logical OR	L-R
14	?:	ternary operator	Right-to-Left .
15.		Assignment Adolition Assignment Subtraction Assignment Multiplication Assignment Division Assignment Mudulus Assignment	Right-to-Left

NOTE: - Smaller number means higher precedence.