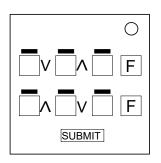
On the Subject of Logic

Logic is easy, but logic AND bomb defusal might not.

If a red light is above the letter, True becomes False, and vice versa.

Letter	Statement	Letter	Statement
A	Batt = Ind	N	> 2 Batt Hol
В	Letters > Num in SN	0	Lit and unlit ind
C	IND ind	P	Parallel port
D	FRK ind	Q	=2 ports
E	=1 unlit ind	R	PS/2 port
F	> 1 port type	S	Sum of SN # > 10
G	> 1 batt	Т	MSA ind
Н	< 2 batt	U	=1 batt hol
I	Odd SN	V	Vowel in SN
J	> 4 batt	W	0 ind
K	=1 lit ind	Х	<u>=l</u> ind
L	> 2 ind	Y	> 5 ports
M	Ports = unique	Z	<2 ports



Gate	Statement	Gate	Statement
v (VD)	$T \wedge T = T$, $T \wedge F = F$, $F \wedge T = F$, $F \wedge F = F$	↓ (NOR)	$T_{\downarrow}T=F$, $T_{\downarrow}F=F$, $F_{\downarrow}T=F$, $F_{\downarrow}F=T$
v (OR)	TVT=T, TVF=T, FVT=T, FVF=F	↔ (XNOR)	$T_{\leftrightarrow}T=T$, $T_{\leftrightarrow}F=F$, $F_{\leftrightarrow}T=F$, $F_{\leftrightarrow}F=T$
⊻ (XOR)	$T \lor T = F$, $T \lor F = T$, $F \lor T = T$, $F \lor F = F$	\rightarrow	$T \rightarrow T = T$, $T \rightarrow F = F$, $F \rightarrow T = T$, $F \rightarrow F = T$
I (NAND)	TIT=F, TIF=T, FIT=T, FIF=T	←	$T \leftarrow T = T$, $T \leftarrow F = T$, $F \leftarrow T = F$, $F \leftarrow F = T$