DBBC3 Control Software Commands DSC mode

Version 110 – 28.02.2018

dbbcif(a|b|c|d|e|f|g|h)=[input[,attenuation[,filter[,target_agc]]]]

Sets configuration for (G)CoMo-IF modules. Parameters:

synth_oen: IF input 1 (no downconversion) (1) or IF input 2 (with downconversion) (2)
selecting input 1 will disable the Synthesizer tone for the downconversion LO
attenuation: *agc* for automatic gain control
             man for manual gain control, retaining last agc value
             value between and 0 (0 dB) and 63 (31.5 dB) in stepsize of 0.5 dB
filter: no function for DBBC3, deprecated
target_agc: target power level for agc mode

core3hstats=board

Gives information about DSC total power, bit statistics and phase correlation values for specified board. Calibration loop must be active (enableloop) to get the values.

time

Prints out time stamps from all Core3H to check for correct time synchronization

checkphase

Checks the delay phase calibration of each ADB3L sampler board.

exit

Exit client program

reconfigure

Reconfigures Core3H, than reinitializes ADB3L and Core3H and does a PPS sync.

cal_offset=board

Calibrates the offset for selected ADB3L. The CoMo connected to this ADB3L should be set to maximum attenuation to get optimal calibration.

cal_gain=board

Calibrates the gain for selected ADB3L. The CoMo connected to this ADB3L should be set to default attenuation to get optimal calibration.

cal_delay=board

Calibrates the delay for selected ADB3L. The CoMo connected to this ADB3L should be set to low attenuation to get optimal calibration.

enablecal=on|off,on|off,on|off

Sets automatic calibration loop to calibrate threshold, gain, and offset. The loop must be activated with the command enableloop.

enableloop

starts automatic calibration loop

disableloop

stops automatic calibration loop

adb3l=command

Sends specified command to adb3l, selection of ADB3L is included in the command

core3h=board,command

sends specified command to selected Core3H

synth=board,command

sends specified command to selected Gcomo Synthesizer (1-4)

adb3linit

reinitializes all ADB3L from ADB3L config file

core3hinit=[board]

Reinitializes selected Core3H (starting with 1) from Core3H config files or all Core3H if board parameter not specified.

synthinit

Initializes Valon synthesizers if GCoMo is present, using values from the config file.