

UCC28740 DESIGN CALCULATIONS

The Values Entered by the User on the DESIGN INPUT Page are Used in the Design Calculations

INPUT			
Input Voltage Type	AC or DC:	AC	User Input Values From Design Input Page
Minimum Input Voltage	$V_{\text{INPUTmin}} =$	85VAC	
Maximum Input Voltage	$V_{\text{INPUTmax}} =$	265VAC	
Nominal Input Voltage	$V_{\text{INPUTnom}} =$	220VAC	
Minimum Line Frequency	$f_{\text{LINEmin}} =$	47Hz	
Minimum Input Voltage for Start-Up	$V_{\text{INPUTrun}} =$	80VAC	
Minimum Peak Bulk Input Voltage	$V_{\text{BULKmin}} =$	120.208V	
Maximum Peak Bulk Input Voltage	$V_{\text{BULKmax}} =$	374.767V	
Nominal Peak Bulk Input Voltage	$V_{\text{BULKnom}} =$	311.127V	
Turn-On Peak Bulk Input Voltage	$V_{\text{BULKstartup}} =$	113.137V	
Line Cycle Period	$t_{\text{LINE}} =$	21.277ms	
OUTPUT			
Regulated Output Voltage, Constant Voltage Mode	$V_{\text{OUT_CV}} =$	80V	User Input Values From Design Input Page
Full Load Rated Output Current	$I_{\text{OUT}} =$	0.6A	
Target Constant Current Mode Output Load Threshold	$I_{\text{OCC_target}} =$	0.7A	
Target Minimum Output Voltage During Constant Current Regulation	$V_{\text{OUT_CC}} =$	79V	
Allowable Output Voltage Drop During Load-Step Transient in Constant Voltage Mode	$V_{\text{OUTA}} =$	1V	
Maximum Peak to Peak Output Voltage Ripple	$V_{\text{RIPPLE}} =$	30mV	
Required Positive Load Step Transient Current	$I_{\text{TRAN}} =$	0.7A	
Maximum Allowable Response Time to Load Step Transient	$t_{\text{RESP}} =$	20ms	
Output Over Voltage Protection	$V_{\text{OUT_OVP}} =$	83V	
Maximum Stand By Power Dissipation	$P_{\text{SBtarget}} =$	50mW	
Estimated Efficiency	$\eta =$	0.850	
Output Power	$P_{\text{OUT}} =$	56.000W	
Estimated Input Power	$P_{\text{IN}} =$	65.882W	

COMPONENT PARAMETER CALCULATIONS

INPUT CAPACITOR, C_{BULK}			
Recommended Input Bulk Capacitance	$C_{\text{BULKrecommended}} =$	256.01 μF	
Actual Input Bulk Capacitance	$C_{\text{BULKactual}} =$	250.000 μF	User Input
Input Capacitor Value Used in Calculations	$C_{\text{BULK}} =$	250.000 μF	
Minimum Valley Voltage on Input Bulk Capacitors	$V_{\text{BULKvalley}} =$	99.527V	
Minimum Input Capacitor Ripple Current Rating	$I_{\text{CIRipple}} =$	1480.649mA	
Minimum Input Capacitor Voltage Rating	$V_{\text{Cin}} =$	400V	

INPUT FUSE			
Voltage Rating	$V_{\text{FUSE}} =$	265VAC	
Peak Input Current	$I_{\text{Npeak}} =$	4.301A	

BRIDGE RECTIFIER			
Voltage Rating	$V_{\text{BRIDGE_minrating}} =$	400.000V	
Current Rating	$I_{\text{BRIDGE_minrating}} =$	1.324A	
Forward Voltage Drop	$V_{\text{F_BRIDGE}} =$	1.100V	User Input
Full Load Power Dissipation of Bridge Rectifier	$P_{\text{BRIDGE}} =$	3257.428mW	

TRANSFORMER TURNS-RATIO, N_{PS}			
Demagnetizing Duty Cycle	$D_{\text{DEMAG_CC}} =$	0.425	Device Parameter
Amplitude Modulation Control Ratio	$K_{\text{AMnom}} =$	4	Device Parameter
Maximum Desired Switching Frequency	$f_{\text{max_target}} =$	100.000kHz	User Input
Desired Switching Period	$t_{\text{SW_target}} =$	10.000 μs	
Resonant Frequency During DCM Dead Time	$f_{\text{RES}} =$	0.500MHz	
Time to First Resonant Valley	$t_{\text{RES}} =$	1.000 μs	
Estimated Maximum Duty Cycle	$D_{\text{max_target}} =$	0.475	

Ideal Primary to Secondary Turns Ratio	$N_{PSideal} =$	1.3669	Ideal N_{PS}
Actual Primary to Secondary Turns Ratio	$N_{PSactual} =$	1.367	User Input
Primary to Secondary Turns Ratio Used in Calculations	$N_{PS} =$	1.367	
Actual Flyback Voltage	$V_{FLYBACK} =$	111.246V	
Allowable Leakage Inductance Voltage Spike	$V_{LEAKAGE} =$	313.987V	
Estimated Maximum On-Time	$t_{ONestimated} =$	4.750 μ s	
Estimated Transformer Efficiency	$\eta_{XFMR} =$	0.9	

CURRENT SENSE RESISTOR, R_{CS} , PEAK PRIMARY CURRENT, I_{PP}			
Constant Current Regulation Factor, Minimum	$V_{CCR_min} =$	318mV	Device Parameter
Constant Current Regulation Factor, Nominal	$V_{CCR_nom} =$	330mV	Device Parameter
Constant Current Regulation Factor, Maximum	$V_{CCR_max} =$	343mV	Device Parameter
Initial estimate for L_p	$L_{P_estimate} =$	218.003 μ H	
Recommended Current Sense Resistor Value	$R_{CSrecommended} =$	0.301 Ω	
Actual Current Sense Resistor Used	$R_{CSactual} =$	0.300 Ω	User Input
Current Sense Resistor Value Used in Calculation	$R_{CS} =$	0.300 Ω	
Power Dissipation of R_{CS}	$P_{Rcs} =$	315.393mW	
Maximum Current Sense Threshold Voltage, Minimum	$V_{CSTmax_min} =$	738mV	Device Parameter
Maximum Current Sense Threshold Voltage, Nominal	$V_{CSTmax_nom} =$	773mV	Device Parameter
Maximum Current Sense Threshold Voltage, Maximum	$V_{CSTmax_max} =$	810mV	Device Parameter
Peak Primary Current, Minimum, Full Load	$I_{PPmin} =$	2.460A	
Peak Primary Current, Nominal, Full Load	$I_{PPnom} =$	2.577A	
Peak Primary Current, Maximum, Full Load	$I_{PPmax} =$	2.700A	
Actual Output Current During Constant Current Mode	$I_{OCC_actual} =$	0.748A	
Peak Primary Current During Light Load, FM Mode	$I_{PP_FM} =$	0.644A	
Worst Case Peak Primary Current	$I_{PP_WC} =$	2.727A	Assumes -1% R_{CS} and V_{CSTmax_max}
Maximum Output Current During Constant Current Mode	$I_{OCCmax} =$	0.792A	Worst Case Estimate

TRANSFORMER PRIMARY INDUCTANCE, L_p			
Calculated L_p to meet f_{max_target} with chosen R_{CS}	$L_{Pcalc} =$	203.880 μ H	
Recommended Primary Inductance to meet t_{CSLEB} with chosen R_{CS}	$L_{Precommended} =$	203.880 μ H	Ideal L_p
Actual Primary Inductance	$L_{Pactual} =$	203.880 μ H	User Input
Primary Inductance Used in Calculations	$L_p =$	203.880 μ H	
Actual Maximum Nominal Switching Frequency	$f_{max} =$	90.000kHz	
Actual Switching Period	$t_{SWactual} =$	11.111 μ s	
Actual Maximum On-Time	$t_{ONmax} =$	5.278 μ s	
Maximum Duty Cycle	$D_{MAX} =$	0.475	
Demagnetization Time	$t_{DEMAG} =$	4.722 μ s	
Primary RMS Current	$I_{PRI_RMS} =$	1.025A	
Secondary Peak Current	$I_{SPmax} =$	3.522A	
Secondary RMS Current	$I_{SEC_RMS} =$	1.326A	
VDD Under Voltage Lock Out (UVLO) Voltage, Maximum	$VDD_{OFF_max} =$	8.150V	Device Parameter
VDD Under Voltage Lock Out (UVLO) Voltage, Minimum	$VDD_{OFF_min} =$	7.350V	Device Parameter
Recommended Auxiliary to Secondary Turns Ratio	$N_{ASrecommended} =$	0.117	
Recommended Primary to Auxiliary Turns Ratio	$N_{PArecommended} =$	11.689	
Actual Primary to Auxiliary Turns Ratio	$N_{PAactual} =$	10.800	User Input
Primary to Auxiliary Turns Ratio Used in Calculations	$N_{PA} =$	10.800	
Nominal VDD Voltage	$VDD =$	9.051V	
Actual Auxiliary to Secondary Turns Ratio	$N_{AS} =$	0.127	
Minimum On-Time, t_{CSLEB}	$t_{ONmin(limit)} =$	280.000ns	
Actual Minimum On-Time	$t_{ONmin(actual)} =$	350.439ns	
Minimum Demagnetizing Time	$t_{DEMAGmin} =$	1.181 μ s	
Minimum Output Voltage During Constant Current Mode	$V_{OUT_CCmin} =$	66.564V	

MOSFET, Q			
Required Drain to Source Voltage Rating, $V_{DSrated} =$	$V_{DSmin_rating} =$	631.817V	
MOSFET Rated Drain to Source Voltage	$V_{DS} =$	800.000V	User Input Values From Design Input
Output Capacitance of Selected MOSFET	$C_{OSS} =$	16pF	
Drain to Source On-Resistance of Selected MOSFET	$R_{DSon} =$	0.310 Ω	

MOSFET Fall Time	$t_f =$	6.000	ns	Page
MOSFET Turn Off Delay Time	$t_{\text{doff}} =$	40	ns	
MOSFET Total Gate Charge	$Q_g =$	30.000	nC	
Actual Resonant Frequency During DCM Dead Time	$f_{\text{RES_actual}} =$	1.970	MHz	
Actual Estimated Time to First Resonant Valley	$t_{\text{RES_actual}} =$	0.254	μs	
Valley Switching Achieved?	YES or NO	YES		
MOSFET V_{DS} Derating	$V_{\text{DSderated}} =$	0.790		
MOSFET Continuous Current Rating	$I_{\text{DRAIN}} =$	10.853	A	
MOSFET Pulsed Current Rating	$I_{\text{PULSED}} =$	27.273	A	
Estimated MOSFET Conduction Losses	$P_{\text{FETconduction}} =$	0.326	W	
Estimated MOSFET Switching Losses	$P_{\text{FETswitching}} =$	0.388	W	
Total Estimated MOSFET Power Loss	$P_{\text{FET}} =$	0.714	W	
Recommended Clamping Voltage on Drain	$V_{\text{DRAINclamp}} =$	273.987	V	

OUTPUT DIODE, D_{OUT}				
Forward Voltage Drop of Output Rectifier, $V_F =$	$V_F =$	1.380	V	User Input
Minimum Required Blocking Voltage Rating	$V_{\text{DOUT_blocking}} =$	557.582	V	
Required Minimum Average Rectified Output Current	$I_{\text{Dout}} =$	1.326	A	
Power Dissipation of D_{OUT}	$P_{\text{Dout}} =$	1.033	W	

AUXILIARY WINDING DIODE, D_{AUX}				
Auxiliary Rectifier Forward Voltage Drop	$V_{\text{FA}} =$	1.250	V	User Input
Minimum Required Blocking Voltage Rating	$V_{\text{DBIAS_blocking}} =$	53.877	V	

OUTPUT INDUCTOR, L_{OUT}				
DCR of Output Inductor	$\text{DCR}_{\text{Lout}} =$	0	m Ω	User Input

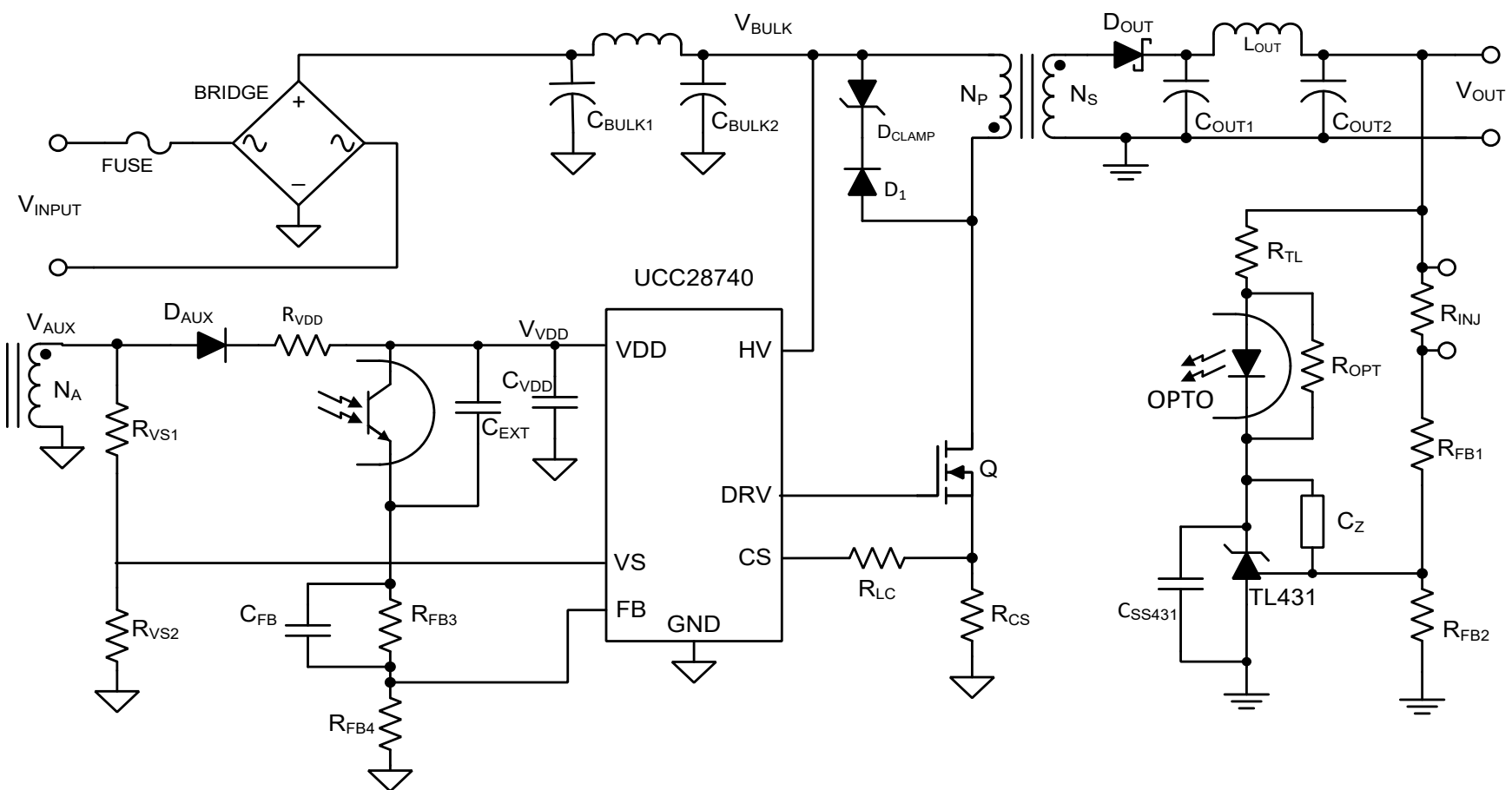
OUTPUT CAPACITOR, C_{OUT}				
Minimum Required C_{OUT} Without Opto-Coupled FeedBack	$C_{\text{OUT_no_opto}} =$	14000.000	μF	The importance of using opto feedback should be noted here!
Recommended Minimum Required Output Capacitor With Opto-Coupled FeedBack	$C_{\text{OUTrecommended}} =$	680.000	μF	
Actual Output Capacitance Used	$C_{\text{OUTactual}} =$	700.000	μF	User Input
C_{OUT} Used in Calculations	$C_{\text{OUT}} =$	700.000	μF	
Required Minimum Ripple Current Rating	$I_{\text{COUTrms}} =$	1.128	A	
Recommended Maximum ESR	$\text{ESR}_{\text{Coutrecommended}} =$	8.517	m Ω	
Actual ESR of C_{OUT} Used	$\text{ESR}_{\text{Coutactual}} =$	50.000	m Ω	User Input
ESR Used in Calculations	$\text{ESR}_{\text{Cout}} =$	50.000	m Ω	
Resultant Output Voltage Peak to Peak Ripple	$V_{\text{OUTripple}} =$	176.206	mV	

VOLTAGE SENSE DIVIDER, $R_{\text{VS1}}, R_{\text{VS2}}$				
VS Line Sense Run Current, Minimum	$I_{\text{VSLrun_min}} =$	190	μA	Device Parameter
VS Line Sense Run Current, Nominal	$I_{\text{VSLrun_nom}} =$	225	μA	Device Parameter
VS Line Sense Run Current, Maximum	$I_{\text{VSLrun_max}} =$	275	μA	Device Parameter
VS Line Sense Stop Current, Minimum	$I_{\text{VSLstop_min}} =$	70	μA	Device Parameter
VS Line Sense Stop Current, Nominal	$I_{\text{VSLstop_nom}} =$	80	μA	Device Parameter
VS Line Sense Stop Current, Maximum	$I_{\text{VSLstop_max}} =$	100	μA	Device Parameter
Recommended Resistor Value for Minimum Start Up Line Voltage	$R_{\text{VS1recommended}} =$	38.300	k Ω	
Actual Resistor Value Used for Minimum Start Up Line Voltage	$R_{\text{VS1actual}} =$	36.3	k Ω	User Input
R_{VS1} Value Used in Calculations	$R_{\text{VS1}} =$	36.3	k Ω	
Resultant Turn On Voltage, Minimum	$V_{\text{TURNONmin}} =$	52.671	VAC	
Resultant Turn On Voltage, Nominal	$V_{\text{TURNONnom}} =$	62.373	VAC	
Resultant Turn On Voltage, Maximum	$V_{\text{TURNONmax}} =$	76.234	VAC	
Resultant Input Brown Out Voltage, Minimum	$V_{\text{BROWNOUTmin}} =$	34.029	VAC	
Resultant Input Brown Out Voltage, Nominal	$V_{\text{BROWNOUTnom}} =$	36.801	VAC	
Resultant Input Brown Out Voltage, Maximum	$V_{\text{BROWNOUTmax}} =$	42.345	VAC	
Internal VS Over Voltage Threshold, Minimum	$V_{\text{OVpmin}} =$	4.52	V	Device Parameter
Internal VS Over Voltage Threshold, Nominal	$V_{\text{OVpnom}} =$	4.600	V	Device Parameter
Internal VS Over Voltage Threshold, Maximum	$V_{\text{OVpmax}} =$	4.710	V	Device Parameter
Recommended Resistor Value for Desired Output Over Voltage Limit	$R_{\text{VS2recommended}} =$	28.700	k Ω	
Actual Resistor Value Used for Desired Output Over Voltage Limit	$R_{\text{VS2actual}} =$	29.000	k Ω	User Input
R_{VS2} Used in Calculations	$R_{\text{VS2}} =$	29.000	k Ω	
Resultant Output Over Voltage Threshold, Minimum	$V_{\text{OUT_OVpmin}} =$	81.790	V	
Resultant Output Over Voltage Threshold, Nominal	$V_{\text{OUT_OVpnom}} =$	83.213	V	Actual Output Over Voltage
Resultant Output Over Voltage Threshold, Maximum	$V_{\text{OUT_OVpmax}} =$	85.170	V	

LINE COMPENSATION, R_{LC}				
Line Compensation Current Ratio, Nominal	$K_{LCnom} =$	25	A/A	Device Parameter
Total Estimated Current Sense Delay	$t_{DELAY} =$	90	ns	
Recommended Resistor Value for Line Compensation	$R_{LCrecommended} =$	1.270	k Ω	
Actual Resistor Value Used for Line Compensation	$R_{LCactual} =$	1.300	k Ω	User Input
R_{LC} Used in Calculations	$R_{LC} =$	1.300	k Ω	
Result of R_{LC} selection	Output Constant Current will have minimal deviation over input line voltage range.			

VDD CAPACITOR, C_{VDD}				
Device Supply Current During Run Mode, Maximum	$I_{RUNmax} =$	2.65	mA	Device Parameter
VDD _{ON} Voltage, Maximum	$VDD_{ONmax} =$	23	V	Device Parameter
VDD _{OFF} Voltage, Maximum	$VDD_{OFFmax} =$	8.15	V	Device Parameter
Estimated Minimum Switching Frequency at No-Load	$f_{SWmin} =$	0.946	kHz	
Estimated Over Voltage Charge Duration	$t_{OV} =$	20.000	ms	
Minimum VDD Capacitor for Start UP	$C_{VDD1} =$	33.000	μ F	
Minimum VDD Capacitor for Load Transient	$C_{VDD2} =$	22.000	μ F	
Minimum VDD Capacitor for Target Ripple on VDD	$C_{VDD3} =$	3.300	μ F	
Recommended Capacitor on VDD	$C_{VDDrecommended} =$	33.000	μ F	

OPTO-COUPLED FEEDBACK				
Reference Voltage of TL431 Shunt Regulator	$VREF_{431} =$	80	V	User Input
Shunt Regulator Reference Input Current, Maximum	$I_{REF431} =$	10000	μ A	User Input
Recommended Bottom Resistor Value for Output Voltage Set Point	$R_{FB2recommended} =$	0.562	k Ω	
Actual Bottom Resistor Value Used for Output Voltage Set Point	$R_{FB2actual} =$	0.55	k Ω	User Input
R_{FB2} Used in Calculations	$R_{FB2} =$	0.55	k Ω	
Recommended Top Resistor Value for Output Voltage Set Point	$R_{FB1recommended} =$	Err:502	k Ω	
Actual Top Resistor Value Used for Output Voltage Set Point	$R_{FB1actual} =$	170	k Ω	User Input
R_{FB1} Used in Calculations	$R_{FB1} =$	170.02	k Ω	
Noise Injection Resistor For Loop Analysis	$R_{INJ} =$	20	Ω	May be changed by User here
Resultant Nominal Constant Voltage Output Voltage	$V_{OUT_{CV}} =$	24810.182	V	
Minimum Current Transfer Ratio of Selected Opto-Coupler	$CTR_{min} =$	50	%	User Input
Response Fall Time of Opto-Coupler	$t_{f_{opto}} =$	3	μ s	User Input
R_L of Specified Opto-Coupler Fall Time	$R_{L_{opto}} =$	100	Ω	User Input
Cut-Off Frequency of Opto-Coupler	$f_{c_{opto}} =$	80	kHz	User Input
Input Forward Voltage of Opto-Coupler	$V_{F_{opto}} =$	1.4	V	User Input
Equivalent Opto-Coupler Output Capacitance	$C_{OPTO} =$	4.775	nF	
Equivalent Internal UCC28740 Dynamic Resistance	$R_{EQU} =$	40	k Ω	
Recommended Value for External Capacitor on Opto-Coupler	$C_{EXTrecommended} =$	0	μ F	
Actual Value of External Capacitor on Opto-Coupler Used	$C_{EXTactual} =$	0	μ F	User Input
C_{EXT} Used in Calculations	$C_{EXT} =$	0	μ F	
Recommended C_{FB}	$C_{FBrecommended} =$	0.047	μ F	
Actual C_{FB} Used	$C_{FBactual} =$	0.047	μ F	User Input
C_{FB} Used in Calculations	$C_{FB} =$	0.047		
Recommended Value For R_{FB4}	$R_{FB4recommended} =$	22	k Ω	
Actual Value for R_{FB4} Used	$R_{FB4actual} =$	22	k Ω	User Input
R_{FB4} Used in Calculations	$R_{FB4} =$	22	k Ω	
Opto-Coupler Emitter Current to FB Pin Current Gain	$G_{FB1} =$	0.355		
FB Pin Current to Control Law Voltage Gain, Full Load	$G_{FB2} =$	-192	k Ω	
Control Law Voltage to Power Stage Modulation Gain, FM Mode	$K_{FM4} =$	50.4	kHz/V	
Power Stage Modulation (FM) to Average Current Gain	$G_{P4} =$	7.778	μ C	
Recommended Value for Shunt Regulator Bias Resistor	$R_{TLrecommended} =$	1.5	k Ω	
Actual Value of Shunt Regulator Bias Resistor Used	$R_{TLactual} =$	1.5	k Ω	User Input
R_{TL} Used in Calculations	$R_{TL} =$	1.5	k Ω	
Recommended Value for Compensation Capacitor	$C_{Zrecommended} =$	1500	pF	
Actual Value Used C_Z	$C_{Zactual} =$	1500	pF	User Input
C_Z Used in Calculations	$C_Z =$	1500	pF	



RECOMMENDED BILL OF MATERIALS

Reference Designator	Description/Comments
BRIDGE RECTIFIER	Minimum DC Blocking Voltage: 400 V
	Minimum Current Rating: 1.324 A
	Power Dissipation: 3257.428 mW
C_{BULKtotal} = C_{BULK1} + C_{BULK2}	Type: Aluminum Electrolytic
	Value: 250 μ F Total Capacitance
	Minimum Voltage Rating: 400 V
	Minimum Ripple Current Rating: 1480.649 mA
C_{EXT}	Type: Ceramic
	Value: 0 μ F \pm 10%
	Minimum Voltage Rating: 50 V
C_{FB}	Type: Ceramic
	Value: 0.047 μ F \pm 10%
	Minimum Voltage Rating: 10 V
C_{OUTtotal} = C_{OUT1} + C_{OUT2}	Type: Aluminum Electrolytic
	Minimum Value: 700 μ F Total Capacitance
	Minimum Voltage Rating: 80.000 V
	Minimum Ripple Current Rating: 1.128 A
	Maximum ESR Rating: 8.517 m Ω
C_{SS431}	Type: Ceramic
	Value: 1 μ F \pm 10%
	Minimum Voltage Rating: 10 V
C_{VDD}	Type: Ceramic
	Minimum Value: 33 μ F \pm 10%
	Voltage Rating: 50 V
C_Z	Type: Ceramic
	Value: 1500 pF \pm 10%
	Voltage Rating: 10 V
D_{AUX}	Type: Switching
	Minimum Required Blocking Voltage: 53.877 V
	Minimum Rated Current: 250 mA

D _{CLAMP}	Type:	Transient Voltage Suppressor	
	Voltage:	273.987 V	
	Power Rating:	600.000 W	
D _{OUT}	Type:	Schottky	
	Minimum Blocking Voltage Rating:	557.582 V	
	Minimum Average Current Rating:	1.326 A	
	Power Dissipation:	1.033 W	
D ₁	Type:	Ultra Fast	
	Voltage Rating:	1000 V	
	Current Rating:	1 A	
FUSE	Type:	Slow Blow	
	Minimum Voltage Rating:	265 VAC	
	Minimum Peak Current Rating:	4.301 A	
OPTO-COUPLER	CTR _{min} :	50 %	
Q	Minimum V _{DS} Voltage Rating:	800 V	
	Minimum Continuous Current Rating:	10.853 A	
	Minimum Repetitive Peak Current Rating:	27.273 A	
	Power Dissipation:	0.714 W	
R _{CS}	Value:	0.300 Ω	±1%
	Power Dissipation:	315.393 mW	
	Type:	Low Inductance	
R _{FB1}	Value:	170 kΩ	±1%
	Power Rating:	1/10 W	
R _{FB2}	Value:	0.55 kΩ	±1%
	Power Rating:	1/10 W	
R _{FB3}	Value:	100 kΩ	±1%
	Power Rating:	1/10 W	
R _{FB4}	Value:	22 kΩ	±1%
	Power Rating:	1/10 W	
R _{INJ}	Value:	20 Ω	±1%
	Power Rating:	1/10 W	
R _{LC}	Value:	1.3 kΩ	±1%
	Power Rating:	1/10 W	
R _{OPT}	Value:	1 kΩ	±1%
	Power Rating:	1/10 W	
R _{TL}	Value:	1.5 kΩ	±1%
	Power Rating:	1/10 W	
R _{VDD}	Value:	2 to 50 Ω	As Needed for Voltage Spike Smoothing
	Power Rating:	1/10 to 1/2 W	
R _{VS1}	Value:	36.3 kΩ	±1%
	Power Rating:	1/10 W	
R _{VS2}	Value:	29 kΩ	±1%
	Power Rating:	1/10 W	
SHUNT REGULATOR	Voltage Reference:	80 V	
TRANSFORMER	Primary Inductance:	203.88 μH	
	Primary to Secondary Turns Ratio:	1.367	N _{PS}
	Primary to Auxiliary Turns Ratio:	10.800	N _{PA}
	Peak Primary Current:	2.577 A	
	Primary RMS Current:	1.025 A	
	Peak Secondary Current:	3.522 A	

Secondary RMS Current:	1.326 A
Maximum Switching Frequency:	90.000 kHz