

UCC28740 DESIGN CALCULATOR

TI Literature Number: SLUC487B

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UCC28740 CONSTANT-VOLTAGE, CONSTANT-CURRENT F

CLEAR ALL USER INPUT CELLS BEFORE START
ALL GREEN CELLS ARE USER INPUT

WHERE APPLICABLE, A RECOMMENDED VALUE IS GIVEN THAT WILL BE THE BEST CHOICE FOR THE USER. IT IS THE BEST INTEREST OF THE USER TO USE A VALUE AS CLOSE AS POSSIBLE TO THE RECOMMENDED VALUE. FOR ACCURATE RESULTS, THE USER MUST ENTER THE ACTUAL VALUE USED.

DESIGN REQUIREMENTS

INPUT SPECIFICATIONS

Input Voltage Type, AC or DC:	AC	
Minimum Input Voltage, V_{INPUTmin} =	85	VAC
Maximum Input Voltage, V_{INPUTmax} =	265	VAC
Nominal Input Voltage, V_{INPUTnom} =	230	VAC
Minimum Line Frequency, f_{LINEmin} =	47	Hz
Minimum Input Voltage for Start-Up, V_{INPUTrun} =	80	VAC

OUTPUT SPECIFICATIONS

Regulated Output Voltage, Constant Voltage Mode, $V_{\text{OUT_CV}}$ =	85	VDC
Full Load Rated Output Current, I_{OUT} =	0.6	A
Target Constant Current Mode Output Load Threshold, I_{OCC} =	0.6	A
Target Minimum Output Voltage During Constant Current Regulation, $V_{\text{OUT_CC}}$ =	30	VDC
Allowable Output Voltage Drop During Load-Step Transient in Constant Voltage Mode, $V_{\text{OUT}\Delta}$ =	0.5	V
Maximum Peak to Peak Output Voltage Ripple, V_{RIPPLE} =	30	mV

Maximum Desired Switching Frequency, User must input value not greater than 100 kHz, $f_{\max} =$	100	kHz
Output Over Voltage Protection, $V_{\text{OUT_OVP}} =$	90	V
Required Positive Load Step Transient Current, $I_{\text{TRAN}} =$	0.65	A
Maximum Allowable Response Time to Load Step Transient, $t_{\text{RESP}} =$	20	ms
Target Maximum Stand By Power Dissipation, $P_{\text{SBtarget}} =$	50	mW

COMPONENT SELECTION USER INPUT

COMPONENT	PARAMETER	
Input Capacitor, C _{BULK}		
Desired Minimum Valley Voltage, V _{BULKvalley_desired} =	100	V
Recommended Input Bulk Capacitance, C _{BULK} =	233.16	μF
Actual Input Bulk Capacitance, C _{BULK} , Used =	250.00	μF
Output Rectifier, D _{OUT}		
Forward Voltage Drop of Output Rectifier, V _F =	1.25	V
Output Inductor, L _{OUT}		
DCR of Output Inductor, DCR _{Lout} , if used =	0	mΩ

Flyback Transformer, Primary to Secondary Turns Ratio

Ideal Primary to Secondary Turns Ratio, $N_{\text{PSideal}} =$	1.313	
Actual Primary to Secondary Turns Ratio Used, $N_{\text{PS}} =$	1.321	Enter Actual N_{PS} of Transformer Used
Current Sense Resistor, R_{CS}		
Recommended Current Sense Resistor, $R_{\text{CS}} =$	0.339	Ω
Actual Current Sense Resistor Used, $R_{\text{CS}} =$	0.374	Ω
Flyback Transformer, T		
Recommended Primary Inductance Value, $L_{\text{p}} =$	260.316	μH

Actual Primary Inductance Used, $L_p =$	250.000	μH
Recommended Primary to Auxillary Turns Ratio, $N_{PA} =$	4.392	Suggested N_p
Actual Primary to Auxiliary Turns Ratio, $N_{PA} =$	4.625	Enter Actual N_{PA} of Transformer Used
MOSFET Switch, Q		
Required Drain to Soure Voltage Rating , $V_{DS\text{rated}} =$	635.314	V
MOSFET Rated Drain to Source Voltage, $V_{DS} =$	800	V
Output Capacitance of Selected MOSFET, $C_{OSS} =$	16	pF
Drain to Source On-Resistance of Selected MOSFET, $R_{DSon} =$	0.36	Ω
MOSFET Fall Time, $t_f =$	6	ns
MOSFET Turn Off Delay Time, $t_{Doff} =$	40	ns
MOSFET Total Gate Charge, $Q_g =$	30	nC

Output Capacitor, C_{OUT}		
Recommended Minimum Output Capacitance, $C_{OUT} =$	470.000	μF
Actual Minimum Output Capacitance, $C_{OUT} =$	540.000	μF
Recommended Maximum ESR, $ESR_{Cout} =$	10.988	m Ω
Actual ESR of C_{OUT} Used, $ESR_{Cout} =$	50.000	m Ω
Bridge Rectifier, D_{BRIDGE}		
Forward Voltage Drop, $V_{F\ BRIDGE} =$	1.1	V
Auxiliary Winding Rectifier, D_{AUX}		
Auxiliary Rectifier Forward Voltage Drop, $V_{FA} =$	1.25	V
Input Line Voltage Turn On Resistor, R_{VS1}		
Recommended Value for R_{VS1} , $R_{VS1} =$	90.900	k Ω
Actual Value for R_{VS1} , $R_{VS1} =$	90.900	k Ω
Output Over Voltage Resistor, R_{VS2}		
Recommended Value for R_{VS2} , $R_{VS2} =$	20.500	k Ω
Actual Value for R_{VS2} , $R_{VS2} =$	20.500	k Ω
Line Compensation Resistor, R_{LC}		
Recommended Value for R_{LC} , $R_{LC} =$	1.400	k Ω
Actual Value for R_{LC} , $R_{LC} =$	1.400	k Ω
Loop Compensation Components, R_{FB1}, R_{FB2}, R_{TL}, R_{OPT}, C_{FB}, C_{EXT}, R_{FB3}, R_{FB4}, C_Z		
Reference Voltage of Shunt Regulator, i.e. TL431, $V_{REF431} =$	2.5	V
Maximum Reference Input Current of Shunt Regulator, $I_{REF431} =$	4	μA
Recommended Value for R_{FB2} , $R_{FB2} =$	44.2	k Ω

Actual Value for R_{FB2} , $R_{FB2} =$	44.2	k Ω
Recommended Value for R_{FB1} , $R_{FB1} =$	1470	k Ω
Actual Value for R_{FB1} , $R_{FB1} =$	1470	k Ω
Minimum Current Transfer Ratio of Selected Opto-Coupler, CTR_{min} =	50	%
Response Fall Time of Opto-Coupler, $t_{f_{opto}} =$	18	μ s
R_L of Specified Opto-Coupler Fall Time, $R_{L_{opto}} =$	100	Ω
Cut-Off Frequency of Opto-Coupler, $f_{c_{opto}} =$	2	kHz
Input Forward Voltage of Opto-Coupler, $V_{F_{opto}} =$	1.2	V
Recommended External Capacitor Across Opto-Coupler Output, $C_{EXT} =$	0	μ F
Actual Value for C_{EXT} Used , $C_{EXT} =$	0.0015	μ F
Recommended Capacitor on Opto_Coupler Emitter, $C_{FB} =$	0.047	μ F
Actual Value for C_{FB} Used, $C_{FB} =$	0.047	μ F
Recommended Value For R_{FB4} , $R_{FB4} =$	22	k Ω
Actual Value for R_{FB4} Used	22	k Ω
Recommended Value for Shunt Regulator Bias Resistor, $R_{TL} =$	1.5	k Ω
Actual Value of Shunt Regulator Bias Resistor Used, $R_{TL} =$	1.5	k Ω
Recommended Value for Compensation Capacitor, $C_z =$	220	pF
Actual Value of Compensation Capacitor Used, $C_z =$	1500	pF

ATOR TOOL

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is licensed solely on an "as is" basis. The entire risk as
omer.

FLYBACK DESIGN CALCULATOR

ING A NEW DESIGN PUTS

oice to meet the given specification. It is in
he suggested recommended value. For
sed in the appropriate cell.

Choose either AC or DC

For universal line enter 47 Hz

Recommend target to be a minimum of 5% higher
than rated lout

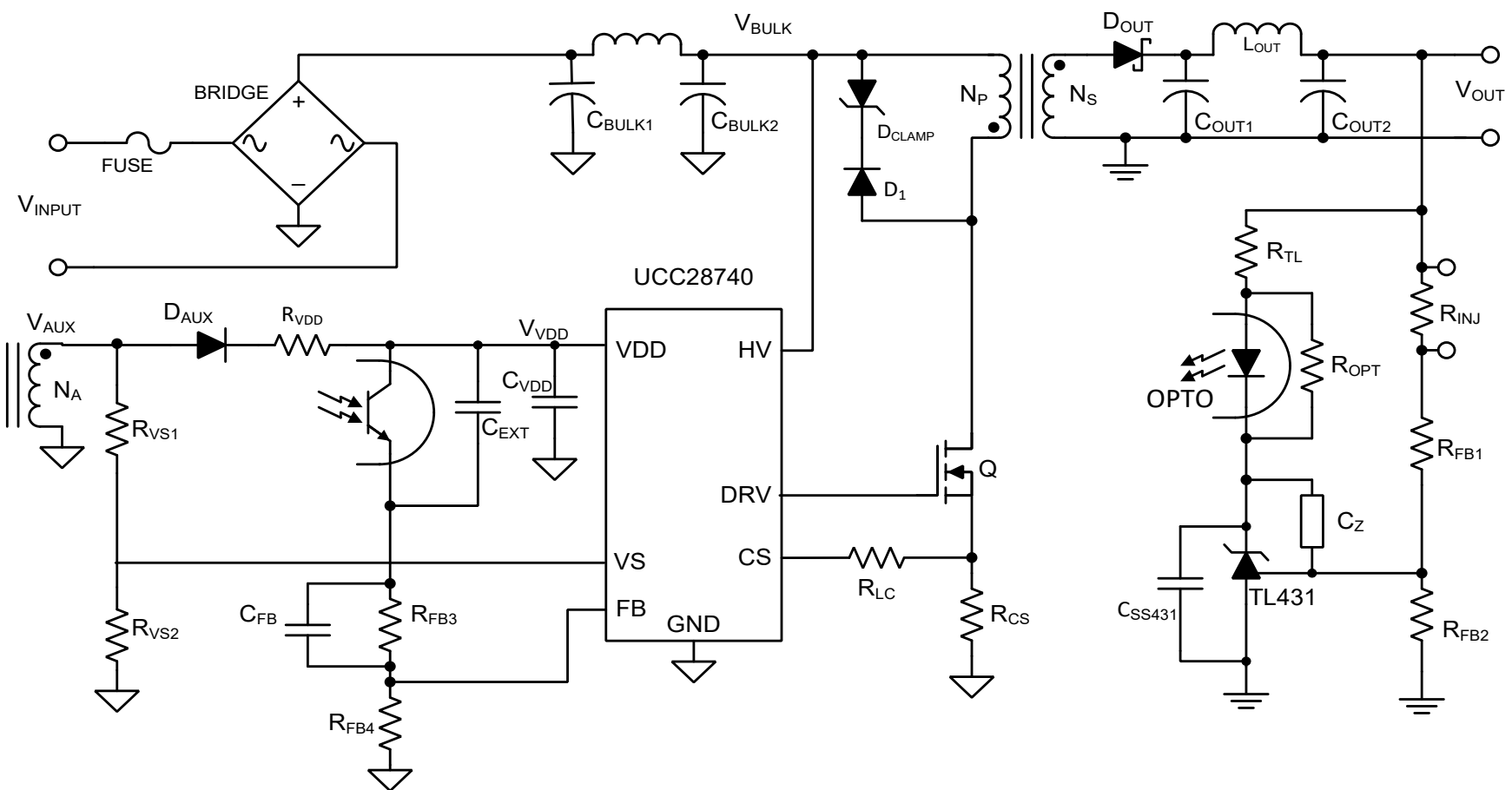
NOTES
COMMENT
Used to determine the required input bulk capacitor at minimum line, full load. For DC input, use $V_{INPUTrun}$
Using a value less than recommended will result in a minimum valley voltage less than desired, requiring a larger power stage to accommodate the higher currents due to the lower input rail. Using a value larger than recommended will result in a higher input rail and lower currents on the power stage but higher peak current in the input capacitor itself.
Enter actual input bulk capacitor used
Enter V_F at full load
Enter 0 if no secondary LC filter used

Recommended N_{PS}
Recommended R_{CS}
Enter Actual R_{CS} Used

locc will be less than target locc becau

Enter Actual R_{FB2} Used
Recommended R_{FB1}
Enter Actual R_{FB1} Used
Enter CTR_{min}
Enter Opto-Coupler t_f
Enter R_L from Opto-Coupler t_f spec
Enter Opto-Coupler Cut-Off Frequency
Enter Maximum V_F of Opto-Coupler
Recommended C_{EXT}
Enter Actual C_{EXT} Used
Recommended C_{FB}
Enter Actual C_{FB} Used
Recommended R_{FB4}
Enter Actual R_{FB4} Used
Recommended R_{TL}
Enter Actual R_{TL} Used
Recommended C_Z
Enter Actual C_Z Used

se Rcs is greater than recommended value



RECOMMENDED BILL OF MATERIALS

Reference Designator	Description/Comments
BRIDGE RECTIFIER	Minimum DC Blocking Voltage: 400 V
	Minimum Current Rating: 1.184 A
	Power Dissipation: 3116.985 mW
C_{BULKtotal} = C_{BULK1} + C_{BULK2}	Type: Aluminum Electrolytic
	Value: 250 µF Total Capacitance
	Minimum Voltage Rating: 400 V
	Minimum Ripple Current Rating: 1416.812 mA
C_{EXT}	Type: Ceramic
	Value: 0.0015 µF ±10%
	Minimum Voltage Rating: 50 V
C_{FB}	Type: Ceramic
	Value: 0.047 µF ±10%
	Minimum Voltage Rating: 10 V
C_{OUTtotal} = C_{OUT1} + C_{OUT2}	Type: Aluminum Electrolytic
	Minimum Value: 540 µF Total Capacitance
	Minimum Voltage Rating: 85.000 V
	Minimum Ripple Current Rating: 0.876 A
	Maximum ESR Rating: 10.988 mΩ
C_{SS431}	Type: Ceramic
	Value: 1 µF ±10%
	Minimum Voltage Rating: 10 V
C_{VDD}	Type: Ceramic
	Minimum Value: 10 µF ±10%
	Voltage Rating: 50 V
C_Z	Type: Ceramic
	Value: 1500 pF ±10%
	Voltage Rating: 10 V
D_{AUX}	Type: Switching
	Minimum Required Blocking Voltage: 128.693 V
	Minimum Rated Current: 250 mA

D _{CLAMP}	Type:	Transient Voltage Suppressor	
	Voltage:	271.297 V	
	Power Rating:	600.000 W	
D _{OUT}	Type:	Schottky	
	Minimum Blocking Voltage Rating:	579.072 V	
	Minimum Average Current Rating:	1.028 A	
	Power Dissipation:	0.725 W	
D ₁	Type:	Ultra Fast	
	Voltage Rating:	1000 V	
	Current Rating:	1 A	
FUSE	Type:	Slow Blow	
	Minimum Voltage Rating:	265 VAC	
	Minimum Peak Current Rating:	3.939 A	
OPTO-COUPLER	CTR _{min} :	50 %	
Q	Minimum V _{DS} Voltage Rating:	800 V	
	Minimum Continuous Current Rating:	8.731 A	
	Minimum Repetitive Peak Current Rating:	21.877 A	
	Power Dissipation:	0.580 W	
R _{CS}	Value:	0.374 Ω	±1%
	Power Dissipation:	254.505 mW	
	Type:	Low Inductance	
R _{FB1}	Value:	1470 kΩ	±1%
	Power Rating:	1/10 W	
R _{FB2}	Value:	44.2 kΩ	±1%
	Power Rating:	1/10 W	
R _{FB3}	Value:	100 kΩ	±1%
	Power Rating:	1/10 W	
R _{FB4}	Value:	22 kΩ	±1%
	Power Rating:	1/10 W	
R _{INJ}	Value:	20 Ω	±1%
	Power Rating:	1/10 W	
R _{LC}	Value:	1.4 kΩ	±1%
	Power Rating:	1/10 W	
R _{OPT}	Value:	1 kΩ	±1%
	Power Rating:	1/10 W	
R _{TL}	Value:	1.5 kΩ	±1%
	Power Rating:	1/10 W	
R _{VDD}	Value:	2 to 50 Ω	As Needed for Voltage Spike Smoothing
	Power Rating:	1/10 to 1/2 W	
R _{VS1}	Value:	90.9 kΩ	±1%
	Power Rating:	1/10 W	
R _{VS2}	Value:	20.5 kΩ	±1%
	Power Rating:	1/10 W	
SHUNT REGULATOR	Voltage Reference:	2.5 V	
TRANSFORMER	Primary Inductance:	250 μH	
	Primary to Secondary Turns Ratio:	1.321	N _{PS}
	Primary to Auxiliary Turns Ratio:	4.625	N _{PA}
	Peak Primary Current:	2.067 A	
	Primary RMS Current:	0.825 A	
	Peak Secondary Current:	2.730 A	

Secondary RMS Current:	1.028 A
Maximum Switching Frequency:	93.714 kHz

UCC28740 DESIGN CALCULATIONS

The Values Entered by the User on the DESIGN INPUT Page are Used in the Design Calculations

INPUT				
Input Voltage Type	AC or DC:	AC		User Input Values From Design Input Page
Minimum Input Voltage	$V_{\text{INPUTmin}} =$	85VAC		
Maximum Input Voltage	$V_{\text{INPUTmax}} =$	265VAC		
Nominal Input Voltage	$V_{\text{INPUTnom}} =$	230VAC		
Minimum Line Frequency	$f_{\text{LINEmin}} =$	47Hz		
Minimum Input Voltage for Start-Up	$V_{\text{INPUTrun}} =$	80VAC		
Minimum Peak Bulk Input Voltage	$V_{\text{BULKmin}} =$	120.208V		
Maximum Peak Bulk Input Voltage	$V_{\text{BULKmax}} =$	374.767V		
Nominal Peak Bulk Input Voltage	$V_{\text{BULKnom}} =$	325.269V		
Turn-On Peak Bulk Input Voltage	$V_{\text{BULKstartup}} =$	113.137V		
Line Cycle Period	$t_{\text{LINE}} =$	21.277ms		
OUTPUT				
Regulated Output Voltage, Constant Voltage Mode	$V_{\text{OUT_CV}} =$	85V		User Input Values From Design Input Page
Full Load Rated Output Current	$I_{\text{OUT}} =$	0.6A		
Target Constant Current Mode Output Load Threshold	$I_{\text{OCC_target}} =$	0.6A		
Target Minimum Output Voltage During Constant Current Regulation	$V_{\text{OUT_CC}} =$	30V		
Allowable Output Voltage Drop During Load-Step Transient in Constant Voltage Mode	$V_{\text{OUTA}} =$	0.5V		
Maximum Peak to Peak Output Voltage Ripple	$V_{\text{RIPPLE}} =$	30mV		
Required Positive Load Step Transient Current	$I_{\text{TRAN}} =$	0.65A		
Maximum Allowable Response Time to Load Step Transient	$t_{\text{RESP}} =$	20ms		
Output Over Voltage Protection	$V_{\text{OUT_OVP}} =$	90V		
Maximum Stand By Power Dissipation	$P_{\text{SBtarget}} =$	50mW		
Estimated Efficiency	$\eta =$	0.850		
Output Power	$P_{\text{OUT}} =$	51.000W		
Estimated Input Power	$P_{\text{IN}} =$	60.000W		

COMPONENT PARAMETER CALCULATIONS

INPUT CAPACITOR, C_{BULK}			
Recommended Input Bulk Capacitance	$C_{\text{BULKrecommended}} =$	233.16 μ F	
Actual Input Bulk Capacitance	$C_{\text{BULKactual}} =$	250.000 μ F	User Input
Input Capacitor Value Used in Calculations	$C_{\text{BULK}} =$	250.000 μ F	
Minimum Valley Voltage on Input Bulk Capacitors	$V_{\text{BULKvalley}} =$	101.326V	
Minimum Input Capacitor Ripple Current Rating	$I_{\text{CIRipple}} =$	1416.812mA	
Minimum Input Capacitor Voltage Rating	$V_{\text{Cin}} =$	400V	

INPUT FUSE			
Voltage Rating	$V_{\text{FUSE}} =$	265VAC	
Peak Input Current	$I_{\text{Npeak}} =$	3.939A	

BRIDGE RECTIFIER			
Voltage Rating	$V_{\text{BRIDGE_minrating}} =$	400.000V	
Current Rating	$I_{\text{BRIDGE_minrating}} =$	1.184A	
Forward Voltage Drop	$V_{\text{F_BRIDGE}} =$	1.100V	User Input
Full Load Power Dissipation of Bridge Rectifier	$P_{\text{BRIDGE}} =$	3116.985mW	

TRANSFORMER TURNS-RATIO, N_{PS}			
Demagnetizing Duty Cycle	$D_{\text{DEMAG_CC}} =$	0.425	Device Parameter
Amplitude Modulation Control Ratio	$K_{\text{AMnom}} =$	4	Device Parameter
Maximum Desired Switching Frequency	$f_{\text{max_target}} =$	100.000kHz	User Input
Desired Switching Period	$t_{\text{SW_target}} =$	10.000 μ s	
Resonant Frequency During DCM Dead Time	$f_{\text{RES}} =$	0.500MHz	
Time to First Resonant Valley	$t_{\text{RES}} =$	1.000 μ s	
Estimated Maximum Duty Cycle	$D_{\text{max_target}} =$	0.475	

Ideal Primary to Secondary Turns Ratio	$N_{PSideal} =$	1.3130	Ideal N_{PS}
Actual Primary to Secondary Turns Ratio	$N_{PSactual} =$	1.321	User Input
Primary to Secondary Turns Ratio Used in Calculations	$N_{PS} =$	1.321	
Actual Flyback Voltage	$V_{FLYBACK} =$	113.936V	
Allowable Leakage Inductance Voltage Spike	$V_{LEAKAGE} =$	311.297V	
Estimated Maximum On-Time	$t_{ONestimated} =$	4.764 μ s	
Estimated Transformer Efficiency	$\eta_{XFMR} =$	0.9	

CURRENT SENSE RESISTOR, R_{CS} , PEAK PRIMARY CURRENT, I_{PP}			
Constant Current Regulation Factor, Minimum	$V_{CCR_min} =$	318mV	Device Parameter
Constant Current Regulation Factor, Nominal	$V_{CCR_nom} =$	330mV	Device Parameter
Constant Current Regulation Factor, Maximum	$V_{CCR_max} =$	343mV	Device Parameter
Initial estimate for L_p	$L_{P_estimate} =$	251.721 μ H	
Recommended Current Sense Resistor Value	$R_{CSrecommended} =$	0.339 Ω	
Actual Current Sense Resistor Used	$R_{CSactual} =$	0.374 Ω	User Input
Current Sense Resistor Value Used in Calculation	$R_{CS} =$	0.374 Ω	
Power Dissipation of R_{CS}	$P_{Rcs} =$	254.505mW	
Maximum Current Sense Threshold Voltage, Minimum	$V_{CSTmax_min} =$	738mV	Device Parameter
Maximum Current Sense Threshold Voltage, Nominal	$V_{CSTmax_nom} =$	773mV	Device Parameter
Maximum Current Sense Threshold Voltage, Maximum	$V_{CSTmax_max} =$	810mV	Device Parameter
Peak Primary Current, Minimum, Full Load	$I_{PPmin} =$	1.973A	
Peak Primary Current, Nominal, Full Load	$I_{PPnom} =$	2.067A	
Peak Primary Current, Maximum, Full Load	$I_{PPmax} =$	2.166A	
Actual Output Current During Constant Current Mode	$I_{OCC_actual} =$	0.580A	
Peak Primary Current During Light Load, FM Mode	$I_{PP_FM} =$	0.517A	
Worst Case Peak Primary Current	$I_{PP_WC} =$	2.188A	Assumes -1% R_{CS} and V_{CSTmax_max}
Maximum Output Current During Constant Current Mode	$I_{OCCmax} =$	0.614A	Worst Case Estimate

TRANSFORMER PRIMARY INDUCTANCE, L_p			
Calculated L_p to meet f_{max_target} with chosen R_{CS}	$L_{Pcalc} =$	260.316 μ H	
Recommended Primary Inductance to meet t_{CSLEB} with chosen R_{CS}	$L_{Precommended} =$	260.316 μ H	Ideal L_p
Actual Primary Inductance	$L_{Pactual} =$	250.000 μ H	User Input
Primary Inductance Used in Calculations	$L_p =$	250.000 μ H	
Actual Maximum Nominal Switching Frequency	$f_{max} =$	93.714kHz	
Actual Switching Period	$t_{SWactual} =$	10.671 μ s	
Actual Maximum On-Time	$t_{ONmax} =$	5.099 μ s	
Maximum Duty Cycle	$D_{MAX} =$	0.478	
Demagnetization Time	$t_{DEMAG} =$	4.535 μ s	
Primary RMS Current	$I_{PRI_RMS} =$	0.825A	
Secondary Peak Current	$I_{SPmax} =$	2.730A	
Secondary RMS Current	$I_{SEC_RMS} =$	1.028A	
VDD Under Voltage Lock Out (UVLO) Voltage, Maximum	$VDD_{OFF_max} =$	8.150V	Device Parameter
VDD Under Voltage Lock Out (UVLO) Voltage, Minimum	$VDD_{OFF_min} =$	7.350V	Device Parameter
Recommended Auxiliary to Secondary Turns Ratio	$N_{ASrecommended} =$	0.301	
Recommended Primary to Auxiliary Turns Ratio	$N_{PArecommended} =$	4.392	
Actual Primary to Auxiliary Turns Ratio	$N_{PAactual} =$	4.625	User Input
Primary to Auxiliary Turns Ratio Used in Calculations	$N_{PA} =$	4.625	
Nominal VDD Voltage	$VDD =$	23.385V	
Actual Auxiliary to Secondary Turns Ratio	$N_{AS} =$	0.286	
Minimum On-Time, t_{CSLEB}	$t_{ONmin(limit)} =$	280.000ns	
Actual Minimum On-Time	$t_{ONmin(actual)} =$	344.689ns	
Minimum Demagnetizing Time	$t_{DEMAGmin} =$	1.134 μ s	
Minimum Output Voltage During Constant Current Mode	$V_{OUT_CCmin} =$	28.860V	

MOSFET, Q			
Required Drain to Source Voltage Rating, $V_{DSrated} =$	$V_{DSmin_rating} =$	635.314V	
MOSFET Rated Drain to Source Voltage	$V_{DS} =$	800.000V	User Input Values From Design Input
Output Capacitance of Selected MOSFET	$C_{OSS} =$	16pF	
Drain to Source On-Resistance of Selected MOSFET	$R_{DSon} =$	0.360 Ω	

MOSFET Fall Time	$t_f =$	6.000	ns	Page
MOSFET Turn Off Delay Time	$t_{doff} =$	40	ns	
MOSFET Total Gate Charge	$Q_g =$	30.000	nC	
Actual Resonant Frequency During DCM Dead Time	$f_{RES_actual} =$	1.779	MHz	
Actual Estimated Time to First Resonant Valley	$t_{RES_actual} =$	0.281	μ s	
Valley Switching Achieved?	YES or NO	YES		
MOSFET V_{DS} Derating	$V_{DSderated} =$	0.794		
MOSFET Continuous Current Rating	$I_{DRAIN} =$	8.731	A	
MOSFET Pulsed Current Rating	$I_{PULSED} =$	21.877	A	
Estimated MOSFET Conduction Losses	$P_{FETconduction} =$	0.245	W	
Estimated MOSFET Switching Losses	$P_{FETswitching} =$	0.335	W	
Total Estimated MOSFET Power Loss	$P_{FET} =$	0.580	W	
Recommended Clamping Voltage on Drain	$V_{DRAINclamp} =$	271.297	V	

OUTPUT DIODE, D_{OUT}				
Forward Voltage Drop of Output Rectifier, $V_F =$	$V_F =$	1.250	V	User Input
Minimum Required Blocking Voltage Rating	$V_{DOUT_blocking} =$	579.072	V	
Required Minimum Average Rectified Output Current	$I_{Dout} =$	1.028	A	
Power Dissipation of D_{OUT}	$P_{Dout} =$	0.725	W	

AUXILIARY WINDING DIODE, D_{AUX}				
Auxiliary Rectifier Forward Voltage Drop	$V_{FA} =$	1.250	V	User Input
Minimum Required Blocking Voltage Rating	$V_{DBIAS_blocking} =$	128.693	V	

OUTPUT INDUCTOR, L_{OUT}				
DCR of Output Inductor	$DCR_{Lout} =$	0	m Ω	User Input

OUTPUT CAPACITOR, C_{OUT}				
Minimum Required C_{OUT} Without Opto-Coupled FeedBack	$C_{OUT_no_opto} =$	26000.000	μ F	The importance of using opto feedback should be noted here!
Recommended Minimum Required Output Capacitor With Opto-Coupled FeedBack	$C_{OUTrecommended} =$	470.000	μ F	
Actual Output Capacitance Used	$C_{OUTactual} =$	540.000	μ F	User Input
C_{OUT} Used in Calculations	$C_{OUT} =$	540.000	μ F	
Required Minimum Ripple Current Rating	$I_{COUTrms} =$	0.876	A	
Recommended Maximum ESR	$ESR_{Coutrecommended} =$	10.988	m Ω	
Actual ESR of C_{OUT} Used	$ESR_{Coutactual} =$	50.000	m Ω	User Input
ESR Used in Calculations	$ESR_{Cout} =$	50.000	m Ω	
Resultant Output Voltage Peak to Peak Ripple	$V_{OUTripple} =$	136.625	mV	

VOLTAGE SENSE DIVIDER, R_{VS1} R_{VS2}				
VS Line Sense Run Current, Minimum	$I_{VSLrun_min} =$	190	μ A	Device Parameter
VS Line Sense Run Current, Nominal	$I_{VSLrun_nom} =$	225	μ A	Device Parameter
VS Line Sense Run Current, Maximum	$I_{VSLrun_max} =$	275	μ A	Device Parameter
VS Line Sense Stop Current, Minimum	$I_{VSLstop_min} =$	70	μ A	Device Parameter
VS Line Sense Stop Current, Nominal	$I_{VSLstop_nom} =$	80	μ A	Device Parameter
VS Line Sense Stop Current, Maximum	$I_{VSLstop_max} =$	100	μ A	Device Parameter
Recommended Resistor Value for Minimum Start Up Line Voltage	$R_{VS1recommended} =$	90.900	k Ω	
Actual Resistor Value Used for Minimum Start Up Line Voltage	$R_{VS1actual} =$	90.9	k Ω	User Input
R_{VS1} Value Used in Calculations	$R_{VS1} =$	90.9	k Ω	
Resultant Turn On Voltage, Minimum	$V_{TURNONmin} =$	56.483	VAC	
Resultant Turn On Voltage, Nominal	$V_{TURNONnom} =$	66.887	VAC	
Resultant Turn On Voltage, Maximum	$V_{TURNONmax} =$	81.751	VAC	
Resultant Input Brown Out Voltage, Minimum	$V_{BROWNOUTmin} =$	34.161	VAC	
Resultant Input Brown Out Voltage, Nominal	$V_{BROWNOUTnom} =$	37.134	VAC	
Resultant Input Brown Out Voltage, Maximum	$V_{BROWNOUTmax} =$	43.079	VAC	
Internal VS Over Voltage Threshold, Minimum	$V_{OVPmin} =$	4.52	V	Device Parameter
Internal VS Over Voltage Threshold, Nominal	$V_{OVPhom} =$	4.600	V	Device Parameter
Internal VS Over Voltage Threshold, Maximum	$V_{OVPmax} =$	4.710	V	Device Parameter
Recommended Resistor Value for Desired Output Over Voltage Limit	$R_{VS2recommended} =$	20.500	k Ω	
Actual Resistor Value Used for Desired Output Over Voltage Limit	$R_{VS2actual} =$	20.500	k Ω	User Input
R_{VS2} Used in Calculations	$R_{VS2} =$	20.500	k Ω	
Resultant Output Over Voltage Threshold, Minimum	$V_{OUT_OVpmin} =$	87.246	V	
Resultant Output Over Voltage Threshold, Nominal	$V_{OUT_OVpnom} =$	88.768	V	Actual Output Over Voltage
Resultant Output Over Voltage Threshold, Maximum	$V_{OUT_OVpmax} =$	90.861	V	

LINE COMPENSATION, R_{LC}			
Line Compensation Current Ratio, Nominal	$K_{LCnom} =$	25	A/A Device Parameter
Total Estimated Current Sense Delay	$t_{DELAY} =$	90	ns
Recommended Resistor Value for Line Compensation	$R_{LCrecommended} =$	1.400	k Ω
Actual Resistor Value Used for Line Compensation	$R_{LCactual} =$	1.400	k Ω User Input
R_{LC} Used in Calculations	$R_{LC} =$	1.400	k Ω
Result of R_{LC} selection	Output Constant Current will have minimal deviation over input line voltage range.		

VDD CAPACITOR, C_{VDD}			
Device Supply Current During Run Mode, Maximum	$I_{RUNmax} =$	2.65	mA Device Parameter
VDD _{ON} Voltage, Maximum	$VDD_{ONmax} =$	23	V Device Parameter
VDD _{OFF} Voltage, Maximum	$VDD_{OFFmax} =$	8.15	V Device Parameter
Estimated Minimum Switching Frequency at No-Load	$f_{SWmin} =$	1.199	kHz
Estimated Over Voltage Charge Duration	$t_{OV} =$	20.000	ms
Minimum VDD Capacitor for Start UP	$C_{VDD1} =$	10.000	μ F
Minimum VDD Capacitor for Load Transient	$C_{VDD2} =$	4.700	μ F
Minimum VDD Capacitor for Target Ripple on VDD	$C_{VDD3} =$	2.200	μ F
Recommended Capacitor on VDD	$C_{VDDrecommended} =$	10.000	μ F

OPTO-COUPLED FEEDBACK			
Reference Voltage of TL431 Shunt Regulator	$VREF_{431} =$	2.5	V User Input
Shunt Regulator Reference Input Current, Maximum	$I_{REF431} =$	4	μ A User Input
Recommended Bottom Resistor Value for Output Voltage Set Point	$R_{FB2recommended} =$	44.2	k Ω
Actual Bottom Resistor Value Used for Output Voltage Set Point	$R_{FB2actual} =$	44.2	k Ω User Input
R_{FB2} Used in Calculations	$R_{FB2} =$	44.2	k Ω
Recommended Top Resistor Value for Output Voltage Set Point	$R_{FB1recommended} =$	1470	k Ω
Actual Top Resistor Value Used for Output Voltage Set Point	$R_{FB1actual} =$	1470	k Ω User Input
R_{FB1} Used in Calculations	$R_{FB1} =$	1470.02	k Ω
Noise Injection Resistor For Loop Analysis	$R_{INJ} =$	20	Ω May be changed by User here
Resultant Nominal Constant Voltage Output Voltage	$V_{OUT_{cv}} =$	85.646	V
Minimum Current Transfer Ratio of Selected Opto-Coupler	$CTR_{min} =$	50	% User Input
Response Fall Time of Opto-Coupler	$t_{f_{opto}} =$	3	μ s User Input
R_L of Specified Opto-Coupler Fall Time	$R_{L_{opto}} =$	100	Ω User Input
Cut-Off Frequency of Opto-Coupler	$f_{c_{opto}} =$	80	kHz User Input
Input Forward Voltage of Opto-Coupler	$V_{F_{opto}} =$	1.4	V User Input
Equivalent Opto-Coupler Output Capacitance	$C_{OPTO} =$	4.775	nF
Equivalent Internal UCC28740 Dynamic Resistance	$R_{EQU} =$	40	k Ω
Recommended Value for External Capacitor on Opto-Coupler	$C_{EXTrecommended} =$	0	μ F
Actual Value of External Capacitor on Opto-Coupler Used	$C_{EXTactual} =$	0.0015	μ F User Input
C_{EXT} Used in Calculations	$C_{EXT} =$	0.0015	μ F
Recommended C_{FB}	$C_{FBrecommended} =$	0.047	μ F
Actual C_{FB} Used	$C_{FBactual} =$	0.047	μ F User Input
C_{FB} Used in Calculations	$C_{FB} =$	0.047	
Recommended Value For R_{FB4}	$R_{FB4recommended} =$	22	k Ω
Actual Value for R_{FB4} Used	$R_{FB4actual} =$	22	k Ω User Input
R_{FB4} Used in Calculations	$R_{FB4} =$	22	k Ω
Opto-Coupler Emitter Current to FB Pin Current Gain	$G_{FB1} =$	0.355	
FB Pin Current to Control Law Voltage Gain, Full Load	$G_{FB2} =$	-192	k Ω
Control Law Voltage to Power Stage Modulation Gain, FM Mode	$K_{FM4} =$	50.4	kHz/V
Power Stage Modulation (FM) to Average Current Gain	$G_{P4} =$	6.402	μ C
Recommended Value for Shunt Regulator Bias Resistor	$R_{TLrecommended} =$	1.5	k Ω
Actual Value of Shunt Regulator Bias Resistor Used	$R_{TLactual} =$	1.5	k Ω User Input
R_{TL} Used in Calculations	$R_{TL} =$	1.5	k Ω
Recommended Value for Compensation Capacitor	$C_{Zrecommended} =$	220	pF
Actual Value Used C_Z	$C_{Zactual} =$	1500	pF User Input
C_Z Used in Calculations	$C_Z =$	1500	pF

Open Loop Gain, Phase Plot

