

15-02-24.

GPIO - have a memory space and address, that memory space is called registers.

GPIOA → 0x4002 0000 — 0x4002 03FF

All registers for GPIOA have been inside different address

GPIOA

Registers

→ MODER → ICKR

→ OTYPER → AFRR

→ OSPEEDR

→ PUPDR

→ IDR

→ ODR

→ BSRR

→ LCKR

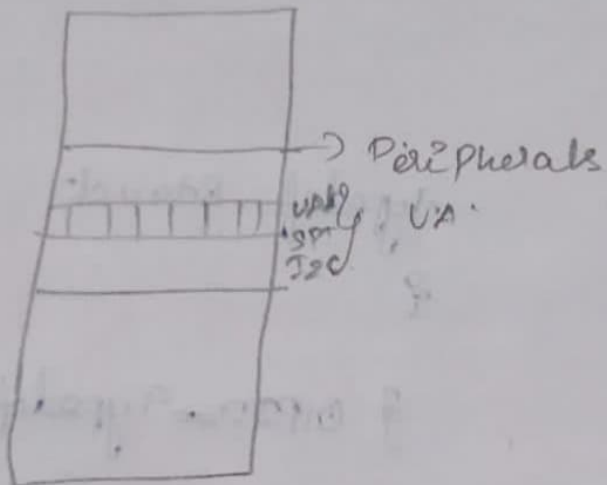
→ AFR

space but only one registers.

When we know the Base and offset we will know where the starting address is

i) $\text{Base} - \text{Starting Address} = \text{offset}$

ii) $\text{Base} + \text{offset} = \text{Starting address.}$



EX

Base = 20

offset = 20

Start address = 40

Start
from.

0x00000000