08.09.24

**ARM Architecture :**

An architecture specifies how a
processor will behave.

* Instruction set
* Register set
* Exception model
* Memory model.
* Debug, Trace and profiling.

**RISC :-**

Reduced Instruction set computer
Load store Architecture

**Micro architecture:**

build and design of a processor is
refer to as micro architecture.

**IP core :**

Based on this ARM Specification, ARM
develops IP core which implements the specification

Implementation in VHDL or Verilog of
a block of hardware logic.

## Processor Family:

ARM processor families group multiple processors.

Starting with Arm1 (1985) up to Arm 11 (2000.)

Naming scheme then changed with cortex family introduced in 2005,

Cortex { letter }{ number }

New family introduced new processors, with an improved design, better performance and new feature.

- .c → source file
- .P → process
- .s → assembly
- .o → object