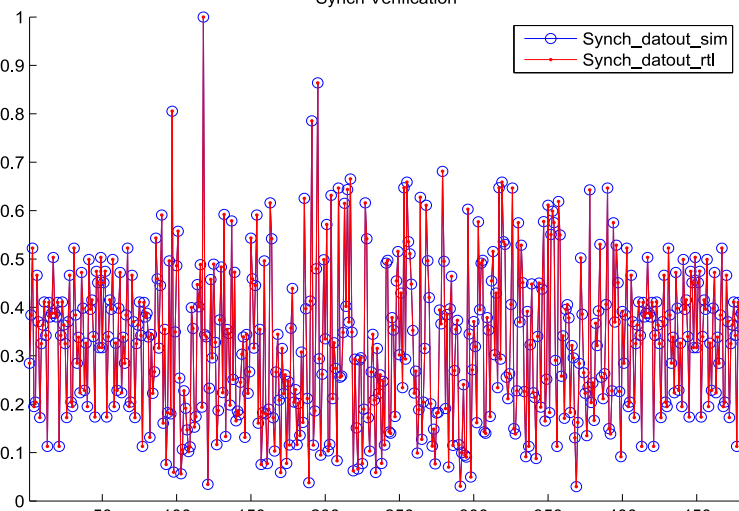
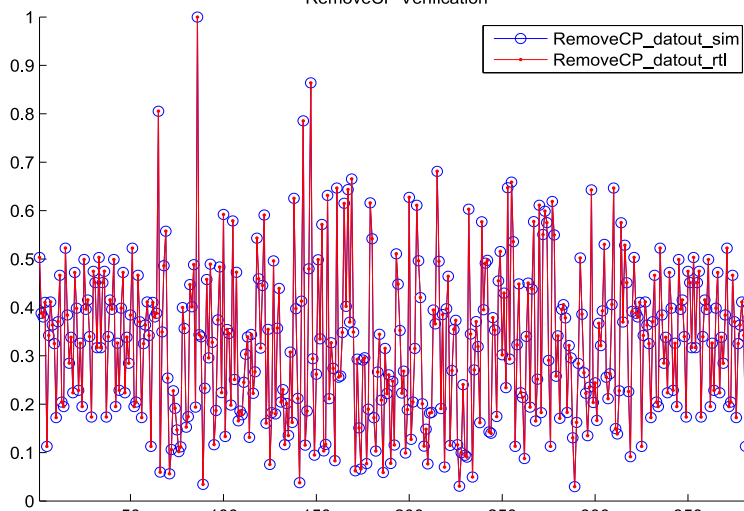


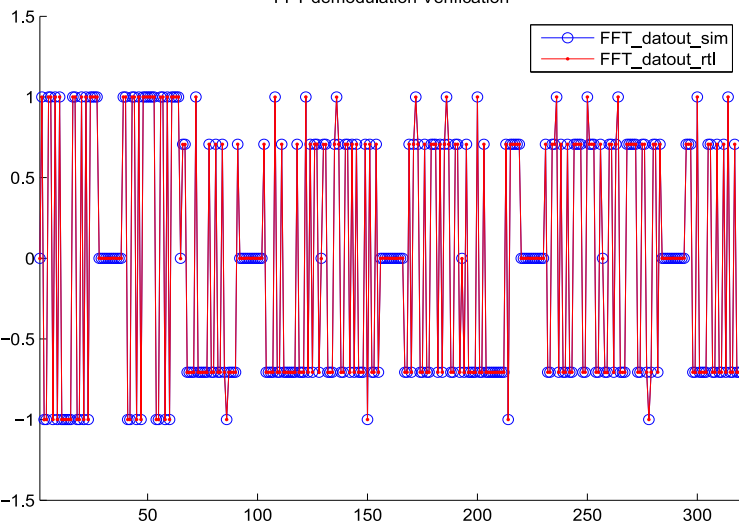
Synch Verification



RemoveCP Verification



FFT demodulation Verification



IFO_Est & Ch_EstEqu Verification

