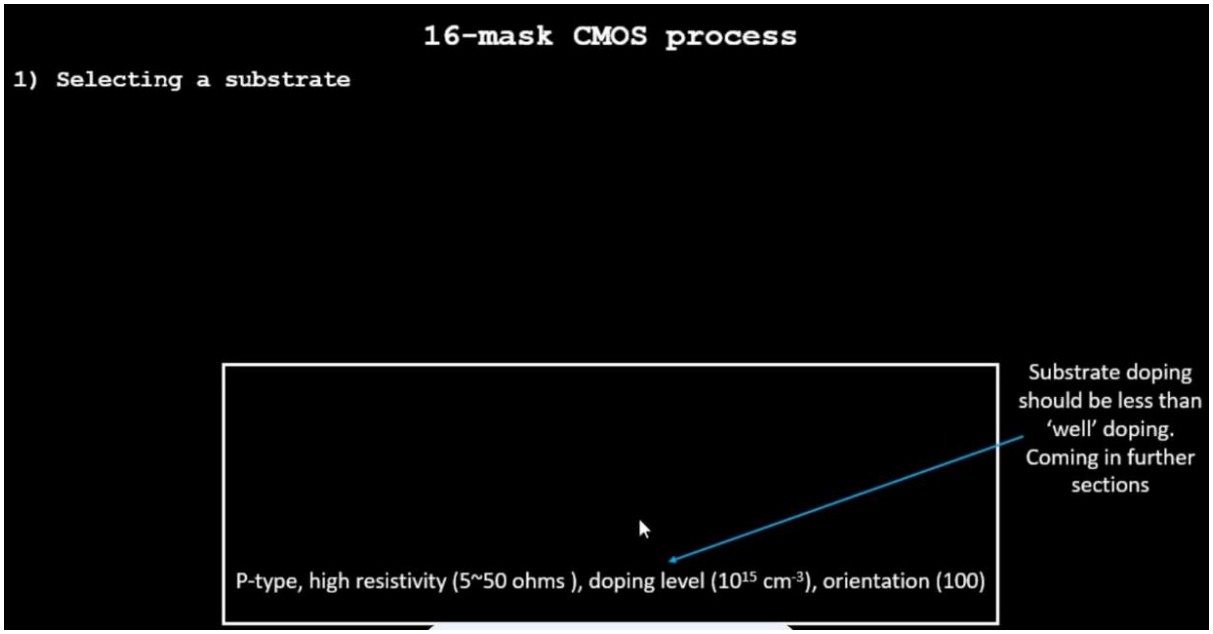
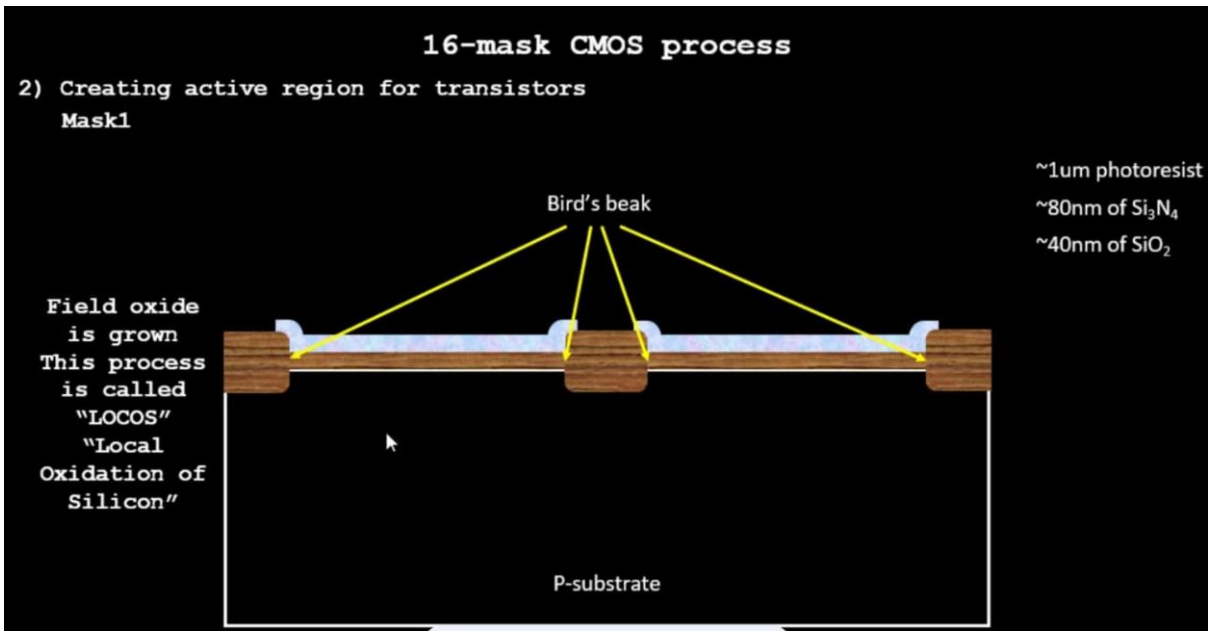
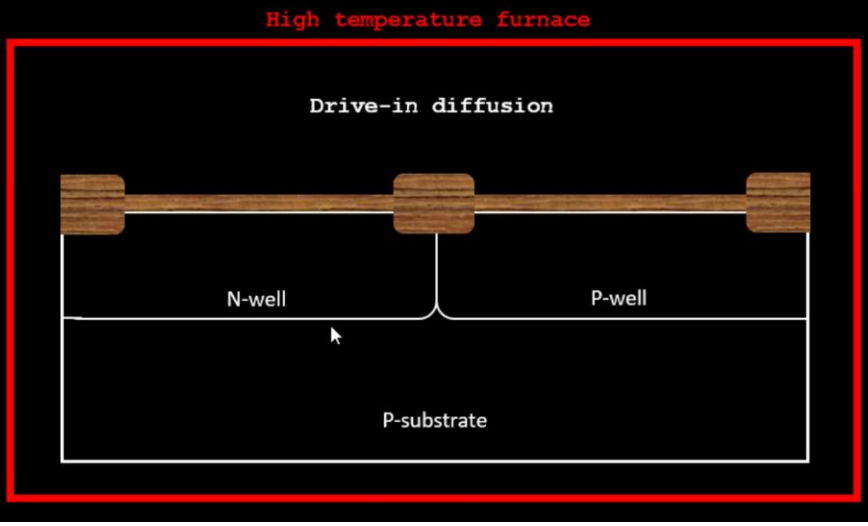
CHIP FABRICATION PROCESS





16-mask CMOS process

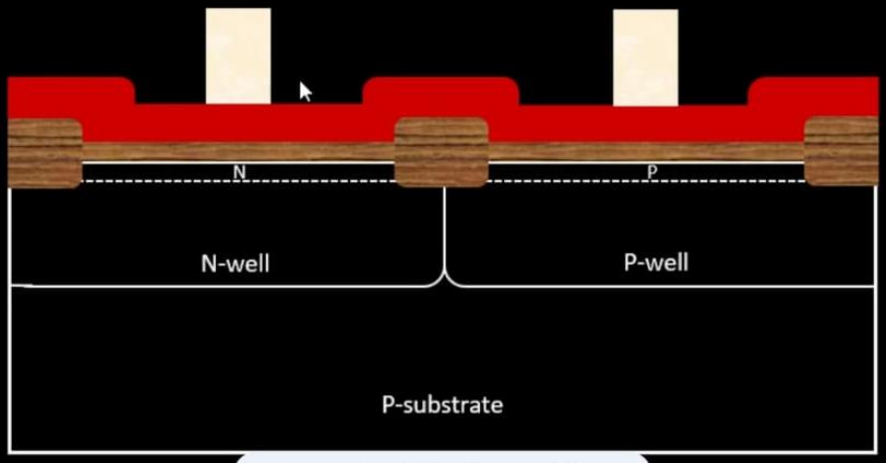
3) N-Well and P-Well formation



udemy

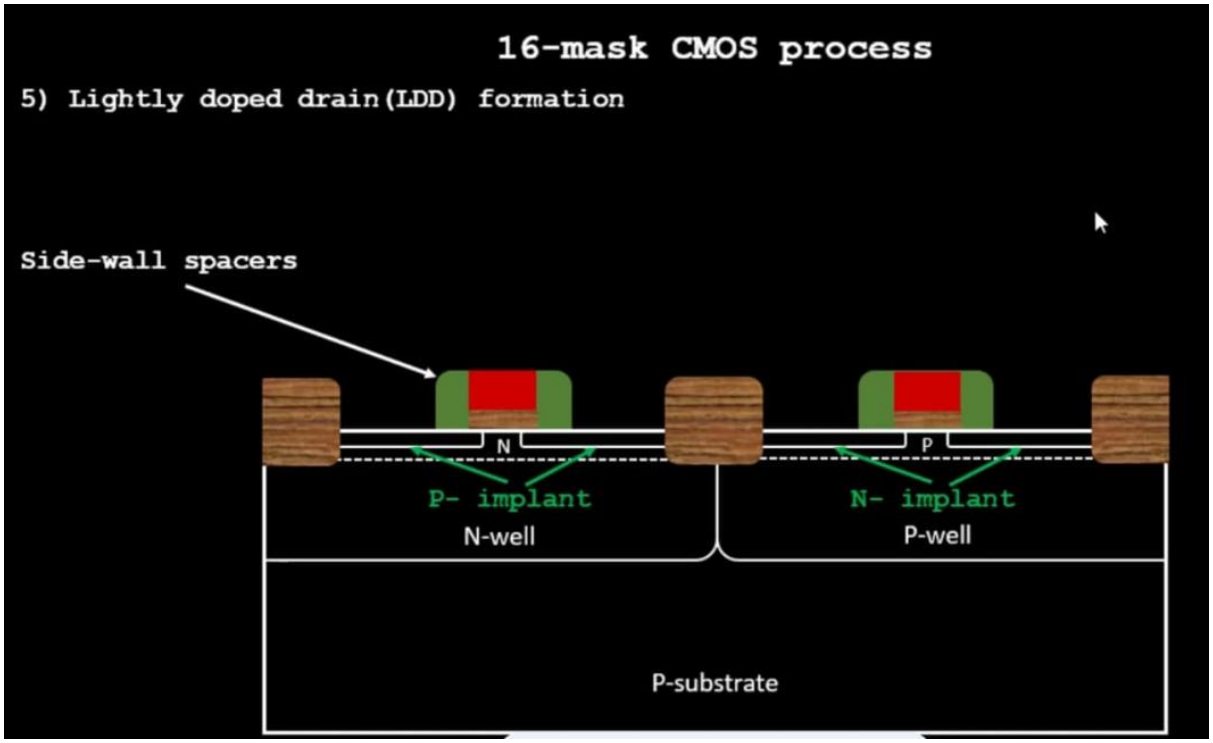
16-mask CMOS process

4) Formation of 'gate'



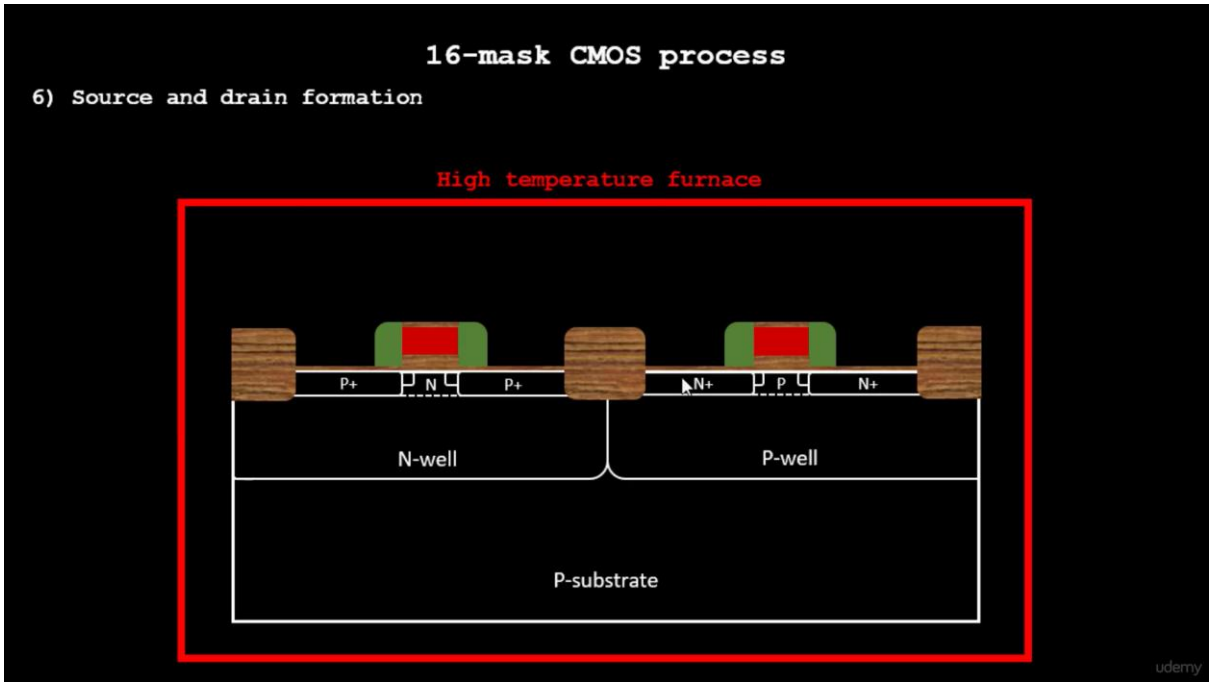
16-mask CMOS process

5) Lightly doped drain(LDD) formation

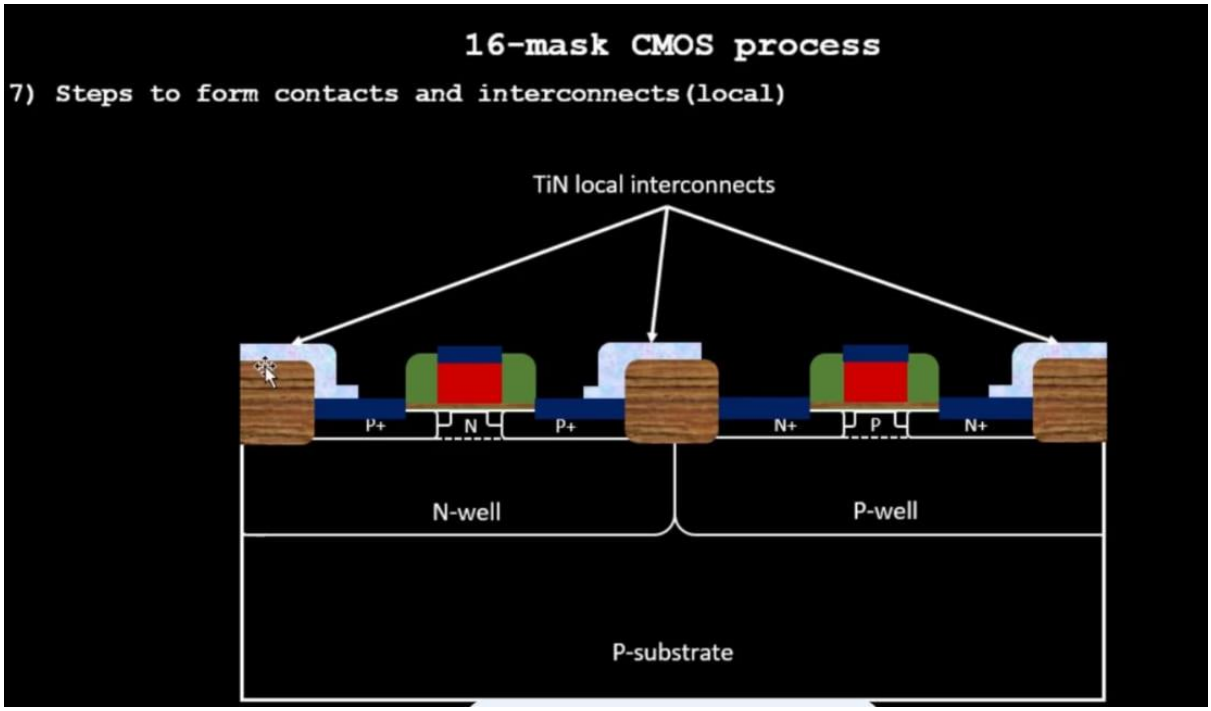


16-mask CMOS process

6) Source and drain formation



udemy



FINAL FABRICATED CHIP