

```
#include <stdio.h>

int main() {
    int i, sum=0, n=7;
    for(i=1; i<=n; ++i){
        sum += i;
    }
    printf("sum of numbers from 1 to %d is %d\n",n,sum);
    return 0;
}
```



```

1016c: 7a018593      addl    a1,gp,1952 # 231a8 <object.5473>
10170: 00012517      aulpc   a0,0x12
10174: e9050513      addl    a0,a0,-368 # 22000 <__FRAME_END__>
10178: 00000317      aulpc   t1,0x0
1017c: 00000067      jr      zero # 0 <register_fini-0x100b6>
10180: 00008067      ret

000000000010184 <main>:
10184: fe010113      addl    sp,sp,-32
10188: 00113c23      sd      ra,24(sp)
1018c: 00813823      sd      s0,16(sp)
10190: 02010413      addl    s0,sp,32
10194: fe042423      sw      zero,-24(s0)
10198: 00700793      li      a5,7
1019c: fef42223      sw      a5,-28(s0)
101a0: 00100793      li      a5,1
101a4: fef42623      sw      a5,-20(s0)
101a8: 0200006f      j      101c8 <main+0x44>
101ac: fe842703      lw      a4,-24(s0)
101b0: fec42703      lw      a5,-20(s0)
101b4: 00f707bb      addw    a5,a4,a5
101b8: fef42423      sw      a5,-24(s0)
101bc: fec42703      lw      a5,-20(s0)
101c0: 0017879b      addiw   a5,a5,1
101c4: fef42623      sw      a5,-20(s0)
101c8: fec42703      lw      a4,-20(s0)
101cc: fe442703      lw      a5,-28(s0)
101d0: 0007071b      sext.w  a4,a4
101d4: 0007879b      sext.w  a5,a5
101d8: fce7dae3      bge     a5,a4,101ac <main+0x28>
101dc: fe842703      lw      a4,-24(s0)
101e0: fe442703      lw      a5,-28(s0)
101e4: 00070613      mv      a2,a4
101e8: 00078593      mv      a1,a5
101ec: 000217b7      lui     a5,0x21
101f0: 1e078513      addl    a0,a5,480 # 211e0 <__clzdi2+0x48>
101f4: 274000ef      jal     ra,10468 <printf>
101f8: 00000793      li      a5,0
101fc: 00078513      mv      a0,a5
10200: 01813083      ld      ra,24(sp)
10204: 01013403      ld      s0,16(sp)
10208: 02010113      addl    sp,sp,32
1020c: 00008067      ret

000000000010210 <atexit>:
10210: 00050593      mv      a1,a0

```



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&

[1] 3758

vsduser@vsduser-VirtualBox:~\$ Gtk-Message: 22:47:01.755: Failed to load module "canberra-gtk-module"

[1]+ Done leafpad sum1ton.c

vsduser@vsduser-VirtualBox:~\$ gcc sum1ton.c

vsduser@vsduser-VirtualBox:~\$ ls -ltr

total 64

-rw-r--r--	1	vsduser	vsduser	8980	Sep 18 2023	examples.desktop
drwxr-xr-x	2	vsduser	vsduser	4096	Sep 18 2023	Videos
drwxr-xr-x	2	vsduser	vsduser	4096	Sep 18 2023	Templates
drwxr-xr-x	2	vsduser	vsduser	4096	Sep 18 2023	Public
drwxr-xr-x	2	vsduser	vsduser	4096	Sep 18 2023	Pictures
drwxr-xr-x	2	vsduser	vsduser	4096	Sep 18 2023	Music
drwxr-xr-x	2	vsduser	vsduser	4096	Sep 18 2023	Downloads
drwxr-xr-x	2	vsduser	vsduser	4096	Sep 18 2023	Documents
drwxr-xr-x	2	vsduser	vsduser	4096	Sep 18 2023	Desktop
drwxrwxr-x	8	vsduser	vsduser	4096	Sep 18 2023	riscv_workshop_collaterals
-rw-rw-r--	1	vsduser	vsduser	150	Jan 8 22:47	sum1ton.c
-rwxrwxr-x	1	vsduser	vsduser	8304	Jan 8 22:47	a.out

vsduser@vsduser-VirtualBox:~\$./a.out

sum of numbers from 1 to 7 is 28

vsduser@vsduser-VirtualBox:~\$

```

addi    a0,a0,-368 # 22000 <__FRAME_END__>
auipc   t1,0x0
jr      zero # 0 <ra>
ret

addi    sp,sp,-32
sd      ra,24(sp)
sd      s0,16(sp)
addi    s0,sp,32
sw      zero,-24(s0)
li      a5,7
sw      a5,-28(s0)
li      a5,1
sw      a5,-20(s0)
j       101c8 <main>
lw      a4,-24(s0)
lw      a5,-20(s0)
addw    a5,a4,a5
sw      a5,-24(s0)
lw      a5,-20(s0)
addiw   a5,a5,1
sw      a5,-20(s0)
lw      a4,-20(s0)
lw      a5,-28(s0)
sxt.w   a4,a4
sxt.w   a5,a5
bge     a5,a4,101ac
lw      a4,-24(s0)
lw      a5,-28(s0)
mv      a2,a4
mv      a1,a5
lui     a5,0x21
addi    a0,a5,480 #
jal     ra,10468 <printf>
li      a5,0
mv      a0,a5
ld      ra,24(sp)
ld      s0,16(sp)
addi    s0,s0,32

```

Calculator

Programmer

1020C + 4 =
1 0210

HEX	1 0210
DEC	66,064
OCT	201 020
BIN	0001 0000 0010 0001 0000

QWORD MS Mv

Bitwise Bit shift

A	<<	>>	CE	ⓧ
B	()	%	÷
C	7	8	9	×
D	4	5	6	-
E	1	2	3	+
F	+/-	0	.	=

```
autpc    li,0x0  
jr        zero # 0 <ra  
ret
```

```
addi      sp,sp,-32  
sd        ra,24(sp)  
sd        s0,16(sp)  
addi      s0,sp,32  
sw        zero,-24(s0)  
li        a5,7  
sw        a5,-28(s0)  
li        a5,1  
sw        a5,-20(s0)  
j         101c8 <main  
lw        a4,-24(s0)  
lw        a5,-20(s0)  
addw      a5,a4,a5  
sw        a5,-24(s0)  
lw        a5,-20(s0)  
addlw     a5,a5,1  
sw        a5,-20(s0)  
lw        a4,-20(s0)  
lw        a5,-28(s0)  
sext.w    a4,a4  
sext.w    a5,a5  
bge      a5,a4,101ac  
lw        a4,-24(s0)  
lw        a5,-28(s0)  
mv        a2,a4  
mv        a1,a5  
lui       a5,0x21  
addi      a0,a5,480 #  
jal       ra,10468 <p  
li        a5,0  
mv        a0,a5  
ld        ra,24(sp)  
ld        s0,16(sp)  
addi      sp,sp,32  
ret
```

```
mv        a1,a0
```



Calculator

Programmer

23

HEX 23
DEC 35
OCT 43
BIN 0010 0011



QWORD

MS

Mv

Bitwise ▾

Bit shift ▾

A

<<

>>

CE



B

(

)

%

÷

C

7

8

9

×

D

4

5

6

-

E

1

2

3

+

F

+/-

0

.

=



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