TRIBHUVAN UNIVERSITY
INSTITUTE OF ENGINEERING
THAPATHALI CAMPUS

A LaTeX Template
For
IOE Thapathali Electronics Project Report Format

Submitted By:

Abhinav Chalise	THA0XXBEI0XX
Student Name	THA0XXBEI0XX
Nom d'étudiant	THA0XXBEI0XX
Nombre del estudiante	THA0XXBEI0XX

Submitted To:

Department of Electronics and Computer Engineering
Thapathali Campus
Kathmandu, Nepal

January 2025

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Submitted To:
Department of Electronics and Computer Engineering
Thapathali Campus
Kathmandu, Nepal

In partial fulfillment for the award of the Bachelor of Engineering in Electronics, Communication and Information.

Under the Supervision of
Mr. <Supervisor Name>

January 2025

DECLARATION

We hereby declare that the report of the project entitled **"IOE Thapathali Electronics Project Report Format"** which is being submitted to the **Department of Electronics and Computer Engineering, IOE, Thapathali Campus**, in the partial fulfillment of the requirements for the award of the Degree of Bachelor of Engineering in **Electronics, Communication and Information**, is a bonafide report of the work carried out by us. The materials contained in this report have not been submitted to any university or institution for the award of any degree and we are the only author of this complete work and no sources other than the listed here have been used in this work.

Abhinav Chalise (Class Roll No: THA0XXBEI0XX) _____

Student Name (Class Roll No: THA0XXBEI0XX) _____

Nom d'étudiant (Class Roll No: THA0XXBEI0XX) _____

Nombre del estudiante (Class Roll No: THA0XXBEI0XX) _____

Date: January 2025

CERTIFICATE OF APPROVAL

The undersigned certify that they have read and recommended to the **Department of Electronics and Computer Engineering, IOE, Thapathali Campus**, a major project work titled **"IOE Thapathali Electronics Project Report Format"** submitted by **Abhinav Chalise, Student Name, Nom d'étudiant, Nombre del estudiante** in partial fulfillment for the award of Bachelor of Engineering in Electronics, Communication and Information. The project was carried out under special supervision and within the time frame prescribed by the syllabus.

We found the students to be hardworking, skilled and ready to undertake any related work to their field of study and hence we recommend the award of partial fulfillment of Bachelor of Engineering in Electronics, Communication and Information.

Project Supervisor
Mr. <Supervisor Name>
Department of Electronics and Computer Engineering, Thapathali Campus

---

External Examiner
Prof. Dr. <Name of External>
Department of Electronics and Computer Engineering, Pulchowk Campus

---

Project Co-ordinator
Er. <Name of Co-ordinator>
Department of Electronics and Computer Engineering, Thapathali Campus

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Head of the Department
Er. <Name of HOD>
Department of Electronics and Computer Engineering, Thapathali Campus

January 2025

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ABSTRACT

Lorem ipsum dolor sit amet, consectetuer adipiscing elit. Ut purus elit, vestibulum ut, placerat ac, adipiscing vitae, felis. Curabitur dictum gravida mauris. Nam arcu libero, nonummy eget, consectetuer id, vulputate a, magna. Donec vehicula augue eu neque. Pellentesque habitant morbi tristique senectus et netus et malesuada fames ac turpis egestas. Mauris ut leo. Cras viverra metus rhoncus sem. Nulla et lectus vestibulum urna fringilla ultrices. Phasellus eu tellus sit amet tortor gravida placerat. Integer sapien est, iaculis in, pretium quis, viverra ac, nunc. Praesent eget sem vel leo ultrices bibendum. Aenean faucibus. Morbi dolor nulla, malesuada eu, pulvinar at, mollis ac, nulla. Curabitur auctor semper nulla. Donec varius orci eget risus. Duis nibh mi, congue eu, accumsan eleifend, sagittis quis, diam. Duis eget orci sit amet orci dignissim rutrum.

Keywords: Aenean, Cras, Morbi, Turpis . . . in alphabetical order

Table of Contents

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List of Abbreviations

IOE Institute of Engineering

TU Tribhuvan University

1 INTRODUCTION

We use \gls{tu} as defined in *acronym.tex* to insert acronyms like Tribhuvan University (TU) and Institute of Engineering (IOE). This will auto-insert used acronyms in List of Abbreviations. The first instance of the abbreviation will show its full form with short form inside braces but further instances like TU will show short form only.

1.1 Background

We use \cite{einstein1905} as defined in *refs.bib* to cite works [1]. This will automatically insert used bibliography at the references section. You'll need to insert BibTeX in *refs.bib* for this to work.

1.2 Motivation

Use \autoref{label-name} to reference figures, tables, sections, equations and so on like Figure 4-1, Table 5-1, section 8 and Equation 5-1.

1.3 Problem Definition

Your Problem Definition here.

1.4 Objectives

The main objectives of our project are listed below *(maximum 2 points and to the point)*:

- Objective 1
- Objective 2

1.5 Project Scope and Applications

Your Project Scope and applications here.

1.6 Report Organization

Briefly explains all the chapters and their focus.

2 LITERATURE REVIEW

This chapter contains all the existing works that have already been carried out in the field related to your project topic. This chapters tells how much you researched before completing your project. You have to explain each of the works as a separate sub-topic with following details:

i. *What is the work of existing/researched related topic?*

ii. *How it is done? used methods, techniques, technology, algorithms and any new innovations of existing/researched related topic)*

iii. *Its importance or applications existing/researched related topic*

iv. *Drawbacks and limitations existing/researched related topic*

v. *Criticize the work of existing/researched related topic*

vi. *Link these criticisms on the existing/researched related topic to the motivation explained in previous chapter.*

vii. *Each information should be properly cited.*

Morbi luctus, wisi viverra faucibus pretium, nibh est placerat odio, nec commodo wisi enim eget quam. Quisque libero justo, consectetuer a, feugiat vitae, porttitor eu, libero. Suspendisse sed mauris vitae elit sollicitudin malesuada. Maecenas ultricies eros sit amet ante. Ut venenatis velit. Maecenas sed mi eget dui varius euismod. Phasellus aliquet volutpat odio. Vestibulum ante ipsum primis in faucibus orci luctus et ultrices posuere cubilia Curae; Pellentesque sit amet pede ac sem eleifend consectetuer. Nullam elementum, urna vel imperdiet sodales, elit ipsum pharetra ligula, ac pretium ante justo a nulla. Curabitur tristique arcu eu metus. Vestibulum lectus. Proin mauris. Proin eu nunc eu urna hendrerit faucibus. Aliquam auctor, pede consequat laoreet varius, eros tellus scelerisque quam, pellentesque hendrerit ipsum dolor sed augue. Nulla nec lacus. [1]

3 REQUIREMENT ANALYSIS

Describe the why and where in your project these requirements are needed.

3.1 Dataset Analysis

Description

3.2 Hardware/Software Requirements

Description

3.2.1 SubSubsection 1

Description

3.2.2 Subsubsection 2

Description

4 SYSTEM ARCHITECTURE AND METHODOLOGY

Explain system block diagram, flowcharts and other methodologies for your project.

Inserting figure *Keep it centered*:

Figure 4-1: Block Diagram of ABC

4.1 Block Diagram

(Explain all the building blocks of your system in details explaining what and how it does the things).

4.2 Flowcharts

Descriptions

5 IMPLEMENTATION DETAILS

Describe how the hardware components / instruments & software function in your project. Describe the calibration process required for correct operation of each module. Describe the interfacing technicalities / protocol of each module used in your project. Explain in detail how all components are interconnected to make a functioning system.

Inserting table:

Table 5-1: Sample Table

Name	Age	Occupation
John Doe	30	Engineer
Jane Smith	25	Teacher
Mike Johnson	35	Doctor

Inserting equations:

$$x = \frac{-b \pm \sqrt{b^2 - 4ac}}{2a}$$

$$\sum_{n=1}^{\infty} \frac{1}{n^2} = \frac{\pi^2}{6}$$

Inserting numbered equations:

$$\oint_C z\,dz \tag{5-1}$$

6 RESULT AND ANALYSIS

(It contains the results/outputs of your project. The output can be numeric or graphical based. Present the outputs of your project in the form of tables / graphs / charts / figures and explain their behavior. You can also represent or write down the results in tabular form if applicable and analyze that by using graphs or charts. Perform error analysis, comparisons (theory, simulation, practical) and validate your output. Discuss the sources of errors in your project that caused your outputs to deviate from expected values.)

6.1 Subsection 1

Description here

6.1.1 Subsubsection 1

Description here

6.2 Subsection 2

Description here

7 CONCLUSION

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8 APPENDICES

It may contains the additional topics or data sheets or reference sheets or even user manual. Project Budget (Detailed Breakdown of Costs), Project Timeline (Gantt chart), Circuit Diagrams (Should be Clear and Legible), PCB Designs (Should be Clear and Legible), Module Specifications (Should be brief - Keep only necessary tables and figures), Details of Dataset can be included here. The appendix name should be given as follows.

Appendix A: Project Schedule

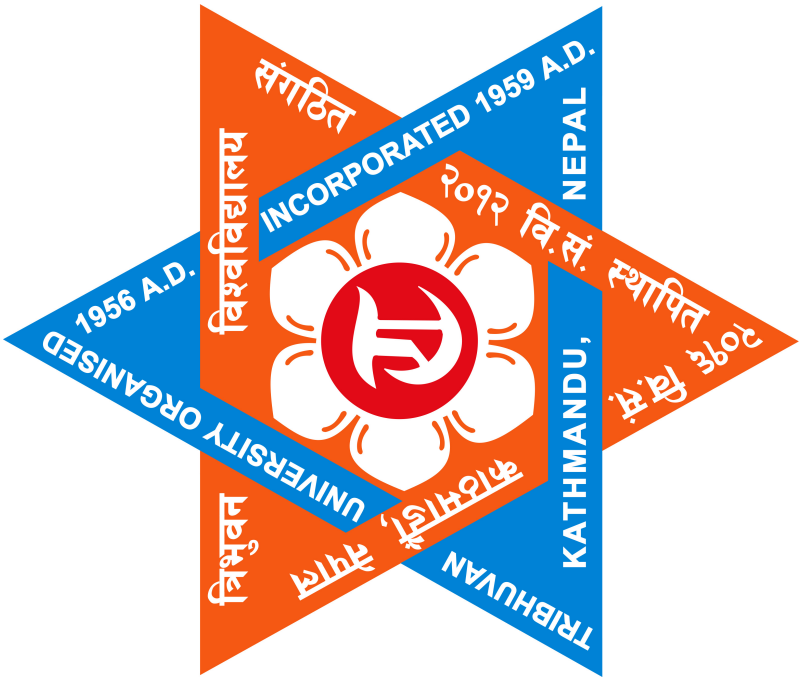

Figure A-1: Gantt Chart

Appendix B: Circuit Diagram

$$F = ma \tag{B-1}$$

References

[1] A. Einstein, “On the electrodynamics of moving bodies,” *Annalen der Physik*, vol. 17, no. 10, pp. 891–921, 1905.