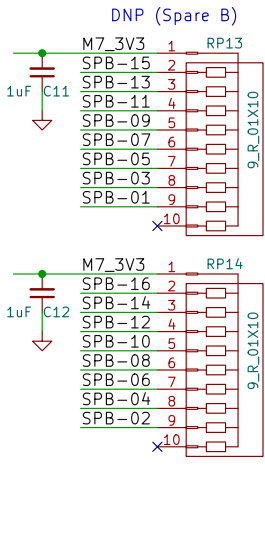
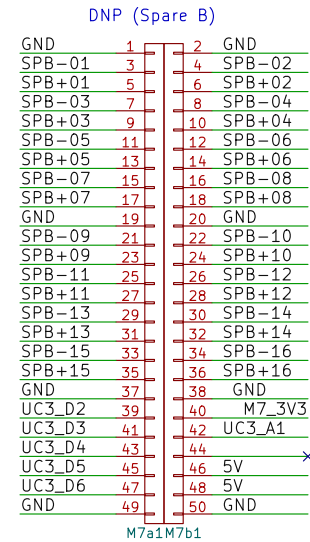
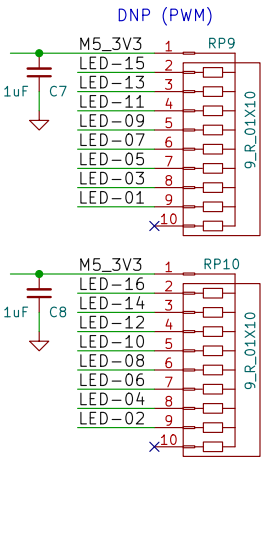
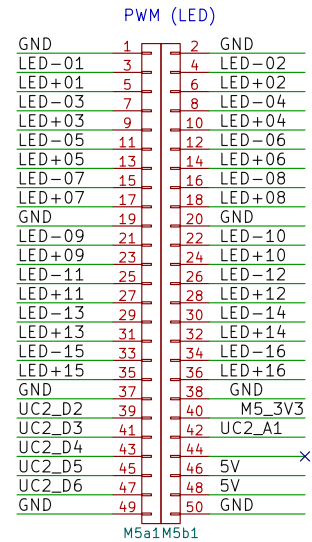
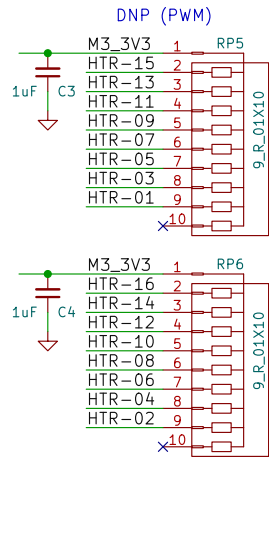
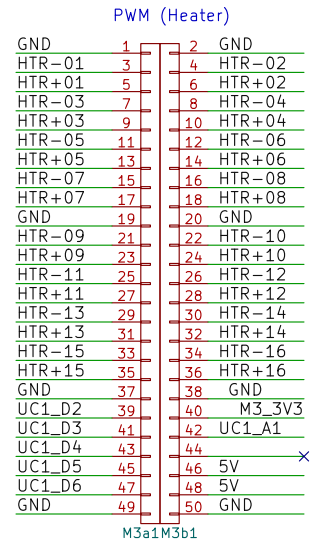
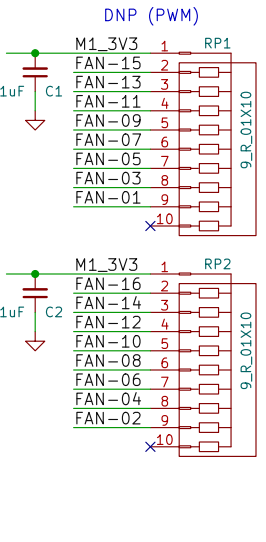
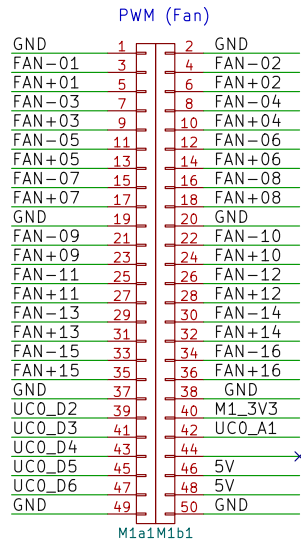
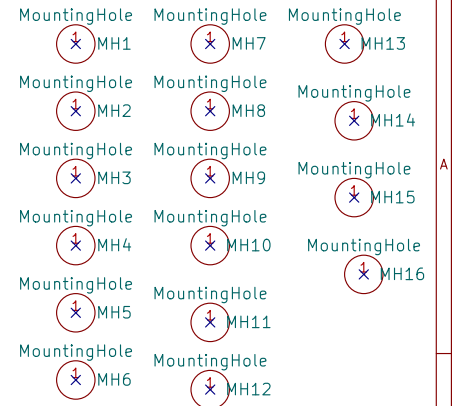
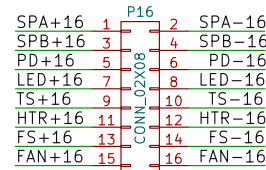
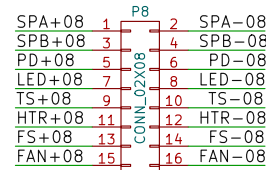
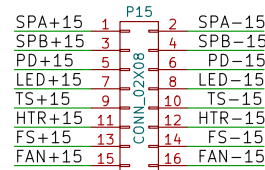
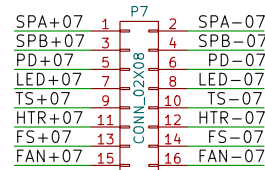
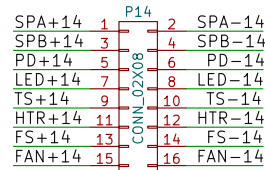
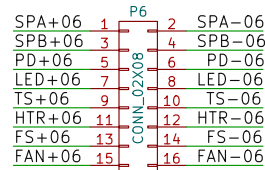
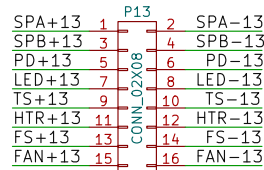
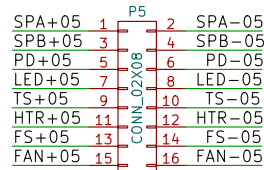
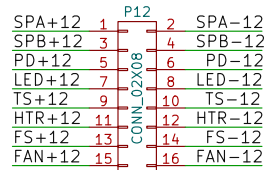
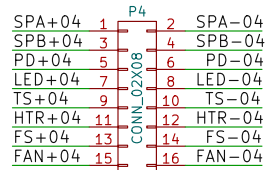
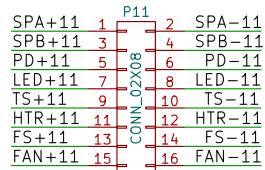
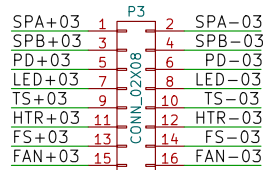
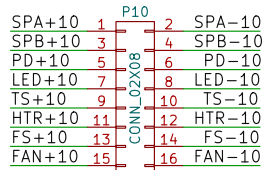
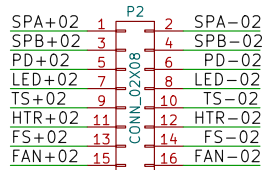
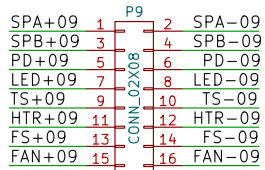
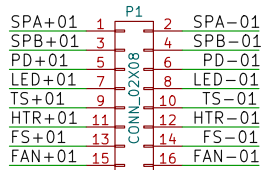
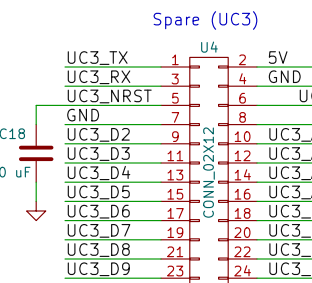
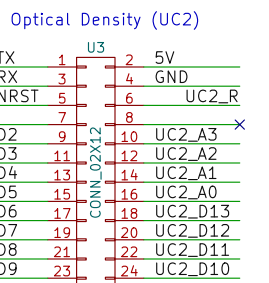
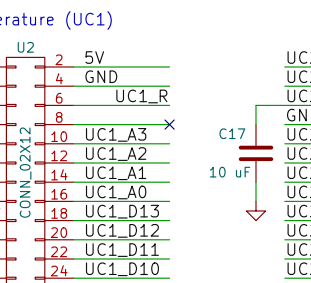
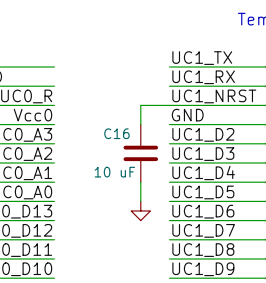
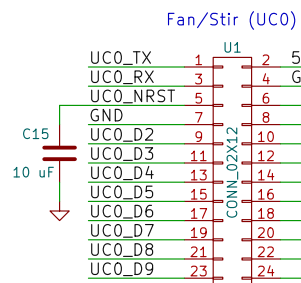
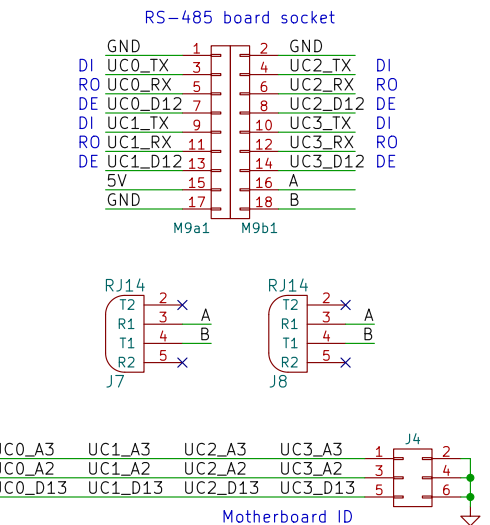
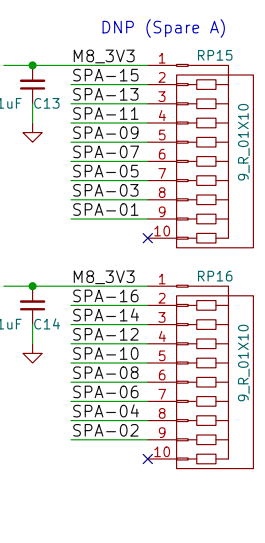
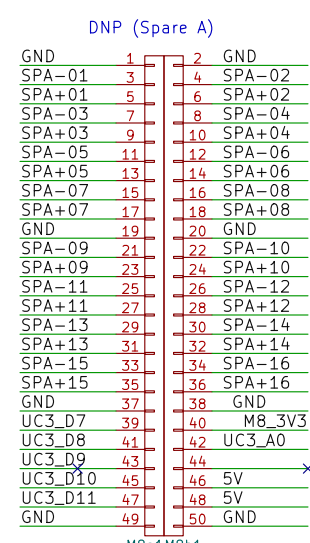
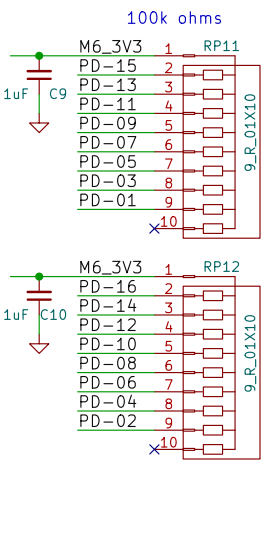
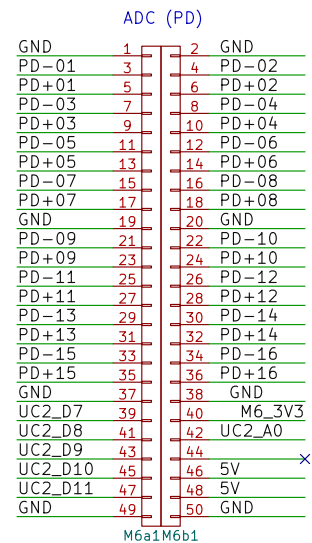
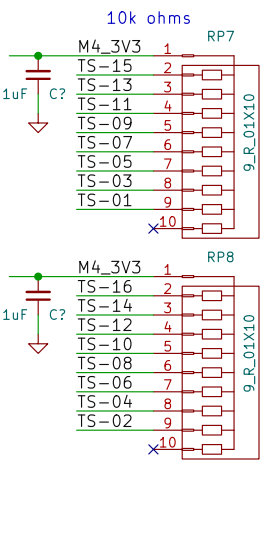
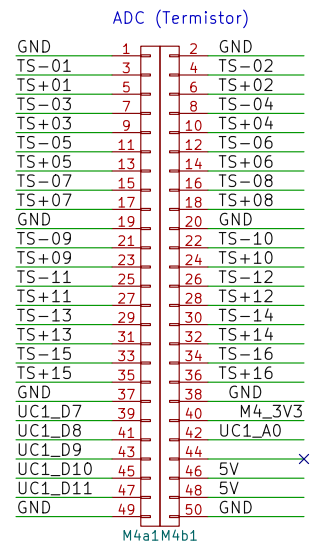
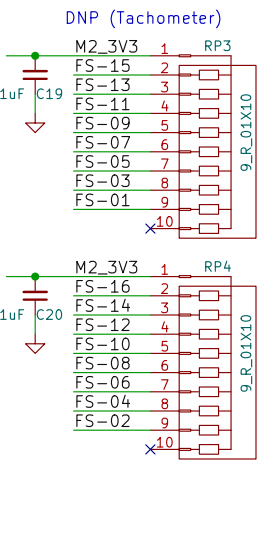
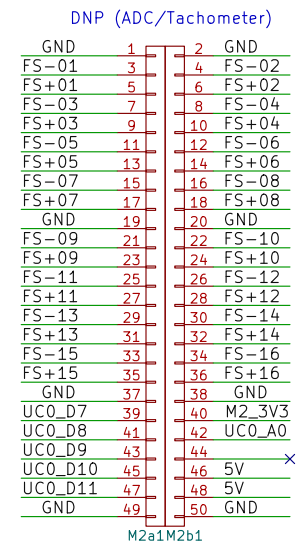


Vial Connectors

Pin 1,2 M8 (Spare A)
Pin 3,4 M7 (Spare B)
Pin 5,6 M6 (Photodiode)
Pin 7,8 M5 (LED)
Pin 9,10 M4 (Termistor)
Pin 11,12 M3 (Heater)
Pin 13,14 M2 (Tachometer)
Pin 15,16 M1 (Fan)

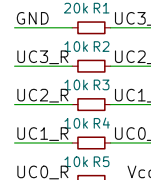


ADC	PWM	CONN	PWM	ADC
OPIN_1	GND	1	GND	GND
OUT_0	3	4	OUT_1	OPIN_4
GND	5	6	+12V	GND
OPIN_2	+12V	7	OUT_3	OPIN_3
GND	9	10	+12V	GND
OPIN_5	OUT_4	11	OUT_5	OPIN_8
GND	+12V	13	+12V	GND
OPIN_6	OUT_6	15	OUT_7	OPIN_7
GND	+12V	17	+12V	GND
GND	GND	19	GND	GND
OPIN_9	OUT_8	21	OUT_9	OPIN_12
GND	+12V	23	+12V	GND
OPIN_10	OUT_10	25	OUT_11	OPIN_11
GND	+12V	27	+12V	GND
OPIN_13	OUT_12	29	OUT_13	OPIN_16
GND	+12V	31	+12V	GND
OPIN_14	OUT_14	33	OUT_15	OPIN_15
GND	+12V	35	+12V	GND
GND	GND	37	GND	GND
M_0	GSCCLK	39	x	+3V3
M_1	XLAT	41	42	BLANK
M_2	SCCLK	43	44	x
M_3	SIN	45	46	+5V
x	SOUT	47	48	+5V
GND	GND	49	50	GND



Note: The uC in this section is only connected to one socket, so many of its pins are not connected.

SAMD21	PWM	ADC
D2 D7	GSCCLK	M_0
D3 D8	XLAT	M_1
D4 D9	SCCLK	M_2
D5 D10	SIN	M_3
D6 D11	SOUT	x
A1 A0	BLANK	M_OUT

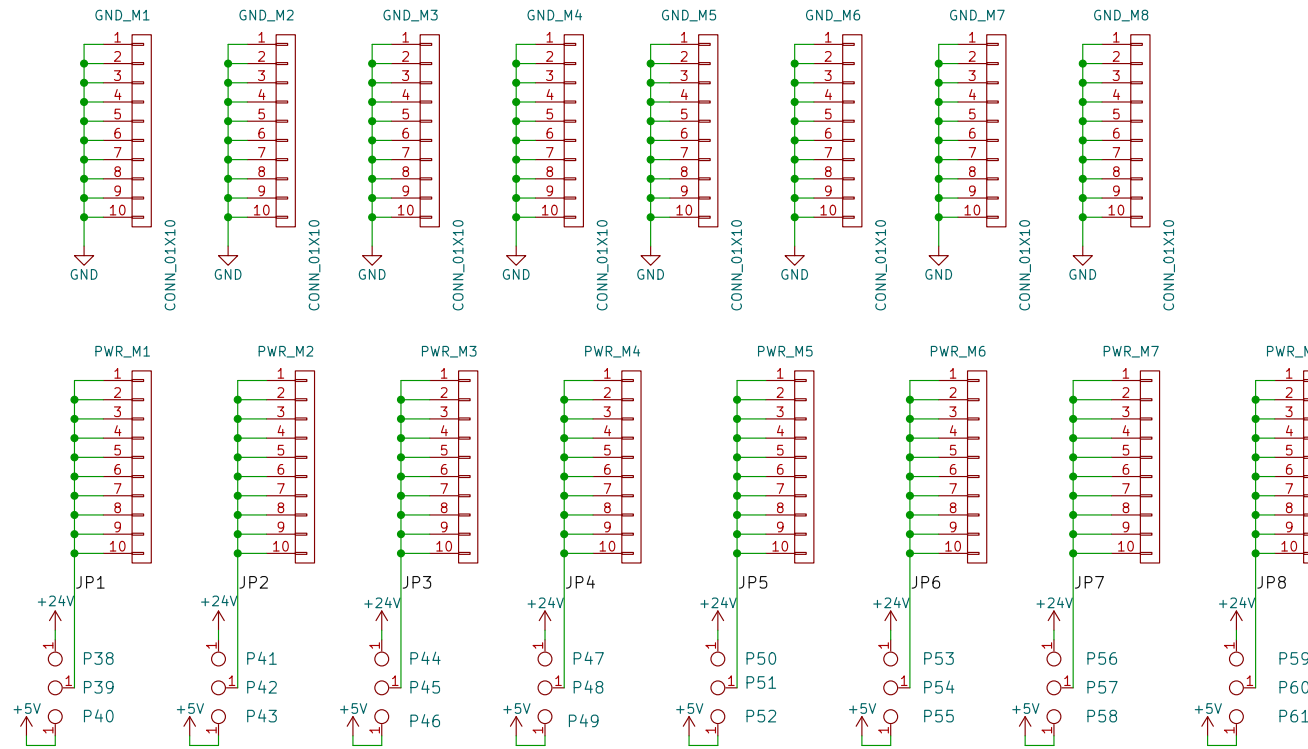


power
POWER

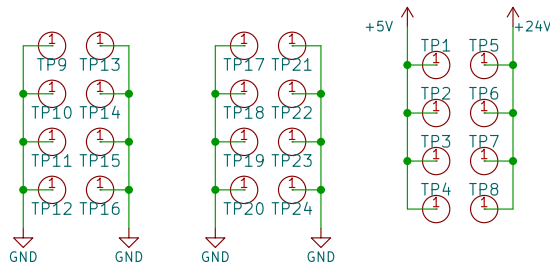
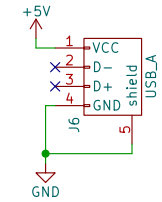
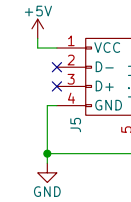
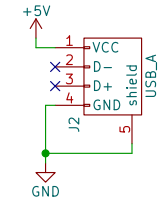
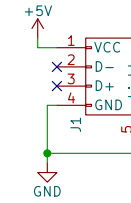
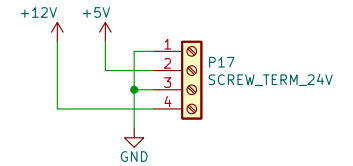
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Sheet 1 of 2	
E. Hazen	
Boston University EDF	
Sheet: /	
File: Motherboard.kicad_sch	
Title: eVOLVER Motherboard	
Size: B	Date: 22 jun 2016
KiCad E.D.A. eeschema 6.0.2+dfsg-1	Rev: A
	Id: 1/2

Extra power for PWM boards
(put on all sites for flexibility)



P38:61 are receptacles that plug into JP1:8;
In turn, parts associated with JP1:8 are not stuffed but placed into P38:61 to jumper either power to the supply of each connector.



Sheet: /power/
File: power.kicad_sch

Title:

Size: A4

Date:

KiCad E.D.A. eeschema 6.0.2+dfsg-1

Rev:

Id: 2/2