

National Research Symposium on Computing - RSC 2016

Jointly organized by
WCE ACM Student Chapter,
Department of Computer Science & Engineering and Department of Information Technology
Walchand College of Engineering, Sangli.



**Collection of Abstracts
Of
National Research Symposium on Computing
(RSC- 2016)
19th and 20th December, 2016**

Editor

Dr. P. J. Kulkarni

Deputy Director & Chair RSC- 2016



Jointly organized by

WCE ACM student chapter

Department of Computer Science and Engineering

&

Department of Information Technology

Walchand College of Engineering

(A Government Aided Autonomous Institute)

Vishrambag, Sangli-416415, Maharashtra, India

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WCE, Sangli

*Collection of Abstracts of National Research Symposium on Computing
(RSC- 2016)*

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PREFACE



Dr. G. V. Parishwad
Director, WCE Sangli



Dr. P. J. Kulkarni
Dy. Director, WCE Sangli

In Higher and Technical Education system, faculty members with higher research qualifications, especially in Information Communication and Technology (ICT) area, have to accept an important challenge of producing more number of researchers who in long run will take up responsibility of educating masses on design and use of modern technological gadgets for betterment of mankind. Over the last two decades, the growth of ICT products has been phenomenal. Rate of obsolescence of these ICT products is also significant. In order to cope with advancement of Engineering and Technology, researchers in the educational institutes need to brain storm, exchange their ideas with peer groups and make their research more fruitful. In line with this theme, Computer Science & Engineering Department and Information Technology Department of Walchand College of Engineering (WCE) proposed for holding of symposium in Computing. With constant proactive efforts of faculty members of these departments, the symposium scheduled in December 2016 has taken up a good shape for its organization. WCE has also been recognized to conduct Ph.D. research programme under Quality Improvement Programme (QIP) of AICTE. Along with this, Technical Education Quality Improvement Programme (TEQIP), being implemented in its phase II at WCE has one of the important components of promoting research culture among the budding engineers and faculty members. Therefore the organization of the symposium attracted a very good support from TEQIP. With very good response received from researchers for their paper presentations and with confirm availability of very good practicing researchers for delivery of key note speeches at the symposium, we are confident that the organization of the symposium will be a grand success. We would like to encourage all the participants of this symposium to take positive and active part in the deliberations at the symposium. On behalf of the college management, we wish them bright and fruitful career ahead.

Director

Deputy Director

Walchand College of Engineering, Sangli

ABOUT WALCHAND COLLEGE OF ENGINEERING

(A Government Aided Autonomous Institute)

Walchand College of Engineering is situated midway between Sangli and Miraj cities at Vishrambag, Sangli. The WCE campus is located on about 90 acres of land on southern side of Sangli – Miraj road.

In 1947, the college made a modest beginning as New Engineering College, with a single program leading to B.E. (Civil) degree. In the year 1955, the College was renamed as Walchand College of Engineering as part of the new arrangements and pursuant to the Rehabilitation and Development Program mainly funded by Seth Walchand Hirachand Memorial Trust and the Government. The Government appointed an Ad Hoc Committee for conducting the college from May 1955, later replaced by the Administrative Council in 1956. The Ad Hoc Committee added two more degree programs in B.E. (Mechanical) and B.E. (Electrical) in 1955 with the intake of 20 each. Three Diploma programs also started in 1955 – Civil (40 intake), Mechanical (20) and Electrical (20).

Post Graduate programs in Civil, Mechanical and Electrical Engineering and Diploma program in Industrial Electronics were introduced in 1971. In 1986 the UG and PG programs in Electronics Engineering and UG program in Computer Science and Engineering were introduced.

PG program in CSE was introduced in 1997. In 2001, added B.E. program in Information Technology with an intake of 60 students. An additional intake of 30 students was also sanctioned for Computer Science & Engineering program, resulting in total intake of 390 students for all branches at UG level and 106 at PG level. As part of strategic planning, PG section is being strengthened and PG intake has now steadily risen to 240 across 10 programs. The College has a QIP scheme for full-time doctoral programs and also offers Ph. D. programs of Shivaji University in various branches of engineering.

Walchand College of Engineering became autonomous in 2007. The college revamped its academic structure and contents, in consultation with few US and IIT academic experts. Accordingly nomenclature of B.E and M.E programs has been changed to B Tech and M Tech programs. After completion of the first term of six years, the College has now received extension of autonomous status for the second term of six years till 2019-20. It participated in the World Bank funded, Government of India scheme, namely, Technical Education Quality Improvement Program (TEQIP) in Phase I from 2005-2009, wherein it stood all-India 2nd out of 127 participating institutions in terms of program impact performance. The college is presently participating in Phase II of TEQIP with outstanding performance.

From the desk of the Chair RSC 2016

Dr. P. J. Kulkarni

Dy. Director and Professor in CSE
Chair RSC 2016



Search and Research are continuous activities. Research culture in Computer Science and Engineering (CSE) at Walchand College of engineering (WCE), over last decade has seen significant positive growth. Year after year, more research outcomes are being strengthened. Quality Improvement Programme (QIP) of AICTE has instituted Ph D research center in CSE at WCE. Along with this, Shivaji University Kolhapur has already recognized the CSE department to conduct Ph D research programme. Association of Computing Machinery (ACM) also identified the CSE department to institute student chapter of ACM. The chapter activities are well progressing. To further encourage CSE research activities, it is envisaged to create a platform to enable researchers in the field of CSE and allied to come together to provide critique on the ongoing research activities to enable shape these activities in a better way. In this direction, CSE Department and Information Technology (IT) department at WCE decided to jointly organize a "National Research Symposium on Computing, RSC-2016". The student chapter of ACM at WCE came forward to support the organization. At WCE, this is first of its kind of organization of the research symposium. Right from the day of its announcement, few months back, the organizing team of the conference started receiving very good responses from research community in CSE and IT. The research papers selected for the symposium are duly peer reviewed by outside research experts in the respective domains. The paper reviewing experts have technically well contributed by providing prompt and critical inputs to the authors of the papers. In order to provide good mentoring to the young researchers and attendees, the organizers of RSC-2016 are fortunate to attract good number of practicing researchers to deliver key note addresses. A pool of expert panel members will provide to the attendees of the symposium a very good exposure to state-of- art in CSE, IT and allied fields. The entire focus of the symposium is to facilitate budding researchers to bring in innovations in their on-going research and make the research fruitful. Many of the participating researchers in the symposium are believed to have come from academic institutes, therefore various issues related to good practices in research methodologies, peer-to-peer sharing, widening contacts of like-minded researchers, presentations of research work, Intellectual Property Rights (IPR) etc. will be well deliberated. I am very much confident that the symposium will mark significant achievement for all attendees of the symposium in practicing quality research work. I wish each one of them excellent prospective research career in future.

Dr. P. J. Kulkarni, Chair, RSC 2016

ORGANIZING COMMITTEE

	Name	Designation
1	Dr. P. J. Kulkarni	Professor - Department of Computer Science & Engineering, Deputy Director - Walchand College of Engineering, Sangli
2	Dr .B. F. Momin	Head of Department - Department of Computer Science & Engineering, Associate Professor - Department of Computer Science & Engineering, Walchand College of Engineering, Sangli
3	Dr. S. H. Bhadari	Associate Professor - Department of Computer Science & Engineering, Walchand College of Engineering, Sangli
4	Dr. D. B. Kulkarni	Professor - Department of Information Technology, Walchand College of Engineering, Sangli
5	Dr. S. P. Sonawane	Head of Department - Department of Information Technology, Associate Professor, Department of Information Technology, Walchand College of Engineering, Sangli
6	Prof. A. R. Surve	Assistant Professor - Department of Computer Science & Engineering, Walchand College of Engineering, Sangli
7	Prof. M. A. Shah	Assistant Professor - Department of Computer Science & Engineering, Walchand College of Engineering, Sangli
8	Prof. M. K. Chavan	Assistant Professor - Department of Computer Science & Engineering, Walchand College of Engineering, Sangli

OUR DISTINGUISHED REVIEWERS

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Dr. Parag Kulkarni	Iknowlation
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Dr. Harish Kenchannavar	Gogte Institute of Technology
Dr. Dinesh Kukarni	Walchand College of Engineering, Sangli
Dr. Yashwant Joshi	SGGS Nanded

PROGRAM SCHEDULE

Day 1: Monday, December 19, 2016

9.30 am to 10.00 am	Registration and Welcome Tea Inauguration		
10.00 am to 10.30am			
10.30 am to 11.15 am	Keynote address -1 Dr. Aditya Abhyankar (ACM Speaker)		
11.15 am to 11.30 am	Tea Break		
SESSION-1 WEB / DATA MINING / NETWORKS Chair : Dr. Aditya Abhyankar Co-chair : Dr. B.F. Momin			
11.30 am to 11.50am Presentation – 01	Paper ID	Title	Author
	02	Generating Informative Summary of Social Image Search Result	Sheetal Takale and Prakash Kulkarni
11.50 am to 12.10pm Presentation – 02	10	Buddy Router: Novel DTN Routing Algorithm using Multi Parameter Composite Metric	Ajit Patil and Prakash Kulkarni
12.10pm to 12.30pm Presentation – 03	14	Improved Over Sampling Techniques for Handling Imbalanced Big Data Set Classification	Sachin Patil and Shefali Sonavane
12.30pm to 12.50pm Presentation – 04	15	Weblog Analysis Using Hadoop	Gauri Kalyankar, Shivananda Poojara and Dr Nagaraj Dharwadkar
1.00 pm to 1.40 pm	Lunch break		
1.45 pm to 2.30 pm	Keynote Address – 2 Dr. Parag Kulkarni (Chief Scientist and CEO of the EKLaT Research Lab, Pune)		
2.30 pm to 3.15 pm	Keynote Address – 3 Mr. Lokesh Venkataswamy (IET Speaker) (Managing Director, Innomantra Consulting Pvt. Ltd., Bangalore)		

SESSION-2**HPC / DISTRIBUTED COMPUTING****Chair : Dr. Parag Kulkarni Co-chair : Dr. S.P. Sonavane**

3.15 pm to 3.35 pm Presentation – 05	03	SCSP: Skyline Computation Strategic Planner for Distributed Environment	Rupali Kulkarni and B.F. Momin
3.35 pm to 3.55 pm Presentation – 06	11	NPL: A tool for managing processor affinity in network	Karveer Manwade and Dinesh Kulkarni
3.55 pm to 4.15 pm Presentation – 07	19	Parallel High Performance Content Based Publish Subscribe System	Medha Shah and D.B. Kulkarni
4.15 pm to 5.15 pm	Tea break followed by Poster presentation Chair : Dr. S.V. Kulkarni Co-chair : Dr. A.J.Umbarkar		
7.30 pm to 9.30 pm Banquet			

Day 2: Tuesday, December 20, 2016

9.30 am to 10.00 am	Welcome Tea																	
10.30 am to 11.15am	Keynote address -4 Dr. Sunita Patil (IET Speaker)																	
SESSION - 3 IMAGE PROCESSING/MACHINE LEARNING Chair : Dr. D.B. Kulkarni Co-Chair : Dr. S.H. Bhandari																		
<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 15%;">Paper ID</th> <th style="width: 45%;">Title</th> <th style="width: 40%;">Author</th> </tr> </thead> <tbody> <tr> <td>12</td> <td>Statistical Machine Translation: Foundations, Challenges And Recent Advances</td> <td>Arun Babhulgaoonkar</td> </tr> <tr> <td>16</td> <td>Underwater Image Enhancement and Color Correction with Wavelet Fusion</td> <td>Sonali Sankpal and Shraddha Deshpande</td> </tr> <tr> <td>13</td> <td>Blink detection using Electrooculography for stress detection</td> <td>Mamata Kalas and Bashirahamad Momin</td> </tr> <tr> <td>24</td> <td>Mosaicing Scenes with Vacant Spaces through a Quadcopter</td> <td>Meghshyam Prasad</td> </tr> </tbody> </table>				Paper ID	Title	Author	12	Statistical Machine Translation: Foundations, Challenges And Recent Advances	Arun Babhulgaoonkar	16	Underwater Image Enhancement and Color Correction with Wavelet Fusion	Sonali Sankpal and Shraddha Deshpande	13	Blink detection using Electrooculography for stress detection	Mamata Kalas and Bashirahamad Momin	24	Mosaicing Scenes with Vacant Spaces through a Quadcopter	Meghshyam Prasad
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11.50pm to 12.30 pm Invitee Presentation																		
12.30 pm to 1.00 pm	Poster Presentation Continued...																	
1.00 pm to 1.40 pm	Lunch break																	
1.45 pm to 2.30 pm	Keynote Address – 5 Dr. R.K. Kamat (Shivaji University, Kolhapur)																	
SESSION-4 INTRODUCTION OF PANEL MEMBERS 2.30 pm to 2.45 pm																		
2.45 pm to 4.15 pm	PANEL DISCUSSION <ul style="list-style-type: none"> ❖ Dr. R. K. Kamat ❖ Dr. Sunita Patil ❖ Dr. Parag Kulkarni ❖ Mr.Meghsham Prasad 																	
4.15 pm to 4.30	<ul style="list-style-type: none"> • Best Paper Presentation award • Best Poster Presentation award • Valedictory Ceremony 																	

Key Note Speech – I

From ACM and IEEE distinguished speaker

Towards Awakening Research Whereabouts

Aditya Abhyankar, *Senior Member, IEEE*

Abstract: Research has become a buzz word in India in last decade or so. While major part of it comes in the form of compulsion by some authoritative body, it is the choice of an individual to maintain the quality of the research. There are connotations associated with research whereabouts. This presentation is an effort to deliberate on the quality norms with the research fraternity and evaluate the parameters to form some consensus in the minds of researchers. The journey from abstraction to creating tangible forms will also be discussed. The importance of IP (Intellectual Property) generation and the dissemination of the same for commercialization in some form is one area often ignored by academicians. This talk will also try and help academicians understand the importance of creation of eco-system and help budding researchers leverage the same to come up with sustainable model of good quality research.

Index Terms: Research, Intellectual Property, Quality of Research, Commercialization, Plagiarism, Quality Norms.

I. INTRODUCTION

THE moment we start discussing research, the quality norms play important role and they should be carefully evaluated and understood to make ones research stand out and count.

The criticalities in the form of compulsions from various bodies of authority have pushed academicians in the realms of research. The entire journey of research from abstraction to tangible forms is interesting as well as challenging. Though what gets achieved from the research effort is largely dependent on the ability of the researcher, there are certain quality norms which can be accepted in universal form. It is important to ask the question why research!

II. RESEARCH WHEREABOUTS

A. Important characteristics of good quality research:

1. The work should be replicable and reproducible
2. The work should be generalizable
3. The work should be based on sound rationale
4. The work should be incremental
5. The work should have good societal impact

B. Important characteristics of bad research

1. Plagiarizing other work
2. Falsification of data
3. Misrepresentation of the data and dubious conclusions

C. Important Research objectives

1. To solve interesting complex problems

2. To make new discoveries
3. To help develop new products
4. To optimize existing systems and bring betterment in the workflows
5. To help satisfy human desires and curiosity
6. To investigate laws of nature
7. To earn higher degrees (M.Phil / PhD etc)

Good research always begins with a good question or enquiry. It is then important to convert good research into a technical document, either a paper or a thesis.

III. POPULAR MISCONCEPTIONS

1. Technical paper should have lot of maths
2. The article should be hard to read
3. One must fill up all the pages

IV. PATENTING BAREBONES

To be able to patent the idea should be

1. Novel
2. Non-obvious
3. Useful

V. CONCLUSION

Research is addictive and can give great satisfaction; it however requires perceived efforts, eye for detailing and smart choices. Most of the inventions are apparent accidents and the accidents are bound to happen, but one has to walk the path first. Through such interactions, hopefully, the readers will start finding the paths and will start walking the same, which eventually will result into interesting technical accidents!

About Speaker: Dr. Aditya Abhyankar



Dr. Aditya Abhyankar is currently working as Dean, Faculty of Technology and Professor in Department of Technology of SP Pune University. He is on lien from his duties as Dean R&D, Director CERD (Center for Excellence in R&D) and Professor in Computer Engineering Department of VIIT, Pune. He is associated as an adjunct professor with COEP, Pune, as research associate with Clarkson University, NY, USA and on advisory committee and BOS (board of studies) of many national and international universities.

Aditya Abhyankar received the BE degree in Electronics and Telecommunication Engineering from Pune University, India in 2001. He received the MS and Ph.D. degrees from Clarkson University, NY, USA in 2003 and 2006 respectively. He worked as a post-doctoral fellow at Clarkson University, NY, USA in the academic year 2006-07. He worked as a consultant for Biometrics LLC, WV, USA in 2007. Aditya has also earned M.A. (Sanskrit) with specialization in Vedant where he stood first in university in 2011, M.B.A. with specialization in finance in 2013 and M.A. (Philosophy) with specialization in Existentialism in 2015. He is pursuing his second

Ph.D. in Sanskrit with his research focusing on Nasadiya sukta from Rigved. His research interests include pattern recognition, signal and image processing, wavelet analysis and biometric systems. Dr. Abhyankar has also won number of national and state awards with the likes of IEI scientist of the year 2011 award, sir C V Raman 2015 award at the hands of Hon'ble CM of Maharashtra to name a few. He has contributed in national level programs for education like NPTEL at IIT Bombay. Dr. Abhyankar holds 8 US patents, 14 Indian patents, 7 disclosures, 8 Technology Transfers, 5 US copyrights to his credit. He has more than 70 international journal articles, 180 international conference papers. He has generated research funding of the tune of 2.5 million USD and has created state-of-the-art infrastructure in the field of pattern recognition. He provides consultancy to number of reputed industries like Biometrics LLC, USA, vision&D, Canada, Optra Systems, India, BYDesign, Bangalore, India etc. He also is director of Adijit SofTech Pvt Ltd which works in creating intelligent frameworks for pattern recognition based solutions.

Key Note Speech – 2
From Research Innovation Lab Speaker

Knowledge Innovation and Machine Learning

Parag Kulkarni
*CEO and Chief Scientist, Iknowlation Research Labs Pvt Ltd Pune
(www.iknowlation.com)*

Traditionally it was more about knowledge acquisition based learning. Knowledge acquisition based learning came up with wonderful methods right from statistical regression based methods, SVM, Bayesians to Deep and reinforcement learning. Reinforcement Machine Learning advocated exploration, which can be termed as dynamic knowledge acquisition. Dynamic Knowledge Acquisition set a new trend in Machine Learning (ML). While traditional mapping based learning demands data, too much context and restricts the learnability, reinforcement tried to introduce the ability to explore. We measure accuracies but hardly learnability of algorithm. There are limitations of Knowledge Acquisition based learning paradigm. With evolution of system and applications demanding more we need to move to creative and rather knowledge innovation based ML. This presentation focuses on Knowledge Innovation based Machine Learning catering to real life applications of Machine Learning. With numerous interesting case studies the aspects of creative machine learning will be discussed. All traditional methods right from rule based systems to recent advanced methods approach learning from same paradigm. As mentioned by Margaret Boden – can we build really creative machines? And if we want to build these machine will these learning paradigms suffice. Well we need different paradigms – we need to look and solve the learning problems in different way where learnability will be at centre. While taking crack at this learning problem, the talk will try to touch on different real life ML case studies. It will introduce some pioneering research in machine learning by iknowlation including Optimal Machine Learning, DEEP reinforcement Machine Learning and Context Vector Machines. In this presentation, I will argue that Optimal Creative ML is going to be next trend in machine learning. Over learning is dangerous so is under learning. While over learning kills creativity – under learning leaves us without clues. It is the time to go for Optimal Machine Learning. Find out more about pointers to optimal learning during this presentation that plans to cover landscape of opportunities in ML with focus on real life applications and research directions.

About Speaker: Parag Kulkarni



Parag is an entrepreneur, Machine Learning researcher and author of best selling Innovation Strategy and Data science books. An avid reader, Parag is founding CEO and Chief Scientist of iknowlation Research Labs – a vibrant Machine Learning Product, research and Consulting Company. Parag has published over 300 research papers, invented over a dozen patents and he authored 15 books. Parag's machine learning ideas resulted in pioneering products those became commercially successful and produced unprecedented impact. As a consultant Parag has contributed to success of over two-dozen organizations including start-ups and established companies. He is pioneer of concepts Systemic Machine

Learning, Context Vector Machines and Deep Explorative Machine Learning. He delivered over 300+ keynote addresses and 200+ tutorials across the globe. An alumnus of WCE Sangli, Parag holds PhD from IIT, management education from IIM and was conferred higher doctorate DSc by UGSM monarch, Switzerland. His work on Systemic Machine Learning published by IEEE is widely cited. His areas of interest include Machine learning and allied areas with focus on optimal and systemic learning.

Key Note Speech – 4

From The IET and Practicing Consultant Speaker

Three-fold Approach to Innovation

Lokesh Venkataswamy

Managing Director

Innomantra Consulting Private Limited, Bangalore

Lokeshv@innomantra.com

Abstract: Innomantra is India's leading Innovation and Intellectual property consulting and services firm. It helps organisations to design and achieve their innovation and Intellectual property goals by 3x. Its clients range from small entrepreneurial enterprises to Fortune Global 500 organisations. The firm is headquartered in Bengaluru, India. Innovation3x describes the firm's philosophy which believes that innovative organisations must identify innovation goals that seeks to achieve a 3x boost in performance. It also represents three-fold approach to innovation that looks at overall business strategy, people and functional systems using Innovation Maturity Model, Collaborative Innovation, Design Thinking and Functional Innovation Methodologies.

About Speaker: Lokesh Venkataswamy



Lokesh is Managing Director at Innomantra. He brings with him more than 20 years of professional experience in Intellectual Property management, New Product Development, and innovation management. Prior to Innomantra, he managed innovation and intellectual property activities at Larsen & Toubro. He began his career with the Medical Equipment and Systems Group at L&T where he spent 12 years in product design & development, strategic planning & initiatives driving Innovation & IP. At Innomantra, Lokesh has worked with clients from several Fortune Global 500 to SME's in a wide range of sectors including Automotive, Industrial Equipment Manufacturing, Information Technology, Engineering Services, Medical Electronics and Consumer Goods in the areas of Intellectual Property Management and Innovation Management. Lokesh, a Mechanical Engineer, has an MBA in Marketing and PG Diploma in Intellectual Property Law from National Law School of India University (NLSIU). Lokesh was also awarded winner of "India Innovation Challenge" at 4th India Innovation Summit - 2008 Organized by CII & Govt. of Karnataka. Lokesh also played a role of mentor and jury in 'Power of Ideas 2010, 2012 and 2015– Initiated by top business daily in India 'The Economic Times' and Indian Institute of Management –Ahmadabad, India' and supports National Entrepreneur Network(NEN) India. He is also a member and Chairman – Bangalore Local Network, Institution of Engineering & Technology (IET) and Member at IT Panel at CII, India. He is also a Co-Founder & Trustee – Association of Intellectual Property Professionals & Owners Association (AIPPO), India.

Key Note Speech – 4

From The IET Speaker

Data Science Vs. Big Data for Business Decisions

Sunita Patil

Vice Principal & Dean Academics & Professor, Department of Computer Engg.

K J Somaiya Institute of Engg. & IT Mumbai

Data have become a real resource of interest worldwide for every industry & business. Along with the rise of data two distinct efforts concerned with harnessing its potential, one is called Data Science and the other as Big Data. These terms are often used interchangeably despite having fundamentally different roles to play in bringing the potential of data of an organization.

Big data is a term that describes the large volume of data; both structured and unstructured that covers a business on a day-to-day basis. But it's not the amount of data that's important. It's what organizations do with the data that matters.

Data science is an interdisciplinary field about scientific processes and systems to extract knowledge or insights from data in various forms, either structured or unstructured, which is a continuation of some of the data analysis fields such as statistics, machine learning, data mining, and predictive analytics, similar to Knowledge Discovery in Databases (KDD).

Data Science and Big Data both are related to Data Driven Decision with the expectations of better decision and increased value. This is a process and involves different stages as Capture the Data, Process & Store it, Analyse and Generate Insights into data and then take Decisions & Actions. In this data driven decision process, Big Data is typically involved in second stage and that too in all the technologies. Big Data & Technology helps in reducing cost in processing volume of data and also making it feasible to do a few typically analyses whereas Data Science is involved at the third stage in data driven decision process. It involves in using Statistical, Mathematical and Machine Learning algorithms to use data and generate insights. Whether a data is "Big data" or not, we can use Data Science to support Data Driven Decisions and take better decisions.

Data Science looks to create models that capture the underlying patterns of complex systems, and codify those models into working applications while Big Data looks to collect and manage large amounts of varied data to serve large-scale web applications and vast sensor networks.

Although both offer the potential to produce value from data, the fundamental difference between Data Science and Big Data can be summarized as collecting data does not mean discovering potential in it.

In the world of data science, it takes a science to convert a raw resource into something of value is because what is extracted from the 'ground' is never in a useful form. 'Data in the raw' has noise, irrelevant information, and misleading patterns. To convert this into precious thing requires a study of its properties and the discovery of a working model that captures the behavior we are interested in. Something unique to business that has given them the knowledge of what to look for, and the codified descriptions of a world that can be mechanized and scaled as further discovery and innovation. No organization would invest in the extraction of a resource without the expertise in place to turn that resource into value.

Unfortunately with Big Data, the largest companies see what solutions they have engineered to compete in their markets. But these companies hardly represent the challenges faced by most organizations. Their dominance often means they face very different competition and their engineering is done predominantly to serve large-scale applications. This engineering is critical for daily operations, and answering to the demands of high throughput and fault-tolerant architectures. But it says very little about the ability to discover and convert what is collected into valuable models that capture the driving forces behind how their markets operate. The ability to explain and predict an organization's dynamic environment is what it means to compete using data.

Understanding the distinction between Data Science and Big Data is critical to investing in a data strategy. For organizations looking to utilize their data as a competitive asset, the initial investment should be focused on converting data into value. The focus should be on the Data Science needed to build models that move data from raw to relevant. With time, Big Data approaches can work in concert with Data Science. The increased variety of data extracted can help make new discoveries or improve an existing model's ability to predict or classify.

About Speaker: Sunita Patil



Sunita R. Patil completed her graduation from North Maharashtra University, Jalgaon in the year 2000 and post-graduation from University of Mumbai in the year 2008 and is currently working as Vice Principal & Dean Academics & Professor, Department of Computer Engg. at K J Somaiya Institute of Engg. & IT Mumbai. She completed her PhD in Data Mining from NMIMS, Vile Parle, Mumbai. She is the convener for IET (Institute of Engineering & Technology, Students Chapter & Academic Affiliation). She is the Vice Chairman for IET Mumbai Local Network & Ex-VC Young Professional of IET Mumbai Local Network. She has specialization in various fields like Data Warehousing Mining & Business Intelligence. She has authorized more than 16 research papers at international, national journals and conference proceedings.

Key Note Speech – 5

From The University Research Group Speaker

Internet of things: A Novel Interdisciplinary Research Paradigm in Computer Science

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Abstract: *A logical extension of the embedded and/or cyber physical systems is the conception of Internet of Things (IoT). Popularly known as the ‘Internet of Everything’ this novel paradigm is now growing in leaps and bounds and is almost about to touch each and every walks of societal life.*

The IoT based systems are characterized by different subsector components such as the sensors, instrumentation, communication protocols, different means of attaining the connectivity, smartphone apps and simultaneously making the system work in the constraint of power, bandwidth as well as other spatial and temporal metrics.

The present talk will exemplify various IoT based systems, their design and deployment in the application domain. The talk will emphasize on the development lifecycle of such systems and the way the interdisciplinary thinking amongst the various branches of Engineering, paving the ways for their development.

About Speaker: Dr. R. K. Kamat



Professor R. K. Kamat is currently Professor and Head of the Department of Electronics, Shivaji University, Kolhapur. He has extensive teaching and research experience spanning over last two decades. He has published 100+ papers in the international journal of repute. Eleven students have been awarded Ph.D. degree under his guidance. He is also recipient of the Young Scientist award of the Department of Science and Technology (DST), Government of India. Currently he is also Vice-President of the IEEE SSCS, India Council. Government of Maharashtra has recently appointed him on the State Level Quality Council (SLQC) which is mandated the quality inculcation in the institutes of Higher Education in the state of Maharashtra.

Key Note Speech – 6

From Invited Speaker

Mosaicing Scenes with Vacant Spaces through a Quadcopter

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Abstract—This paper focuses on a method of constructing panoramas from a quadcopter, and a new mosaicing sub-problem when the scene contains significant regions of vacant spaces. These vacant spaces yield little to no features to match input images and hence challenge existing mosaicing techniques. We describe a framework that is able to handle this unique input by leveraging the availability of the inertial measurement unit (IMU) data from the quadcopter. We demonstrate the efficacy of our approach on a number of input sequences that cannot be mosaiced by existing methods.

I. INTRODUCTION

Finding features and using them to align images to construct panoramas is one of the success stories of computer vision. Virtually all recent consumer cameras have this technology embedded. The success of these methods relies significantly on finding common features in the images that can be used to establish the appropriate warps to register the images together.

Problem Definition: There are scenes, however, that makes this challenging. One situation is when the scene needs to be probed in an orthographic view, and is not easily accessible. Murals on large urban architecture is an example. This suggests a ‘close up orthographic view’ by a moving camera fitted on, say a quadcopter. A related situation is when a scene area simply does not contain features (too little, or no features, e.g. multiple adjoining posters in an conference event).

The goal of this paper is to compute a panorama of a scene lying on planar surface that is, possibly, difficult to reach manually. The scene is assumed to have vacant spaces. A schematic for this problem is shown in Figure 1.

II. RELATED WORK

Panoramic image stitching (alternatively, image mosaicing) is a well-studied problem in the field of computer vision. Representative works include [1], [2], [3], [4] [5] [6]. A full discussion on related works is outside the scope of this paper, readers are referred to [7] for an excellent survey. Given the maturity of this area, there are various freeware as well as commercial software available for performing image stitching; most notable are AutoStitch [8], Microsoft’s Image Compositing Editor [9], and Adobe’s Photoshop [10].

All of these methods are based on a similar strategy of finding features in each image, matching these features between images, and then computing pairwise image warps to align them together. A bundle adjustment is often applied to globally refine the alignment. All of the aforementioned



Fig. 1. Problem definition. Vacant spaces are encountered in various scenes. When individual portions are captured by a quadcopter, how does one create the complete mosaic given that common features are either not available as in this example, or confusing (see Fig. 7)?

methods assume the imaged scene is planar or that the camera has been rotated carefully around its center of projection to avoid parallax.

Brown et al. [11] proposed a method that used invariant features located at Harris corners in discrete scale-space and oriented using a blurred local gradient for stitching. Eden et al. [12] were able to stitch images with large exposure difference as well as large scene motion into single HDR quality image without using any additional camera hardware.

All of the image mosaicing methods work only when there is an “intersection” in feature space of images to be stitched. When there are “gaps” (either physical or due to lack of features) between images to be stitched it is not clear how to perform the stitching.

III. METHODOLOGY

The method adopted is pictorially depicted in the overview shown in Figure 2 and is described in detail later on. In brief, we systematically acquire a video of the scene, reduce the input video to a manageable number of images, and finally combine the images acquired from different positions into a mosaic.

A. Video acquisition

We first dispatch the quadcopter to as close to the scene as possible. The corners of a rectangular area of interest are provided to the quadcopter, and it is programmed to traverse

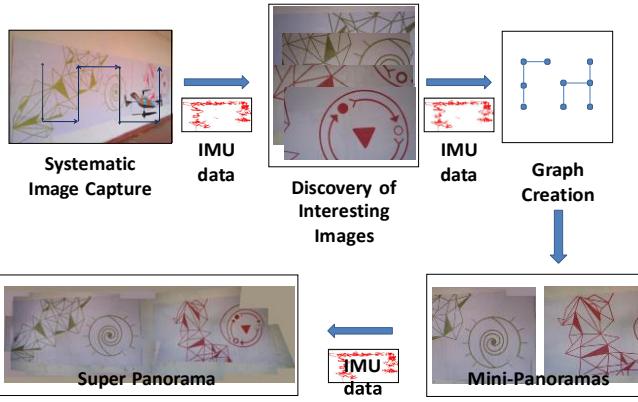


Fig. 2. Overview: Input imagery is systematically acquired (top left) by a quadcopter. In the next step, interesting images are found by clustering the video into regions based on positional data. A graph is constructed using proximal images. For each connected component in a graph, standard stitching techniques are used to create mini-panoramas which are then joined together into super panorama again using the IMU data.

the area in a raster scan fashion. Note that trying to create a mosaic in an incremental linear fashion by combining adjacent frames is prone to loss of two-dimensional spatial proximal information. It is also computationally overwhelming.

The quadcopter returns with a video of the scene. Images extracted from a short video of about a minute or more overwhelms existing mosaicing software, such as AutoStitch or Adobe Photoshop.

B. Selecting interesting images

Our goal in this step is to reduce the amount of input data and produce a set of interesting images. In other words, we wish to convert a video into an album of images. The key difference between our problem and standard albumization [13], [14] is the use of positional information. A standard quadcopter has an IMU that, after calibration, may give reasonable information of positions. Using positional information it is possible to cluster the images, and sort the images into an $m \times n$ grid. The number of cluster centers is automatically determined using the agglomerative bottom up hierarchical clustering method [15], with the additional requirement that the whole scene (represented by the positional data) is covered.

Clustering Details We assume that each IMU data position corresponds to an image of definite fixed dimensions. Consider each position of the IMU data to be a leaf node. Two nodes are greedily combined based on the closest Euclidean distance, and replaced with an internal node; the position of the internal node is set to be the centroid of the two nodes, and each internal node now corresponds to a virtual image of the same size taken by a virtual quadcopter. The algorithm recursively merges all the nodes till we end up with a root. In the next phase, we produce cluster centers; a set of nodes is considered for being the output as cluster centers if the union of these nodes completely spatially cover the scene. From the bottom-up construction, it is clear that the root will represent a single position, and thus a single virtual image, and will not cover

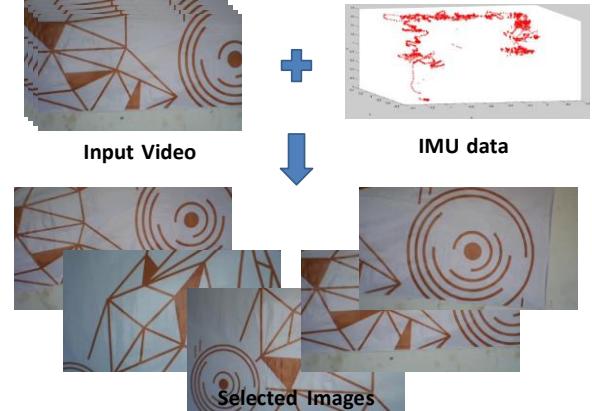


Fig. 3. We align the image stream with the IMU data, and then transform the video into a set of interesting images with a clustering algorithm.

the scene. At the other extreme, the set of all leaf nodes will cover the scene. To resolve this, during the calibration phase, we pre-decide the minimum distance between two center of projection to have least overlap. This is used as the threshold in the clustering algorithm. Once cluster centers are found, we pick the leaf node which is closest to the cluster center to find a real image. This process is schematically shown in Figure 3.

In practice, the number of cluster centers and thus images for the scenes we have covered is now within the capacity of AutoStitch. As mentioned in the introduction, as long as there are sufficiently varying and “matchable” features, AutoStitch is able to perform a reasonable result. However, if there are very few features in overlapping region of two images, then the output is not acceptable. This situation will arise when there is vacant space in the imagery.

Mini-Panoramas Specifically, we assume at this point that the interesting photos are available in the form of a $m \times n$ grid. First, we find SURF [16] features for each image in a grid. Next, we use Best of Nearest Neighbor matcher (from the OpenCV library) with Random Sample Consensus (RANSAC) [17] to find geometrically consistent matches between neighborhood images inside grid. We create a graph with images being nodes, and add an edge between two nodes if there are sufficient matches. We have to recall at this point that if there are “vacant spaces” there will not be enough features for successful matches; the graph will end up with multiple (disconnected) components. We next compute multiple spanning trees for the various components. Given a spanning tree, the center of the spanning tree is a node from which the distance to all other nodes is minimal [18]. Next we calculate the homography of each image with respect to spanning tree center. Finally, for each spanning tree, we stitch all pictures within the spanning tree to create a mini-panorama using the computed homographies by warping all images with reference to the image at spanning tree center. The spanning tree is an $O(N)$ structure. The process is described in Figure 4.

C. Super-panorama

In this section, we consider the situation when programs like AutoStitch fail. We assume that the output of the previ-

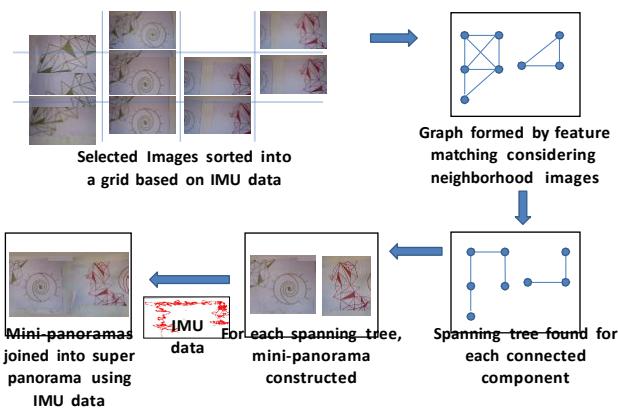


Fig. 4. Interesting images acquired are segmented and individual (mini-panoramas) are constructed. These are then later combined into the desired super-panoramas using the IMU data.

ous step has resulted in multiple spanning trees where each spanning tree center corresponds to a specific depth, since we have stitched all images by taking the spanning tree center as a reference. Individual panoramas for each spanning tree termed mini-panoramas have been created. A super-panorama must be created from mini-panoramas; these usually correspond to different depths due to jerky motion of quadcopter.

A super-panorama is done using a two step process. Assume two trees in the forest corresponding to area A and area B of the scene (see Figure 5). Assume that a mini-panorama is created from these two areas, and the depth of the planar surface from the camera is more for A, than for B. We then take the mini-panorama image captured at B, and ‘move’ it to a new location B’ whose depth (from the imaged surface) is the same as that of A. The resulting image will be smaller; the images are related by the equation

$$\frac{x^0}{x} = \frac{Z}{Z^0} \quad (1)$$

where x (respectively x') represents a pixel location of the image in B (respectively, B') and Z (respectively Z') represents the depth of the images surface from B (respectively, B').

In order to form a super-panorama from the depth of A, we can now treat the resulting images from A (unchanged) and B (computed from Equation 1) forming a simplistic stereo pair at the depth of position A. Using the stereo disparity formula we can “place,” from the view point of A, the image captured from B’, thereby creating a super-panorama. (We could as well present the entire scene from the viewpoint of B’ (since it is at the same depth); we prefer these pictures to the one that one may be created from the depth of B.)

IV. EXPERIMENTS AND RESULTS

All our experiments have been completed with the inexpensive consumer quadcopter called a Parrot’s AR Drone 2.0. We have implemented our algorithm in C++ using the OpenCV library. Experiments were performed on a PC with

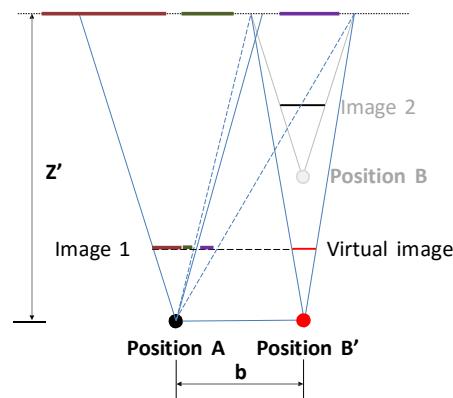
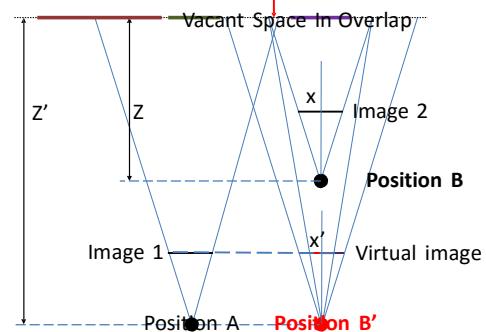


Fig. 5. (Top) The virtual picture as seen from position B’ is computed using Equation 1 from the real picture taken from B. (Bottom) Using the stereo disparity, calculated from the baseline width b and depth Z' , it is possible to depict the composite scene obtained from both A and B’ (from the view point of A).

Intel Core i7 processor(@3.4GHz) and 8GB RAM. Please visit <http://goo.gl/sYvoVP> for datasets and code related to the paper.

A. Indoor Imagery with Vacant Spaces

Our first set of experiments were conducted in an indoor environment.

The input stream had about 9000 input images. The selection algorithm pruned the video into $N = 13$ images. A sample of the selected images are seen in Figure 6(a). The scene as captured by a smartphone can also be seen, as well as the outputs of the state of the art stitchers. Note that AutoStitch is only able to stitch the upper half of the scene. Our result Figure 6(e) clearly stands out in comparison.

B. Outdoor Imagery with Vacant Spaces

Our next set of experiments were conducted in an outdoor environment. The input stream had about 12000 images. The selection algorithm pruned the video into $N = 30$ images. A sample of the selected images are seen in Figure 7(a). The scene as captured by a smartphone can be seen in Figure 7(b). Figures 7(c), (d) and (e) shows the comparison of outputs

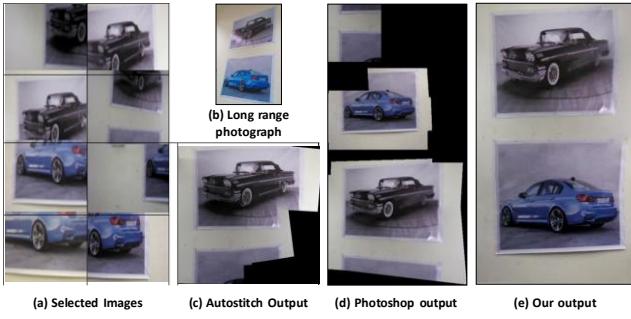


Fig. 6. (a) Salient images from the quadcopter video using our selection algorithm of (b) an indoor scene. This long range photograph has been captured separately by a smartphone camera only for context. Notice a significant vertical vacant space (around three feet) in the imagery. (c) Output of AutoStitch – only the upper half of the scene is output. (d) Output of Adobe Photoshop CS6 – the vacant space posed a problem to the feature matching algorithm, so instead of a mosaic, individual pieces were output as mini-panoramas (e) Our output on the selected images. We are able to present the scene in high fidelity in an orthographic view.

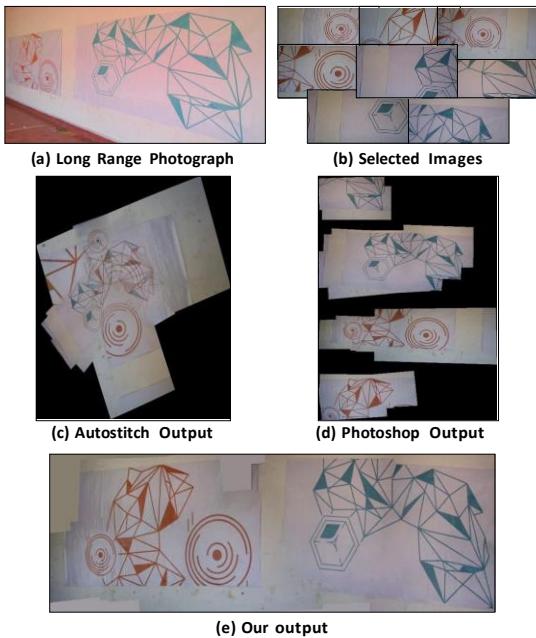


Fig. 7. (a) An outdoor scene captured by a standard camera in an exhibition. The approach to the area is normally cordoned off and one needs permission to get a quadcopter to take the picture. Notice a significant gap (more than two feet) between the two posters. (b) Salient images from the quadcopter video using our selection algorithm. (c) Output of AutoStitch on the selected images. The mosaic is not reasonable presumably because of the confusion in features. (d) Output of Adobe Photoshop CS6 on the selected images. The vacant space posed a problem to the feature matching algorithm, so instead of a mosaic, individual pieces were output as mini-panoramas (e) Our output on the selected images. We are able to join two posters (separated by vacant space) using the IMU data.

of state of the art stitchers with the output of our algorithm. Note that AutoStitch is getting confused by too many matching features.

V. CONCLUDING REMARKS

In this paper, we have described a method of imaging large scenes using a quadcopter enabling close orthographic views. We also defined a new problem, that of computing a mosaic of a planar scene with vacant spaces. Vacant space relates to images in an input stream where there are not enough features for traditional mosaicing algorithms to estimate geometric warps to align the images.

Our solution to this problem is to use an autonomous quadcopter which is capable of taking pictures. The quadcopter has an inertial measurement unit that is capable of outputting reasonable spatial locations, but unreliable roll, yaw and pitch. Using this positional information, our algorithm selects an “interesting” subset of the video imagery. Whenever there is overlap in feature space in the subset, we stitch images using traditional vision-based methods avoiding the erroneous roll based warping. At other times, we use positional information, and reduce the resulting problem of computing a complete panorama to the stereo problem of merging mini-panoramas. Our method works on both indoor and outdoor scenes.

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Generating Informative Summary of Social Image Search Result

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Abstract

Tag based social media search presents search results as a ranked list of images. However, the search results are not informative. Informative summary is with regards to the identification of: important visual concepts, locations-of-interest (LOI), and context describing tags. In this paper, we propose to generate an informative summary of social image search results. The proposed scheme exploits multiple modalities in order to understand context and content of geo-tagged social images. We formulate the problem as a graph clustering problem, where edge weights are geographical distance, textual and visual similarity between images. Late fusion of three different edge weight parameters avoids computational overhead. Performance evaluation is based on intrinsic and extrinsic methods using both Ground Truth and an evaluation protocol having no human intervention. Through empirical study, we demonstrate the effectiveness of our algorithm to generate informative summary which is relevant, representative and diverse.

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Buddy Router : Novel DTN Routing Algorithm using Multiparameter Composite Metric

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Abstract

Delay Tolerant Networks (DTN) are special networks where nodes are thinly distributed and therefore long period of disconnections are observed in it. These sporadic connections, along with limited resources at mobile nodes pose challenging environment for routing in DTN. Opportunistic forwarding mechanism is explored by DTN routing strategies to deal with inherent transmission uncertainties. Maximizing message delivery ratio and reducing delivery overheads at the same time is the major objective behind number of opportunistic forwarding policies. This paper introduces two forwarding strategies: Buddy Router and its variant Buddy Router with Time Window. These are novel routing schemes for DTNs, which use new sociality metric called buddy metric. This buddy metric value is calculated by each node with every other node it has encountered with, using contact frequency, aggregate contact duration and contact recency between them. The main contribution of this paper is to present detailed formulation of Buddy Router and exploit buddy metric concept for efficient DTN routing. We also present comparative analysis of proposed routing strategies with existing DTN routing protocols.

Keywords—Delay Tolerant Network(DTN), Routing, Opportunistic Routing and Pocket Switched Networks (PSN)

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Improved Oversampling Techniques for Handling Imbalanced Big Data Set Classification

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Abstract — Big Data is a catchphrase of today's research. It depends basically on huge data generated in zettabytes per year. The spotlight is to focus on the analysis of information, by integrating and exploiting it as per the usage. There is a tremendous requirement to study data sets beyond the ability of commonly used software tools and the processing feasibility of the single machine architecture. Training of performance techniques helps to bind the efficient management of Big Data streams. Classification of imbalanced datasets is the point of priority concern in the real world applications. The standard classifiers have produced a notable drawback in the performance of the classification of datasets having imbalanced class distribution. In this paper, a methodology is presented for the handling of single-class/multi-class imbalanced data sets with enhanced over_sampling (O.S.) techniques to improve classification. Various classifiers are further applied on produced balanced data sets to analyze classification. The proposed techniques are applied to imbalanced Big Data (I.B.D.) using mapreduce environment. Experiments are performed on Apache Hadoop using sample datasets. F-measures, ROC area are used to measure the performance of the classification.

Keywords: imbalanced datasets, Big Data, over sampling techniques, data level approach

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Weblog Analysis Using Hadoop

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Abstract— In today's world of Internet, websites are convenient source of information. Millions of people are accessing these websites and navigating number of pages by liking, sharing, clicking, and tweeting per day. Hence log files of these websites are increasing in size per day. These log files are significant source of information to recognize the behavior of the user. In current internet world, analysis of log file is becoming a needful task to examine the customer's behavior with the purpose of improvements in advertising and sales. As the web log file size is huge parallel processing and reliable data storage system for processing these files is required. Hadoop is an open source framework which provides parallel and reliable storage and processing of such large datasets. In this work weblog analysis system is implemented by using Hadoop which is capable to process large sizes log files and helps to analyze the output results to study user's behavior.

I. INTRODUCTION

As per the needs of the today's world, everything is going online. Each field is having their own way to put their applications and business online on the internet. Seating in the house anyone is able to do shopping, banking related work; Also able to get weather information, and numerous other services. And in such a competitive environment, different service providers are anxious to know about if they are offering the best services of the market, if people are buying their product, are obtaining interesting and easy to use their application, or in the banking sector they need to know that how many customers are waiting for their bank scheme. Similarly, they also need to know about problems that have been verified, how to solve, how to make web sites or interesting web applications, which products people are not buying, and if so how to improve advertising strategies to attract customers, what should be the future marketing plans. Log files are useful to get the answers of all these questions [1][2][8].

In today's world of Internet, websites are convenient source of information. Millions of people are accessing these websites and navigating number of pages by liking, sharing, clicking, and tweeting per day. Hence log files of these websites are increasing in size per day. To discover the behavior of the user ,these large log files plays vital role. Objective is to study Hadoop Framework in detail and implement Weblog Analysis System using Hadoop.

The data generated in the form of web logs is measures in terms of giga bytes to tera bytes in large there tier web applications and usage of the application is dynamical changeable over the time. Hence its need of an hour to process the logs files for real time analysis of user activities.

Web log consists of data gathered is too huge, dynamic and different variety. Hence it can be considered under problem scenario of big data analytics .Big data analytics is the process which examines large datasets which contains variety of types of data. Big data Analytics is used to uncover hidden information, hidden patterns and to discover knowledge from the large volume data. In th experiment we are using Hadoop framework for data processing and analysis of the web logs.

Overall structure of the paper is as follows Section II briefly describes about prior work. Section III describes the fundamentals of Hadoop eco system and web logs which motivated to conduct the experiment. Section IV indicates the methodology adopted for analysis. Section V describes about results and discussion. Lastly the Section VI concludes the overall research work.

II. PRIOR WORK

Web log analysis is an experimental and distinctive field which is constantly formed and modified by the convergence of many different emerging web technologies. Because of its interdisciplinary nature, the diversity of

the problems it faces. The variety and the number of web applications is the subject of many distinctive and different research methodologies.

Weblog analysis is used to find the statistics from web sites. Weblog analysis can be done using different techniques and statistical tools [6][7].

Weblog analysis system based on Hadoop and its ecosystem was developed, in which authors used MapReduce framework with Pig programming for the efficient results and better performance. Pig programming is used for the effective analysis of log files. The proposed system is equipped with two functions, one for the application server traffic analysis,

and a performance statistics program. The proposed system provides fault tolerance and performance tuning. The main objective of the proposed system is the system helps to quickly capture and analyze the data hidden in enormous potential value and as an important basis for business decisions [1].

The analysis system based on Weka was proposed for the analysis of weblogs .A Weka is open source Java based platform for data mining, which contains different machine learning algorithms which can be used to extract data, including pre-processing of data, classification, clustering, association of mining rules and visual interactive page. The developed system process web logs on Weka platform. Weka processes and analyzes the data, mines uncommon information and finds trends of different users which visit the website and by this it helps to provide support for the decision making and in making structure of website [6].

A framework was developed to analyze weblog social networks built by a query-dependent exploration scheme and query-dependent ranking algorithm. The Weblog social network analysis uses content analysis and semantic issues, and then associate bloggers who never get his interaction. Interactive information visualization techniques such as fish- eye have been introduced for users to explore different features at various levels of abstraction over the underlying social network. Authors have also introduced the applications developed based on the link and content analysis and visualization techniques, which benefits the activities of national security and even the general public to understand each weblog social network[7].

III. HADOOP AND WEBLOG

A. Weblog

When an internet search or link any web site, the web server records the details of it in the form of Web Access Logs. In the Web Access Log you can see the file types, users log on to the place from which the application and other information such as the type of web browser and visitors of the devices in use are made. An access log contains the list of all the items that users have requested from a website. Now days many people are using the internet for their daily activities, such as social networking, news, education and many others. Therefore, websites are acquiring thousands of visitors every day. As a web server keeps record of all user actions, the file size web server log is growing every day. These log files are semi-structured and hence very difficult to store, process and analyze using traditional database systems or traditional data warehouse in minimum time [1][3][8].

B. Hadoop Framework

Apache Hadoop is an open source framework written in Java that allows you to distribute the processing of large amounts of data across clusters of nodes that use simple programming models. Frame-worked Hadoop application works in an environment that provides distributed storage and computing capabilities of computer clusters. Hadoop is designed in such a way that it scales from single server to thousands of nodes, each of which offers the calculation and local storage[1].

- 1) Hadoop Common: These are the Java libraries, and utilities used by other Hadoop modules. These libraries offers file system and abstraction level of the operating system and contains the necessary Java files and scripts needed to start Hadoop.
- 2) Hadoop YARN: It is a reference framework for the planning process and the cluster resource management.
- 3) Hadoop Distributed File System (HDFS): A distributed file system that provides access to high-throughput of application data. The Hadoop Distributed File System (HDFS), is completely based on the Google File System (GFS) and designed to run on large clusters and also provides a distributed file system that is fault-tolerant. HDFS is based on master- slave architecture where the master works as a single Name Node that manages the file system metadata and one or more slave Data Nodes data. The Name Node controls the mapping of blocks to Data Nodes.

The Data Nodes are responsible for reading and writing operations with the file system. They also concerns about the block creation, deletion of blocks and also replication of them based on instructions given by Name Node. HDFS offers a file system to store and access data, and also list of commands which are available to interact with the file system [11].

- 4) Hadoop MapReduce: This is YARN-based system used for parallel processing of large volume of data sets.

C. MapReduce Framework:

Hadoop MapReduce is a framework that enables ease of writing software applications that are able to process large volume data in parallel on thousands of machines which are commodity hardware, in a authentic and fault-tolerant way [12].

The MapReduce includes these two different phases that are running Hadoop programs as shown in Figure 1:

- 1) The Map Phase: This is the initial phase that takes the input data and after processing, converts it into intermediate data which is in the form of key/value pairs.
- 2) The Reduce Phase: This is the second phase in which the output of a map phase is taken as an input and combines the values associated with the key. The task of reducing is always performed after the operation map.
- 1) Starting Hadoop: After setting all the parameters of system Hadoop can be started by using bin/start-all.sh command. The jps command is used to start NameNode, SecondaryNameNode, SecondaryNode, and JobTracker.
- 2) Weblog Analysis and Processing: Initial state of system consists running Hadoop infrastructure. Figure 2 shows overall working of Weblog analysis system on Hadoop infrastructure.

HDFS stores both the input and output of the MapReduce framework. The MapReduce framework is able to take care of planning, monitoring the tasks and also re-running the failed tasks [12].

The MapReduce framework composed with a single master Job Tracker and one TaskTracker slave for cluster-node. The master is responsible for the management of resources, monitoring of consumption and availability of resources, the planning of the tasks of the members work on the slaves, their monitoring and re-executing the failed tasks. The TaskTracker slaves execute the tasks as directed by the JobTracker, and provide information about the progress of tasks completion [12].

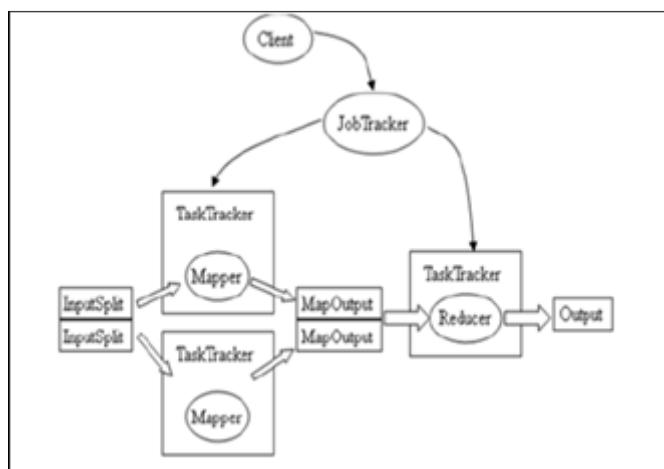


Fig.1. Map Reduce workflow diagram

The Job Tracker is the only point of failure for the Hadoop MapReduce service that means that all running processes are stopped if JobTracker goes down.

IV. METHODOLOGY

The Weblog analysis system is implemented by using Hadoop Framework. Hadoop MapReduce framework is used for processing large size semi structured log files data. The weblog data sets are downloaded from source

sites and converted into text file format for further processing. Python is used for the MapReduce programming. The hardware and software requirements for the implementation of weblog analysis system using Hadoop are

A. Hardware requirements

Personal computer with 1). Processor: i5-4590 @3.30 GHz.2). RAM : 4 GB. 3). HDD: 500 GB.

B. Software requirements

a) VMware Workstation b) Cloudera-quickstart-vm-5.5.0 c) Centos 6.7.

C. Experiment set up and process

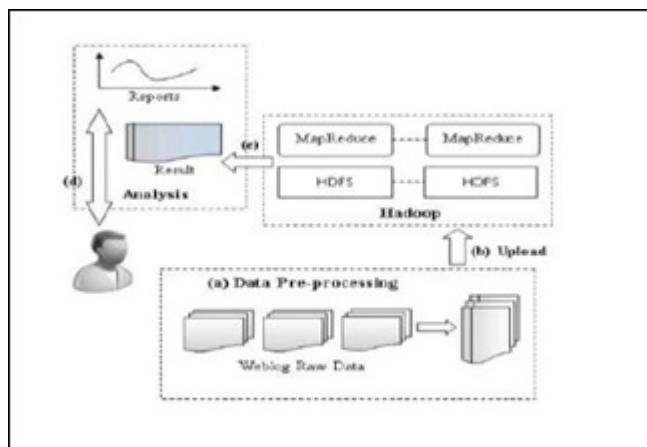


Fig. 2. Web Log Analysis Architecture[1]

In this firstly

[1] The weblog datasets which are in the raw format are converted into text file format as shown in fig 3.

entry_Netlogd	
146387	http://osa3.blog.ocn.ne.jp/project/2004/10/post_13.html
146389	http://blog.livedoor.jp/kngcurts/archives/8211528.html
146524	http://valuation.jugem.cc/?eid=231
146590	http://sugamo.jugem.cc/?eid=268
146540	http://numazu.jugem.jp/?eid=333
146541	http://rediyudan.jugem.jp/?eid=372
146393	http://taai.blog.ocn.ne.jp/taai/2004/10/post_14.html
146534	http://iris.jugem.jp/?eid=27
146538	http://brownshoes.jugem.cc/?eid=171
146378	http://kyomasa-hawks.blogzine.jp/annex/2004/11/11.html
146391	http://blog.livedoor.jp/nakonoya/archives/8039253.html
146397	http://blog.livedoor.jp/a126blue/archives/8105893.html
146576	http://hoku-to-blog.ocn.ne.jp/worker/2004/10/post_55.html
146388	http://aikankankohum.blog.ocn.ne.jp/tendurezaiku/2004/10/post_8.html
146394	http://rockriver-area.jp/revisor/archives/2004/10/post_27.html
146385	http://nekoya.blogzine.jp/area22/2004/11/post_2.html
146579	http://blog.livedoor.jp/jzake/archives/868891.html
146387	http://osa3.blog.ocn.ne.jp/project/2004/10/post_13.html
146389	http://blog.livedoor.jp/kngcurts/archives/8211528.html
146524	http://valuation.jugem.cc/?eid=231
146590	http://sugamo.jugem.cc/?eid=268
146540	http://numazu.jugem.jp/?eid=333
146541	http://rediyudan.jugem.jp/?eid=372

Fig.3. Input Weblog Files in Text File Format

[2] The input file is uploaded on HDFS for processing.

[3] MapReduce Framework is used for processing the input files. In Map phase the input data is converted into intermediate data that is in key value pair as shown in fig 4.

The output shows the string as a key is url of a website and the value denotes number of times the particular url is hit by the user in the data. That is the frequency of each url occurred in the particular data.



```
[cloudera@quickstart ~]$ cat entry.txt |python mapper.py
http://osa3.blog.ocn.ne.jp/project/2004/10/post_13.html 1
http://blog.livedoor.jp/kingcurtis/archives/8211528.html
http://valuation.jugem.cc/?eid=231 1
http://sugamo.jugem.cc/?eid=268 1
http://numazu.jugem.jp/?eid=333 1
http://redkyudan.jugem.jp/?eid=372 1
http://taai.blog.ocn.ne.jp/taai/2004/10/post_14.html 1
http://irisaa.jugem.jp/?eid=27 1
http://brownshoes.jugem.cc/?eid=171 1
http://kiyomasa-hawks.blogzine.jp/annex/2004/11/11.html 1
http://blog.livedoor.jp/nakomaya/archives/8029253.html
http://blog.livedoor.jp/a126blue/archives/8189893.html 1
http://hoku-to.blog.ocn.ne.jp/worker/2004/10/post_55.html
http://zakkankirokunin.blog.ocn.ne.jp/turedurezakki/2004/10/post_8.html 1
http://rockraver.xrea.jp/revirkcor/archives/2004/10/post_27.html 2
http://taai.blog.ocn.ne.jp/taai/2004/10/post_2.html 2
http://redkyudan.jugem.jp/?eid=372 2
http://valuation.jugem.cc/?eid=231 2
http://kiyomasa-hawks.blogzine.jp/annex/2004/11/11.html 2
http://brownshoes.jugem.cc/?eid=171 2
http://hoku-to.blog.ocn.ne.jp/worker/2004/10/post_55.html 2
http://blog.livedoor.jp/jizake/archives/8068991.html 2
http://zakkankirokunin.blog.ocn.ne.jp/turedurezakki/2004/10/post_8.html 2
http://numazu.jugem.jp/?eid=333 2
http://irisaa.jugem.jp/?eid=27 2
http://blog.livedoor.jp/kingcurtis/archives/8211528.html 2
http://blog.livedoor.jp/a126blue/archives/8109893.html 2
http://blog.livedoor.jp/nakomaya/archives/8029253.html 2
34
```

Fig 4. Map phase

- [4] In Reduce phase intermediate data is converted into final output by combining all the values associated with a key as shown in fig 5.



```
[cloudera@quickstart ~]$ cat entry.txt |python mapper.py |sort |python red.py
http://osa3.blog.ocn.ne.jp/project/2004/10/post_13.html 2
http://nekooya.blogzine.jp/area22/2004/11/post_2.html 2
http://sugamo.jugem.cc/?eid=268 2
http://rockraver.xrea.jp/revirkcor/archives/2004/10/post_27.html 2
http://taai.blog.ocn.ne.jp/taai/2004/10/post_14.html 2
http://redkyudan.jugem.jp/?eid=372 2
http://valuation.jugem.cc/?eid=231 2
http://kiyomasa-hawks.blogzine.jp/annex/2004/11/11.html 2
http://brownshoes.jugem.cc/?eid=171 2
http://hoku-to.blog.ocn.ne.jp/worker/2004/10/post_55.html 2
http://blog.livedoor.jp/jizake/archives/8068991.html 2
http://zakkankirokunin.blog.ocn.ne.jp/turedurezakki/2004/10/post_8.html 2
http://numazu.jugem.jp/?eid=333 2
http://irisaa.jugem.jp/?eid=27 2
http://blog.livedoor.jp/kingcurtis/archives/8211528.html 2
http://blog.livedoor.jp/a126blue/archives/8109893.html 2
http://blog.livedoor.jp/nakomaya/archives/8029253.html 2
34
```

Fig 5. Reduce Phase

- [5] Output is moved to Linux file system which is stored in specified directory in text file format. After getting result user can get maximum hit count of particular url of a website.

V. RESULTS AND ANALYSIS

By performing weblog analysis results are come in the form of key value pairs as shown in fig 6. The output of the the program is in key value pair as required.



```
http://osa3.blog.ocn.ne.jp/project/2004/10/post_13.html 12
http://nekooya.blogzine.jp/area22/2004/11/post_2.html 22
http://sugamo.jugem.cc/?eid=268 6
http://rockraver.xrea.jp/revirkcor/archives/2004/10/post_27.html 32
http://taai.blog.ocn.ne.jp/taai/2004/10/post_14.html 6
http://redkyudan.jugem.jp/?eid=372 16
http://valuation.jugem.cc/?eid=231 12
http://kiyomasa-hawks.blogzine.jp/annex/2004/11/11.html 16
http://brownshoes.jugem.cc/?eid=171 22
http://hoku-to.blog.ocn.ne.jp/worker/2004/10/post_55.html 12
http://blog.livedoor.jp/jizake/archives/8068991.html 12
http://zakkankirokunin.blog.ocn.ne.jp/turedurezakki/2004/10/post_8.html 12
http://numazu.jugem.jp/?eid=333 6
http://irisaa.jugem.jp/?eid=276
http://blog.livedoor.jp/kingcurtis/archives/8211528.html 12
http://blog.livedoor.jp/a126blue/archives/8109893.html 12
http://blog.livedoor.jp/nakomaya/archives/8029253.html 37
```

Fig 6. Output Results

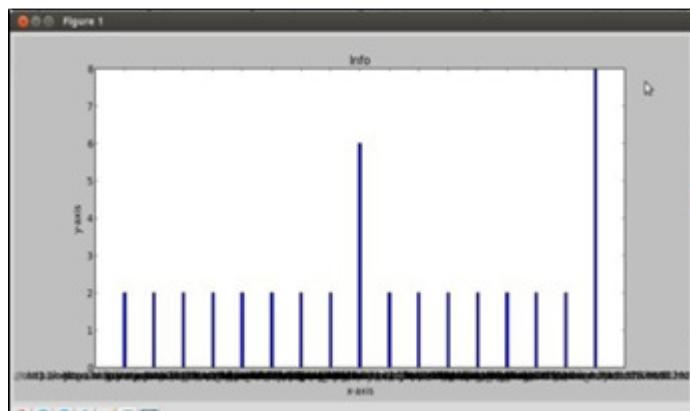


Fig 7. Analysis of Result

From analyzing the results it is possible to conclude about the frequent use of particular url. The graph shown in fig 7 shows the most frequently accessed url. In above graph x-axis denotes link address and y-axis denotes hit count of particular link address. From the graph user can conclude about the frequently accessed url and according to it can make business decisions.

VI. CONCLUSION

In today's internet world, websites are useful source of information. Number of people are accessing to these websites and requesting number of pages, due to which the log files of these websites are getting bigger every day. These log files are convenient source of information to identify the behavior of the user. As developed weblog analysis system is based on Hadoop framework it is capable to process large data sets of weblog. By experiments it is also concluded that by using Hadoop MapReduce framework, current system can perform large size weblog processing and gives efficient results with less efforts and time. As a future work, Hadoop ecosystem like Pig, Flume, Hive for efficient processing of data can be embed in current system to get more effective analysis result and also can use different data visualization techniques to view the results.

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SCSP: Skyline Computation Strategic Planner for Distributed Environment

Abstract - A skyline query helps to identify the best objects in a multi-attribute dataset. A typical skyline provider experiences frequent and near to frequent queries on a popular dataset. When the skyline query attributes are more, dataset is very large and frequent updates are common on a dataset, response time can be remarkably improved if the execution of the subsequent queries is planned with the help of previously available results of the skyline queries.

Through this paper, we present a novel model namely SCSP, a skyline computation strategic planner which is suitable for computing the skylines of queries that are raised against a distributed dataset. The model presents the novel strategies which help to optimize the response time of skyline queries. We then present a novel algorithm based on the SCSP model, the DiSkyline algorithm to optimize the response time of the skylines queries in a distributed environment.

Keywords— Skyline queries; Query profiler; Skyline computation strategic planner

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NPL: A tool for managing processor affinity in network

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Abstract—Message Passing Interface (MPI) is a dominant parallel programming paradigm, used in high performance computing (HPC) applications. It is suitable for distributed memory systems, so MPI processes can be placed on HPC systems which contain nodes in the network. Formerly, placing of MPI processes on nodes in network was easy because nodes used to have a single processor. MPI process placement in HPC system has become difficult because of development in multi-core and many-core systems. Such systems contains multiple cores and different levels of memory hierarchy. On such complex systems, topology aware process placement is possible when the architectural and network details of the systems are known. In literature, various tools are proposed to explore the architectural details, but to explore network details no effective tool is available. In this paper, we have proposed a Network Processor Locality (NPL) tool which explores the topology of the system based on parameters like distance between cores, memory hierarchy between cores, network bandwidth, hop count, propagation delay, packet loss ratio, network contention etc.

Keywords—Topology aware MPI process placement, High performance computing, System topology

I. INTRODUCTION

Message Passing Interface (MPI) is a dominant programming paradigm used in HPC cluster. While executing MPI applications in cluster, multiple processes are created and scheduled for execution in a cluster. The evolution in multi core and many core technologies [1] made the hardware system more complex. Now a days manufacturing of computer system with hundreds or even thousands of cores is possible. 'Sunway TaihuLight' [2] is the highest ranked HPC system developed at National Supercomputing Center China and it has 10,649,600 cores. On such complex systems placing of MPI processes is difficult. To place processes easily and to get benefits of huge computational power of these systems, the architectural details like number of cores, memory hierarchy etc needs to be considered. Also several modern interconnect technologies like Myrinet, Infiniband, Ethernet-based technologies [3] are available which provide bandwidth in Gigabytes. To take advantage of such huge available bandwidth, processes should be placed by considering system topology. Topology aware MPI process placement [4] involves three steps: (i) finding application topology (ii) finding system topology and (iii) topology aware process placement algorithm. In this paper, the emphasis is given on finding system topology. To construct HPC cluster topology various (compute) node parameters as

well as network parameters should be used. In literature various tools are available for finding topology of system but in those only compute node parameters are used and network parameters are not used. We have developed a tool for finding topology of HPC cluster by considering both compute node parameters and network parameters.

The rest of this paper is organized as follows; Section 2 focuses on motivations behind the development of this tool. In section 3 different terminologies related to HPC system topology are elaborated. The implementation details of proposed tool are given in section 4. In section 5, related work is given. In section 6, results are elaborated. Finally in section 7 the conclusion of our work and future plans are given.

II. MOTIVATION

A. Evolution in hardware architecture

There is a huge advancement in architecture of computer systems as illustrated in [5]. The architectural development is mainly in following areas.

- Evolution in processor technology from uniprocessor to dual core processor to multicore processor.
- Hierarchical placement of cores on different socket/s.
- Provision of different level of cache memory.

This is an era of exascale computing where machines with thousands of cores and different levels of memory hierarchies are available. In Table I, list of top five [6] parallel machines is shown. To extract benefits of such huge computing power, processes should be placed by considering architectural details of the node.

TABLE I
TOP FIVE MULTI CORE SERVERS

RANK	SYSTEM	CORES
1	SUNWAY TAIHULIGHT	10,649,600
2	TIANHE-2	3,120,000
3	TITAN - CRAY XK7	560,640
4	SEQUOIA - BLUEGENE/Q	1,572,864
5	K COMPUTER	705,024

B. Evolution in interconnect technology

Modern interconnect technologies provide bandwidth in gigabytes [6] [7]. Table II shows the bandwidth offered by

various interconnect technologies. MPI applications are not benefited from these technologies, as network parameters are not used while placing processes.

TABLE II
MODERN INTERCONNECT TECHNOLOGIES AND THEIR CORRESPONDING
BANDWIDTH

INTERCONNECT TECHNOLOGY	MAX. BANDWIDTH (MB/S)
GigE	112
10GigE	875
DDR InfiniBand	1482
QDR InfiniBand	3230

Various network topologies [7] with different network diameter & hop count are available. To improve performance of applications executing in such networks, topology should be considered. Thus, large scale HPC systems are complex as they contain high configuration servers and high speed interconnection networks.

C. Evolution in MPI process placement

The communication latency reduction is one of the technique to improve the performance of MPI applications. Topology aware process placement technique is used for this purpose. Figure 1 shows that, topology aware process placement improves application performance significantly than default process placement. In [8], [9] [10], effects of topology aware process placement on performance of MPI applications are demonstrated. To achieve performance improvement through topology aware process placement, knowing the topology of underlying system is essential.

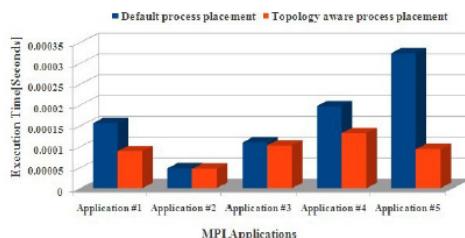


Fig. 1. Performance improvement using topology aware process placement

III. TOPOLOGY OF HPC SYSTEM

A. Parameters of HPC system

HPC system contains compute nodes connected in the network. To construct its topology, compute node parameters as well as the network parameters should be considered. Each parameter has an influence on performance of application executing on the system. Based on the influence of these parameters on application performance, different weights are assigned as shown in Table III. From Table III, first two parameters are of compute node parameters and remaining are network parameters.

TABLE III
PARAMETERS FOR SYSTEM TOPOLOGY AND THEIR CORRESPONDING
WEIGHTS

PARAMETERS	WEIGHT
DISTANCE BETWEEN CORES	6
MEMORY HIERARCHY BETWEEN CORES	5
BANDWIDTH BETWEEN NODES	4
HOP COUNT BETWEEN NODES	3
PROPAGATION DELAY BETWEEN NODES	2
PACKET LOSS RATIO BETWEEN NODES	1

The significance of each parameter in deciding the affinity between cores is given below.

- 1) DISTANCE BETWEEN CORES: The distance parameter indicates the physical distance between cores. If cores are on same socket then distance is 1, if cores are on different socket but on same node then distance is 2 and distance is 3, if cores are on different nodes.
- 2) MEMORY HIERARCHY BETWEEN CORES: If cores have L1 level cache between them then affinity is 3. If cores have L2 level cache between them then affinity is 2, for L3 it is 1 and for cores on different nodes it is 0.
- 3) BANDWIDTH BETWEEN NODES: This parameter indicates the available bandwidth between nodes.
- 4) HOP COUNT BETWEEN NODES: This parameter indicates, number of hops travelled by the message from one node to another node. If nodes are connected under same switch or router then hop count is 1. It is equal to number of switching devices travelled by the message.
- 5) PROPAGATION DELAY BETWEEN NODES: This parameter indicates time taken by the message to travel from one node to another. The affinity between cores is less when propagation delay is more.
- 6) PACKET LOSS RATIO BETWEEN NODES: The packet loss cause resend of message, therefore more time is required for transmission of message between nodes. The less packet loss ratio indicates more affinity between nodes.

B. Affinity between cores

While defining affinity between cores in multicore and network based system various parameters and their impact on processor affinity should be considered. The affinity between cores can be expressed as follows.

$$(c_i, c_j) \in \text{Clust}_A \quad \text{where } i = j = k \text{ if} \quad (1)$$

$$(i) \quad \text{dist}(c_i, c_j) < \text{dist}\{(c_i, c_k), (c_j, c_k)\} \quad (2)$$

$$(ii) \quad \text{mem-hier.}(c_i, c_j) < \text{mem-hier.}\{(c_i, c_k), (c_j, c_k)\} \quad (3)$$

$$(iii) \quad \text{bandwidth}(c_i, c_j) > \text{bandwidth}\{(c_i, c_k), (c_j, c_k)\} \quad (4)$$

$$(iv) \quad \text{hop-count}(c_i, c_j) < \text{hop-count}\{(c_i, c_k), (c_j, c_k)\} \quad (5)$$

$$(v) \quad \text{prop.-delay}(c_i, c_j) < \text{prop.-delay}\{(c_i, c_k), (c_j, c_k)\} \quad (6)$$

$$(vi) \text{ packet-loss}(c_i, c_j) < \text{packet-loss}\{(c_i, c_k), (c_j, c_k)\} \quad (7)$$

Let us assume that, c_i , c_j and c_k are the cores under consideration. Core c_i and c_j can be grouped in same cluster clust_k if they have less distance, memory hierarchy, propagation delay, hop count, packet loss ratio and more bandwidth between them.

C. Topology of HPC system

To represent topology of HPC system we have used weighted graph. Let $G(V, E, W)$ be the weighted graph where V is a set of compute nodes, E be the set of edges between compute nodes and W be the set of weights on edges. To store values of each parameter, matrices are used. Let S be the system topology matrix, its values are calculated as,

$$S[i][j] = A[i][j] \times 6 + B[i][j] \times 5 + C[i][j] \times 4 + D[i][j] \times 3 + E[i][j] \times 2 + F[i][j] \times 1 \quad (8)$$

Where, A , B , C , D , E , F are the matrices of system parameter.

The dimension of node topology matrix depends on number of cores in the node and dimension of network topology matrix depends on number of nodes in the system. The dimension of system topology matrix is dependent on both number of nodes in the system and number of cores in node. So, the dimension (Dim) for system topology matrix is,

$$\begin{aligned} \text{Dim} = & \text{MAX dimension}\{A, B, C, D, E, F\} \times \\ & \text{MAX processors}\{A, B, C, D, E, F\} \end{aligned} \quad (9)$$

D. Data structure for topology

Topology aware process to core mapping is NP-complete problem as shown in [11]. The mapping time depends on data structure used for representation of the system topology and application topology. Therefore we have used a data structure called as dendrogram [12] to represent topology of system. The key benefits of this data structure are,

- It explores the hierarchical nature of the system
- It forms clusters of cores based on various parameters of node as well as network and hence finds the nearest cores.
- Its tree like structure reduces the mapping time
- It is compatible with existing work as it works on data in matrix format

The sample system topology is shown in figure 2 and its corresponding topology in dendrogram format is shown in figure 3.

As shown in figure 2, the system contains four nodes 'Node1', 'Node2', 'Node3' and 'Node4'. Each node has two sockets and on each socket four cores are situated. The 'dendrogram' groups cores socket wise (G1 to G8), node wise (G9 to G12) and system wise (G13) as shown in figure 3.

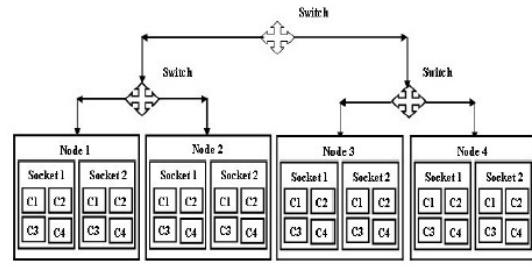


Fig. 2. Sample system topology

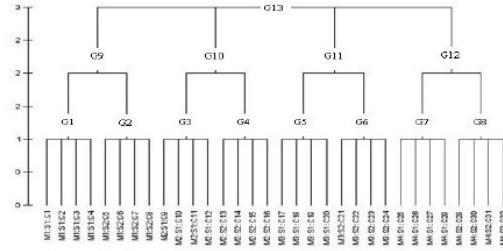


Fig. 3. Sample system topology in Dendrogram format

IV. NETWORK PROCESSOR LOCALITY (NPL) TOOL

A. Architecture of NPL

The architecture of NPL consists of following four layers as shown in figure 4.

- 1) Layer1: It contains nodes from HPC system and network components. The HPC nodes are servers having complex structure of multiple cores and memory hierarchy. The network components are different interconnect technologies and networking devices such as switch, router, hub etc.
- 2) Layer2: It contains tools which are available to explore the topological details of nodes and network. The hwloc tool is used to extract parameters related to the architectural details of a node such as number of cores, physical locality of core, memory hierarchy, size of cache etc. Linux utilities like ping, traceroute, lscpu, lspwd, /etc/proc, etc/sys are used to extract network parameters like bandwidth, propagation delay, packet loss ratio and hop count.
- 3) Layer3: It contains our tool which merges the topology information of nodes, network and produces topological information for entire HPC system. This topological information is stored in matrix format. While producing the final topology matrix weighted sum of all parameters is taken. Weights are assigned to each parameter based on its impact in deciding affinity between the cores.
- 4) Layer4: It contains a hierarchical clustering tool called as 'multidendrogram' [12] which creates cluster of nodes.

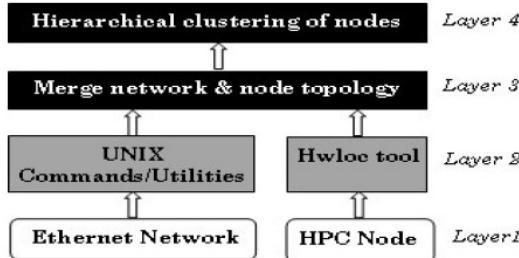


Fig. 4. Architecture of NPL

B. Implementation of NPL

To get information for system parameters, we have used basic utilities from 'linux' and 'hwloc' [13]. The summary of parameters and corresponding utilities is shown in Table IV.

TABLE IV
SYSTEM TOPOLOGY PARAMETERS AND THEIR CORRESPONDING TOOLS

PARAMETER	UTILITIES USED
BANDWIDTH BETWEEN NODES	ping, ipconfig
PROPAGATION DELAY BETWEEN NODES	ping
PACKET LOSS RATIO BETWEEN NODES	ping
DISTANCE BETWEEN CORES	lstopo, lscpu
MEMORY HIERARCHY BETWEEN CORES	lstopo, lscpu

The obtained values of parameters are stored in matrices. The system topology matrix is constructed by taking weighted sum of values from node and network parameter matrices.

Algorithm 1: MergeTopology

Input:
A //Distance matrix(Node parameter)
B //Mem. Hierarchy matrix (Node parameter)
C //Bandwidth matrix (Network parameter)
D //Hop_count matrix (Network parameter)
E //Propagation_delay matrix (Network parameter)
F //Packet_loss matrix (Network parameter)
Output:
S //System topology matrix
Procedure:
Begin
1. A' =ExpandNodeMatrix (A)
2. B' =ExpandNodeMatrix (B)
3. C' =ExpandNetworkMatrix (C)
4. D' =ExpandNetworkMatrix (D)
5. E' =ExpandNetworkMatrix (E)
6. F' =ExpandNetworkMatrix (F)
7. For i=0; i < Dim(S); i++ //Dim(S) is a dimension of system topology matrix and is given in equation (2)
8. For j=0; j < Dim(S); j++
9. S[i][j] = A'[i][j] x 6 + B'[i][j] x 5 + C'[i][j] x 4 + D'[i][j] x 3 + E'[i][j] x 2 + F'[i][j] x 1
//As shown in equation (1)

End

Procedure: ExpandNodeMatrix (X)

X : Node parameter matrix
Begin
1. For i=0 ; i < Dim(X) ; i++
2. For j=0 ; j < Dim(X) ; j++
3. k=j
4. X'[i][j] = X[i][j]
5. X'[i][j+k] = X[i][j]
6. X'[i+k][j] = X[i][j]
7. X'[i+k][j+k] = X[i][j]
8. Return X'

End

Procedure: ExpandNetworkMatrix (Y, d)

Y : Network parameter matrix
d[]: Array of number of cores per node
Begin
1. cores = 0
2. For i=0 ; i < Dim(Y) ; i++
3. For j=0 ; j < d[i] ; j++
4. For k=0 ; k < d[i] ; k++
5. Y'[j+cores] [k+cores] = Y[j][k]
6. cores = cores + d[i]
7. Return Y'

End

To construct system topology matrix, we have used node parameters as well as network parameters. The node parameter matrices are focusing on details of node and network parameter matrices are focusing on details of network. To construct the system topology matrix, expansion and merging of these matrices is required. To expand network parameter matrix we have developed 'ExpandNetworkMatrix' algorithm. The process of expansion is demonstrated in figure 5, where 4x4 network parameter matrix is expanded to 8x8 matrix. Similarly to expand node parameter matrices, we have developed 'ExpandNodeMatrix' algorithm. The process of expansion is shown in figure 6. After expansion of these matrices, the system topology matrix is constructed using 'MergeTopology' procedure from algorithm 1. As the time complexity of NPL tool is $O(N^2)$, it scalable with respect to number of nodes in the system.

V. RELATED WORK

As shown in [14], various tools are available in Linux for finding system topology. These tools provide information about CPU, cache, PCI, data bus, CPU clock etc. To find topology of system, intergeneration of such information from all nodes is required. These tools are useful for node parameters only.

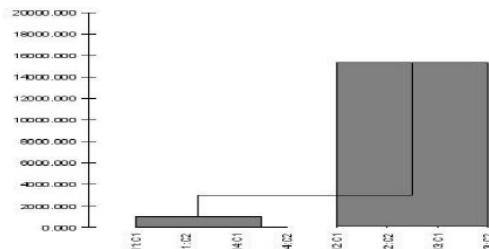


Fig. 8. Dendrogram for system topology

created two clusters as (N 1, N 4) and (N 2, N 3). We have analysed clustering performance of our tool by using minimum distance parameter. As shown in figure 9, our tool creates clusters (N 1, N 4) and (N 2, N 3) with minimum distance which reduces inter node communication latency. Thus it improves performance of application in case of topology aware process placement. Also the accuracy of

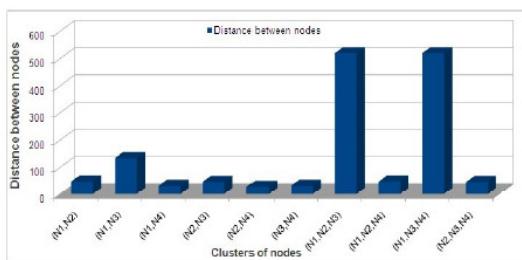


Fig. 9. Performance analysis of NPL tool

NPL tool is compared with existing 'hwloc' and 'netloc' tools by using 'total intra cluster bandwidth' parameter. The results are given in Table VI. NPL creates clusters of processors/cores with maximum intra cluster bandwidth as compared to 'hwloc' and 'netloc' tools.

TABLE VI
 COMPARISON BETWEEN NPL, HWLOC AND NETLOC

Tool	Clusters obtained	Total Intra cluster bandwidth (Mbps)
NPL	(N1,N4), (N2,N3)	160487
hwloc	(N1),(N2),(N3),(N4)	106674
netloc	(N1),(N2),(N3),(N4)	106674

VII. CONCLUSION

As the HPC system consists of nodes connected through network, the network parameters plays an important role in deciding the affinity between the cores in entire system. In our tool we have considered network parameters and their corresponding weights in deciding the affinity between cores. We have obtained correct clustering of cores. If only distance between cores, memory hierarchy are considered like 'hwloc'

and 'netloc' tool, wrong clustering of cores would be generated. Thus performance of MPI program will be not improved. We have come to the conclusion that for standalone system it is sufficient to use tools like 'hwloc' and 'netloc', but for network based system these tools are not effective. Our tool is useful for both standalone as well as network based system. We are planning to extend our tool for heterogeneous system which contains different types of hardware architectures. We want to extend this tool for hybrid system which contains NUMA as well as GPU architecture. Our tool works for Ethernet network and we are planning to extend it for InfiniBand network as well.

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Parallel High Performance Content Based Publish-Subscribe System

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Abstract- Publish-subscribe model is a loosely-coupled paradigm where applications interact indirectly and asynchronously. This model does not maintain fixed communication channel as messages are routed based on their content. Publish/subscribe model is a natural choice for designing large scale systems, where applications from different domains communicate via middle-ware solutions. The key problems in content based publish/subscribe (pub-sub) system lies in an efficient matching of an event against a large number of subscribers on a single message broker. Existing matching algorithms are inherently sequential and unable to exploit multicore, many-core and accelerator framework readily available in current generation computers. Here we present a novel hybrid model for parallel event processing by combining message passing interface and CUDA (MPI-CUDA), the parallel computing platform and programming model, invented by NVIDIA. We compare our work with CCM (Cuda Content Matching Algorithm), a high performance, and parallel content matching algorithm. The result shows approximately 32% improvement in matching latency. This approach works well where the rate of arrival of an event is significant.

Keywords— Matching Latency, MPI-CUDA, High performance, parallel event processing.

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Statistical Machine Translation: Foundations, Challenges and Recent Advances

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Abstract—In general, statistical machine translation (SMT) treats the translation of natural language as a machine learning problem. By examining test samples of human produced conversion, SMT algorithms automatically learn the mechanism. It is observed that, SMT has made tremendous strides in less than two decades and new ideas are frequently introduced. This survey paper presents an overview of the state of the art in the area. The paper describes current research for two main problems namely alignment modeling problem and language modeling problem of phrase based SMT. Along the way, the paper presents taxonomy of few approaches within concerned areas and also enlists some other difficulties in SMT techniques.

Keywords —Machine translation (MT), phrase based machine translation, word alignment, language modeling.

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Underwater Image Enhancement and Color Correction with Wavelet Fusion

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Abstract - As water is 800 times denser than air, degradation of underwater images is different from normal images. In underwater imaging light interact with water in two ways, scattering and absorption. The scattering and absorption of light together is called attenuation of light. This attenuation is wavelength dependent. Because of scattering and absorption, underwater images are affected by back scattering, forward scattering and absorption of light in water. As a result of which the images are degraded, and also dominated by green or blue color. Water is not only source of degradation, but underwater images are also affected by suspended particles and dissolved compounds in water. The degradation also depends upon depth of water, day time, geographical location, source of light and physical properties of water etc. Many underwater applications need clear underwater images. Clear underwater images can be achieved by image enhancement. Underwater image enhancement techniques includes contrast enhancement, blur removal, and color correction techniques. This paper proposed the new method for contrast enhancement and color correction with wavelet fusion. The image quality metrics used in this paper are based on primary and fundamental vision parameters perceived by human vision system. The method is compared quantitatively with some state of art methods.

Keywords: underwater image enhancement, color correction, haze removal, contrast enhancement, wavelet fusion.

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Fuzzy Extreme Learning Machine with Attribute Selection

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Abstract - Classification is one of the major thrusts in the field of Machine Learning and Pattern Recognition. In this research work, the issues of learning speed and generalization performance in classification; attribute subset selection as well as fast learning approaches are discussed. Extreme Learning Machine is one of fastest learning algorithms because the weights between input layers to the hidden layer are assigned randomly and weights between hidden layers to the output layer are determined analytically. Extreme Learning Machine has various advantages like speed, reliability and generalization over traditional neural network classifiers.

Fuzzy Extreme Learning Machine with various attribute subset selection is the main idea of this proposed work. Attribute subset selection methods such as Filter, Wrapper, Hybrid, Embedded and Ensemble are used for evaluation. The main objective of subset selection is to provide faster and more cost-effective model. This also gains, a deeper insight into the underlying processes that generate the data. Clinical datasets such as Statlog Heart Disease and Pima Indian Diabetes dataset from University of California Irvine machine repository are used for experimentation. Simulation results are calculated by using Waikato Environment for Knowledge Analysis (WEKA) and MATLAB 14. In general, the simulation results on the benchmark dataset have shown that the proposed FELM classifier with attribute subset selection is immensely efficient with good performance in undertaking the pattern classification task.

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Accelerating Learning Performance of Facial Expression Recognition Using Deep Learning

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Abstract: We propose a deep learning based approach using Convolution Neural Networks (CNNs). The goal is to classify each facial image into one of seven (e.g., happy, sad, disgust, surprise, angry, fear expressions) facial emotion categories. Deep Convolution neural network (DCNN) with multiple sequences of layers and appropriate settings is an efficient machine learning method. We trained deep model using Kaggle data set. We developed model in Theano and used Graphics Processing Unit (GPU) computation in order to accelerate the training process. The proposed architecture achieves 61% accuracy with Kaggle data-set. GPU has a speedup of approximately 5 times. GPU has great potential as high-performance co-processor.

Smart Street Management And Emergency System

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Abstract- This paper describes about a smart street system. This system is an integration of various modules namely, the accident detection, license plate recognition for vehicles emitting smoke, Fire detection in case of accidents and face recognition for the individuals who are missing or wanted as per the database. The first phase of this system deals with the detection and recognition using image processing and pattern recognition while the second phase deals with the reporting of these events for service from the respective facilitation centers.

Keywords: Filtering, Color, Signal processing, Convolution neural network, Metrics, Metafiles, Image processing ,Routing protocols and layout, Gray values, Computer vision, Server, Clustering algorithms, Video Analysis, Calibrated camera, Error Analysis.

Survey on the Prospective and Significance of Deep Learning in Big Data Analysis

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Abstract— nowadays Big Data describes the strikingly increasing growth in raw data. This data is heterogeneous originated from various sources and having various structures. Most of the data is unstructured and analytics of such unstructured, structured, semi-structured data by using traditional mining algorithm is very crucial. As traditional mining algorithm requires uploading of whole data in a memory for processing and also they are not scalable to process huge complex data. In this paper, a survey on Deep Learning approach for the analysis of Big Data with challenges and future application is discussed. The human brain is capable of processing complex input data; Deep Learning tries to impersonate the same characteristic. Deep Learning model based on high-level abstraction on data. Deep Learning has already proved its ability in speech recognition, computer vision, and natural language processing but it is yet rarely applied on high-dimensional heterogeneous data. Hence, it becomes necessary to check the performance, feasibility of Deep Learning for mining heterogeneous data.

Index Terms— Deep learning, Big Data, heterogeneous data, unstructured data.

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Addressing Presence of Selfish Nodes in MANET Using Trust

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ABSTRACT: MANET routing protocols are prone to various types of denial of service attacks. These protocols work on basic principle that, all participating nodes are honest and cooperative. However this can't be guaranteed all the time [1]. The nodes participating in a network may exhibit selfish behavior. The packet dropping attack by selfish nodes degrade performance of the network radically [3]. Trust is useful for decision making in many applications. Trust based security mechanisms have edge over cryptographic approaches while dealing with behavioral attacks [2]. The research work studies known literature on using trust in MANET routing and key challenges in it [4]. The ORPSN [5] is trusted opportunistic routing protocol. ORPSN chooses trustworthy nodes closer to the destination for packet forwarding. ORPSN packet delivery ratio is 10% more than CORMAN protocol.

Keywords

MANET, Routing Protocol, Security, Trust, Opportunistic Routing.

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Overview on Scheduling of Virtual Resources for Scientific Workflows across Federated Cloud

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Abstract— Scientific applications require large computing power, traditionally exceeding the amount that is available within the premises of a single institution. Scientific experiments are being conducted in collaboration with teams that are dispersed globally. Each team shares its data and utilizes distributed resources for conducting experiments. As a result, scientific data are replicated and cached at distributed locations around the world. These data are part of application workflows, which are designed for reducing the complexity of executing and managing on distributed computing environments. Cloud computing is emerging as a viable platform for scientific exploration. Workflow execution cannot be achieved only by using public clouds or private, because of cost and time factor. In scientific workflow execution resources are to be leased dynamically to fulfill the request in time and cost efficient. So there is a combination of multiple clouds required to fulfill the request. Objective of research is to study and analysis of dynamic provisioning broker policies for multi cloud environments.

Keywords: Scientific Workflow, Cloud Computing, federated cloud, Dynamic provisioning.

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Blink detection using Electrooculography for stress detection

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Abstract- There are several human activities where the awareness and conscious control is a very important factor. The research work has significance in the general field of safety of individual working in monotonous environment like, academic work, data operators with heavy equipment operation, hazardous materials manipulation. Excessive mental workload has negative impact on performance and can lead to errors with disastrous outcome. Therefore developing systems for monitoring the mental workload of operator and reducing the stress of the operator when he is not paying adequate attention to the performance of task is essential in order to prevent system failures.

We have to characterize mental states of operator performance, by finding patterns in timely changing physiological, measures like EOG(ElectroOcculography), with eye blinks and EEG signals.

In this work, suppression of blink artifacts is considered. There are two approaches in blink effect suppression. The first approach is the independent processing of EEG channels like subtracting the filtered electrooculogram (EOG) channel from EEG or using wavelet transform to remove blink artifacts. The second approach uses the correlation of blink functions from different channels. Blink waveform in electrooculogram (EOG) data is used to develop and adjust the method of stress detection in operators and academicians who are engaged in continuous reading and learning operations. We devise different features based on these characteristics and select a subset of them using feature selection. We validate the method using dataset RMS Maximus. We have implemented Blink detection algorithm which is used to study the EOG signal and give explanation of different kind of waveforms in EOG signal, and give suggestions to improve the blink detection algorithm.

Keywords: Eye tracking, Electoocculogram(EOG), Blink detection algorithm , Electroencephalograph (EEG).

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Parallel Implementation of Bellman-Ford Algorithm Using OpenMP

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Abstract— To find out the shortest paths from a single source to all other vertices is a common problem in graph theory. The Bellman-Ford's algorithm is the solution that solves such a single-source shortest path (SSSP) problem in large and complex graph set and it is better suited to be parallelized for many thread architectures. The drawback of sequential Bellman-Ford algorithm is that it is a very time-consuming process for the large number of vertices in a graph. Parallel implementation of Bellman-Ford's algorithm significantly reduces the number of relaxing operation as well as a number of iterations and time required for search the shortest path is minimized. This paper represents a parallel implementation of the Bellman-Ford algorithm based on Pthread using OpenMP (Open Multi-Processing). OpenMP based parallel computing is an alternate way to increase the speed up for search task. This paper gives detailed information about modified parallel implementation of Bellman-Ford's algorithm to solve optimization problem and to improve the work efficiency and performance of GPU-based computing.

Keywords: Bellman-Ford, SSSP, GPU (Graphics Processing Unit), OpenMP (Open Multi-Processing).

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Real time monitoring system for road traffic detection using twitter and spark

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Abstract: In this age of Information and Communication Technology, people rely on social media to get regular updates about world events and happenings. Sites like Google, Facebook and Twitter contribute a lot to give real time information feeds on smart devices. In every second, thousands of events occur around. It necessitates people to receive relevant information on these sites. This paper presents a real time monitoring system which uses Twitter as a source of information. Here we fetch tweets related to road traffic congestion, car accidents etc. and process those tweets by text mining techniques using apache spark. The goal of this system is to assign a class label to each tweet so as to identify whether it is a traffic related tweet. We use Geo-tags (longitude and latitude) to fetch location wise tweets. For classification model we use support vector machine classifier.

Keywords: Traffic event detection, Apache spark, Twitter4j, tweets classification, text mining.

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Secured Deep Learning in Cloud- An Approach

Mr. S. S. Sayyad, Dr. D B Kulkarni

Abstract: Deep Learning is an emerging technique which is used to model, classify, cluster and predict complex datasets involving speech, text, images, videos, etc. The accuracy factor involved in Deep Learning Algorithms has made them into the core of emerging Artificial Intelligence based services. Many organizations which involve dealing with huge amount of data on a larger scale have been the core user of this technique since the success has handled the larger volume of training data quite effectively. When data is involved in huge volume for learning algorithms then it surely produces privacy issues. Many data sources preferably from Medical Hospitals and Institutions that are looking for Artificial Intelligence based learning approaches are willing to produce learning models without comprising on the data privacy issues.

In this paper, we propose a practical system that will enable multiple parties to collaboratively learn a neural network model for a common objective without compromising the privacy issues of the data involved. Our proposed system will provide access control based cloud system for users to upload their data in Cloud. Most complex operations of learning algorithms i.e. Activation Function and Update of the model can be outsourced to cloud based on GPU. We propose to execute our secured deep learning in cloud approach on benchmark datasets.

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