AKSHAY CHANDRASHEKAR RAIKAR

(716) 426-8550 · acraikar@gmail.com · www.linkedin.com/in/acraikar · IEEE INFOCOM 2023 · ePortfolio

EDUCATION

Master of Science in Electrical and Electronics Engineering **Aug 2021 – Feb 2023**

University at Buffalo, The State University of New York *GPA: 3.83/4.0*

Bachelor of Engineering in Electronics and Communication **Aug 2016 – Aug 2020**

Visvesvaraya Technological University, Bengaluru, India *CGPA: 8.10/10*

EXPERIENCE

Wireless Support Engineer **Mar 2023 - Present**

Tarana Wireless, Inc.

- As a Technical Support Engineer, I adeptly resolve intricate wireless, network, and product-related challenges for a diverse global client base.
- The role involved problem solving skill in addressing various RF related issues by analyzing link metrics like Pathloss, Rx Signal Levels, Interference Noise ratio, TBER etc.
- Collaborated seamlessly with cross-functional teams—Engineering, Management, Sales, and Logistics to escalate issues, report bugs, and propose feature requests.
- Engaged in creation and maintenance of a comprehensive internal and customer knowledge base, resulting in reduced average resolution time and fewer recurring cases.

TECHNICAL SKILLS

Languages: MATLAB, Python, Verilog
Certifications: 4G /5G Network Fundamentals, Aruba Networking, Data Structures, Deep Learning
Core Competencies: CBRS**,** LTE - protocol stack, call flow, OFDM, NR Architecture, Ambitious and Motivated
Equipments: Vector Network Analyzer, Oscilloscope, Multi-meter, USRP B210 SDR

PROJECTS

Graduate Student Researcher **Nov 2021 – Jun 2022**

Wireless Networks 4 Smart Systems Lab, University at Buffalo

- Collaborated with a team of 3 to develop a low cost, low complex, LTE enabled Micro Phasor Measurement Unit (μPMU) by employing ARM Cortex-M3 Microcontroller.
- Utilized internal timers of Arduino to bring down form factor and performed data transmission from multiple μPMUs to Phasor Data Concentrator (AWS Linux Cloud) by deploying UDP and assessed end-to-end delay.

Implementation of srsRAN **Jun 2022 – Jan 2023**

Wireless Intelligent Networking and Security Lab, University at Buffalo

- Integrated srsRAN, an open-source 4G/ 5G software radio suite to implement a flyable cellular network using SDR (Software Defined Radio) carried by an octocopter UAV.
- Increased scalability of the 4G LTE network by connecting multiple srsENB to a srsEPC and achieved 3 times more performance with Intel Nuc as srsENB compared to Raspberry Pi 4.

Wireless Communication Simulations using MATLAB **Feb 2022**

- Simulated BPAM, 4-PAM and 4-QAM digital modulation schemes with optimum receiver detection and examined results using BER vs SNR curves.
- Generated four independent Rayleigh fading channels using Jake's fading simulator and plotted number of channel samples to channel amplitude in dB scale to review slow and fast fading.
- Simulated MISO (2x1) and MIMO (2x2) system using maximum likelihood decoding at the receiver and compared system performance by tracing BER vs SNR curve.

3-section Microstrip Directional Coupler using ADS **Nov 2021**

- Designed a 20dB parallel line 3-section coupler with a center frequency of 3 GHz and bandwidth of 4 GHz.
- Realized directivity of 15 dB with low Insertion loss by compensating parasitic capacitances and inductances.