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1. Purpose

Define the In-Process and End-of-Line tests and the engineering drawing notes that will trigger these tests for electric motor sub-assemblies and assemblies.

2. Scope / Applicability

Who - Engineering, Process, Production, Quality

What - EWP drawings - parts and assemblies

When - Manufacturing - In Process and End of Line

Where - Gates Windsor

3. Responsibility

Documentation - Design Engineering

Awareness / Compliance - Process, Manufacturing, Quality

4. Related Documents

Governing Documents - None

Other Reference Documents - Test requirements from customer specifications

5. Definitions / Abbreviations

5.1. ABBREVIATIONS

IP - In Process

EOL - End of Line (Assembly Line)

DMM - digital multimeter

BEMF - back- emf (electromotive force, measured in volts)

VDC - volts DC

DUT - device under test

5.2. DEFINITIONS

U, V, W terminals - the 3 terminals on the motor housing that organize and provide power to

the 3 phase windings

Back-EMF - Voltage which is generated by the field from the permanent magnets

moving in the vicinity of the stator coils

6. Drawing Notes to Trigger In-Process and/or End-of-Line Electrical Tests

6.1. Foreword

This document defines requirements for EWP In-Process and End-of-Line Tests. It also specifies specific drawing notes. Example drawings notes are provided directly after the requirement.

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6.2. Background

In-process and End-of-Line tests will be used to periodically qualify assemblies and sub assemblies before going to the next stage of production. The intent is to minimize adding value to parts that show early signs of downstream nonconformance.

6.3. <u>Drawing Notes</u>

Drawing notes are organized by test type, whether electrical or controller related.

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DRAWING NOTES

1. DRAWING NOTES - ELECTRICAL IP TESTS

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DRAWING NOTES SHALL BE SHOWN ON SUB-ASSEMBLY OR FINAL ASSEMBLIES

1.1. PHASE RESISTANCE

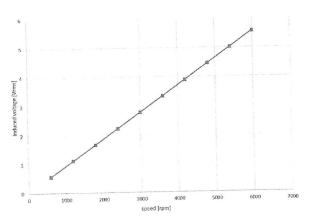
- 1. Phase resistance shall be measured across terminal pairs UV, UW, and VW. Measurement is to be made in milliohms, DMM precise to 2 or more decimal places.
- 2. Phase resistances shall be noted on the sub-assembly drawing, example "Phase resistance 7.0 7.6 m Ω ".
- 3. The differences in phase resistance between UV, UW, and VW shall be evaluated from the data.

1.2. PHASE INDUCTANCE

- 1. PHASE inductance shall be measured across terminal pairs UV, UW, AND VW. Measurement is to be in micro-henry, LCR meter to be precise to 2 or more decimal places. Phase inductance shall be noted on the sub-assembly drawing, example " $26.6~\mu\text{H}$ $27.1~\mu\text{H}$."
- 2. The differences in phase inductance between UV, UW, and VW shall be evaluated from the data, and shall be less than 1.5% of the value between each phase.

1.3. BACK EMF - POWERED MOTOR

- BACK EMF (BEMF), shall be measured across terminal pairs UV, UW, AND VW. Measurement is to be in VDC, precise to 2 or more decimal places. BEMF shall be specified in a table on the partial assembly drawing for various speeds.
- 2. The differences in BEMF between UV, UW, and VW shall be evaluated from the data.



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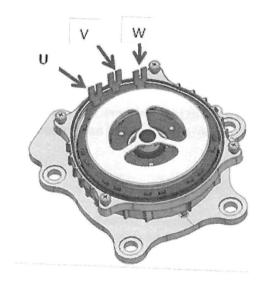
FIGURE 1: Typical BEMF Output

2. DRAWING NOTES - MOTOR AND CONTROLLER EOL TESTS

- 2.1. MOTOR RESPONSE TO CONTROLLER OUTPUT (NO LOAD / DRY TEST)
 - Motor and controller response (to a speed command), and current draw shall be verified at 3 input voltages and ≥ 2 operating speeds and meet the requirements of the assembly drawing.

TEST AND EVALUATION PROCEDURES

3. TEST AND EVALUATION PROCEDURES - INLINE ELECTRICAL TESTS



3.1. PHASE RESISTANCE

- 3.1.1. PURPOSE Confirm acceptable phase resistance on sub-assembly
- 3.1.3. PROCEDURE
 - 1. Set DMM to measure resistance
 - 2. Zero the meter

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- 3. Set computer to begin test
- 4. Connect and measure resistance across motor U & V terminals
- 5. Hit button on screen for resistance recording of phases $\dot{\textbf{U}}$ and V
- 6. When prompted, disconnect phase V and connect phase W
- 7. Hit button on screen for resistance recording of phases U and W
- 8. When prompted, disconnect phase U and connect phase V
- 9. Hit button on screen for resistance recording of phases V and W
- 10. Disconnect test leads from phase terminals
- 11. Resistance measurement data must meet the value on the applicable assembly drawing
- 12. Compliant samples may proceed to the next production station. Failures shall be routed into the reject bin for examination. Failures may be retested if results or procedure seems questionable

3.2. DIFFERENCE IN PHASE RESISTANCE

3.2.1. PURPOSE

Calculate balance of phase resistances between the UV, UW and VW phases

3.2.2. EQUIPMENT

Computer Labview software to calculate the difference between the measurements above

3.2.3. PROCEDURE

- 1. Calculate the difference between steps 4 & 7 above
- 2. Calculate the difference between steps 4 & 9 above
- 3. Calculate the difference between steps 7 & 9 above

3.3. PHASE INDUCTANCE

3.3.1. PURPOSE

Confirm acceptable phase inductance

3.3.2. EQUIPMENT

Inductance meter capable of measuring μH and data recording computer Non conductive workbench No metallic objects within 50cm of the DUT

3.3.3. PROCEDURE

- 1. Zero the LCR meter
- 2. Set computer to begin test
- 3. Connect and measure resistance across motor U & V terminals
- 4. Hit button on screen for inductance recording of phases U and V
- 5. When prompted, disconnect phase V and connect phase W

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- 6. Hit button on screen for inductance recording of phases U and W
- 7. When prompted, disconnect phase U and connect phase V
- 8. Hit button on screen for inductance recording of phases V and W
- 9. Disconnect test leads from phase terminals
- 10. Inductance measurement data must meet the value on the applicable assembly drawing
- 11. Compliant samples may proceed to the next production station. Failures shall be routed into the reject bin for examination. Failures may be retested if results or procedure seems questionable

3.4. **DIFFERENCE IN PHASE INDUCTANCE**

3.4.1. PURPOSE

Calculate balance of phase inductance between the UV, UW and VW phases

3.4.2. EQUIPMENT

1. Computer Labview software to calculate the difference between the measurements above

3.4.3. PROCEDURE

- 1. Measurements versus the programmed values from drawing will be compared
- 2. Inductance measurement data must meet the value on the applicable drawing
- 3. Compliant samples may proceed to the next production station. Failures shall be routed into the reject bin for examination. Failures may be retested if results or procedure seems questionable.

3.5. **BACK EMF**

3.5.1. PURPOSE

Verify acceptable BEMF levels at defined speeds

BACK EMF – POWERED ROTOR / PWM CONTROLLED

EQUIPMEMENT

- 1. Power supply capable of providing 20.0 \pm 0.1 VDC
- 2. DMM precise to 2 or more decimal places

3.5.2.2. PROCEDURE

- 1. Zero the meter
- 2. Connect the 3 phase test leads from the controller to the housing sub-assembly
- 3. Apply 12.5 VDC power and appropriate command signal to the test stand control board
- 4. Begin the test on the computer by hitting button
- 5. the computer will send a PWM command to the controller to spin the rotor at preprogrammed speeds (shown on drawing)
- 6. Equipment will automatically measure the voltage generated across motor U, V $\&~\mathrm{W}$

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- 7. Equipment will automatically increase speed to next test step and repeat step 5 until test is completed
- 8. BEMF measurement data must meet the value on the applicable assembly drawing
- 9. Disconnect leads from the U, V and W phases
- 10. BEMF data must meet the values on the applicable assembly drawing
- 11. Compliant samples may proceed to the next production station. Failures shall be routed into the reject bin for QC examination. Failures may be retested if results or procedure seems questionable.

3.6. DIFFERENCE IN BACK EMF

3.6.1. PURPOSE

Verify the balance of BEMF measurements across the 3 phases

- 3.6.2. EQUIPMENT
 - 1. Computer will automatically calculate the differences between phases

4. TEST AND EVALUATION PROCEDURES - EOL MOTOR AND CONTROLLER TESTS

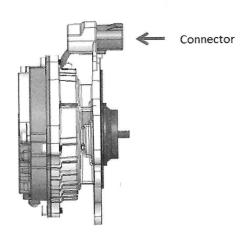
4.1. MOTOR RESPONSE CHECK

4.1.1.1. PURPOSE

Check motor response to the software speed commanded by the controller board when the motor is not loaded

4.1.1.2. EQUIPMENT

- Power source capable of providing minimum 20.0 ±0.1 VDC
- PCB controller PWM input command
- PCB controller output PWM analyzer
- DMM capable of measuring voltage



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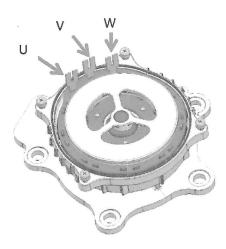
4.1.1.3. PROCEDURE

- 1. Power the EWP with 13.5 VDC
- 2. Start test by pushing start button on computer screen
- 3. The computer will command the pump assembly to run at the lowest speed
- 4. Measure the pump controller PWM output (amplitude and frequency, input voltage and current.
- 5. Equipment will automatically increase speed to next test step and repeat step 4
- 6. Measurement data must meet the values on the applicable assembly drawing
- 7. Repeat steps 2-6 at 9.0VDC
- 8. Repeat steps 2-6 at 16.0VDC
- 9. Compliant samples proceed to the next production station. Failures shall be routed into the reject bin for QC examination. Failures may be retested if results or procedure seems questionable.

Document Revision History

Revision	Data		
INC VISIOII	Date	Revision Notes	A
A 9/28/2020		Author	
	3,23,2020	Initial Release	George Spehar
			Be openal

Step 1: Connect the measurement equipment leads to the corresponding phase terminals of the motor assembly as shown in picture below.



Step 2: Measure the following

a) Phase resistance

Phase resistance value should be within the range of ------

b) Phase Inductance

Phase inductance value should be within the range of -----

Step 3: Power the motor and control it to run from 300 RPM to 6000RPM and measure the following

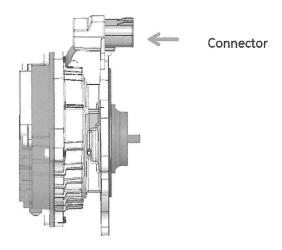
a) RPM vs. Current

Current vs. RPM values should be within the range as shown in the following graph

b) Back EMF

Back emf vs. RPM values should be within the range as shown in the following graph

Step 4: Connect the tester connector to the pump as shown below.



Step 5: Check the following parameters

a) Communication check: send signal & return signal

RPM	Sent signal	Return signal
600	tbd	tbd
3500	tbd	tbd
4500	tbd	tbd
6000	tbd	tbd

b) PWM vs. RPM

RPM	Sent signal	Return signal
600	tbd	tbd
3500	tbd	tbd
4500	tbd	tbd
6000	tbd	tbd

c) Voltage Check

Conduct above tests at high voltage (tbd)

Conduct above tests at low voltage (tbd)