Definition. Given a graph $\langle \mathcal{V}, \mathcal{E} \rangle$ and a labeling of each edge $e \in \mathcal{E}$ with a poset R_e , the *design complexity* $DC(\langle \mathcal{V}, \mathcal{E} \rangle)$ is defined as

$$DC(\langle \mathcal{V}, \mathcal{E} \rangle) = \min_{F \text{ is an AFS}} width(\prod_{e \in F} \mathbf{R}_e).$$