Definition. Given a graph $\langle \mathcal{V}, \mathcal{E} \rangle$ and a labeling of each edge $e \in \mathcal{E}$ with a poset R_e, the *design complexity* $\mathrm{DC}(\langle \mathcal{V}, \mathcal{E} \rangle)$ is defined as

$$\mathrm{DC}(\langle \mathcal{V}, \mathcal{E} \rangle) = \min_{F \text{ is an AFS}} \mathsf{width}(\prod_{e \in F} R_e).$$