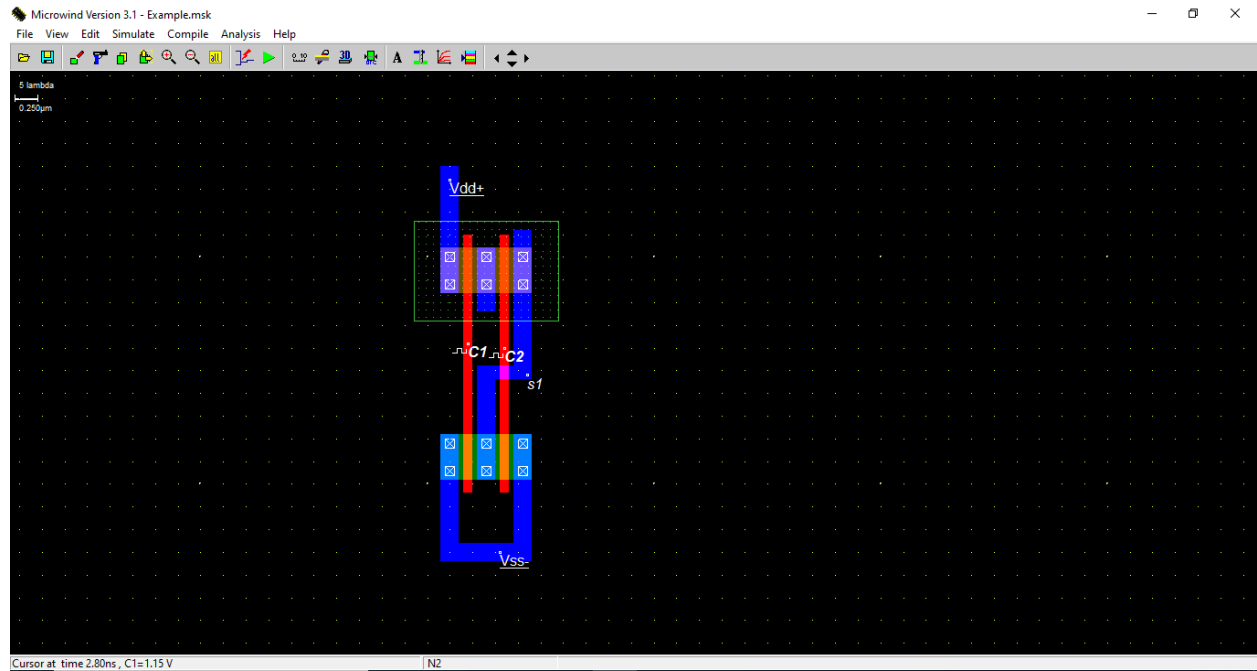
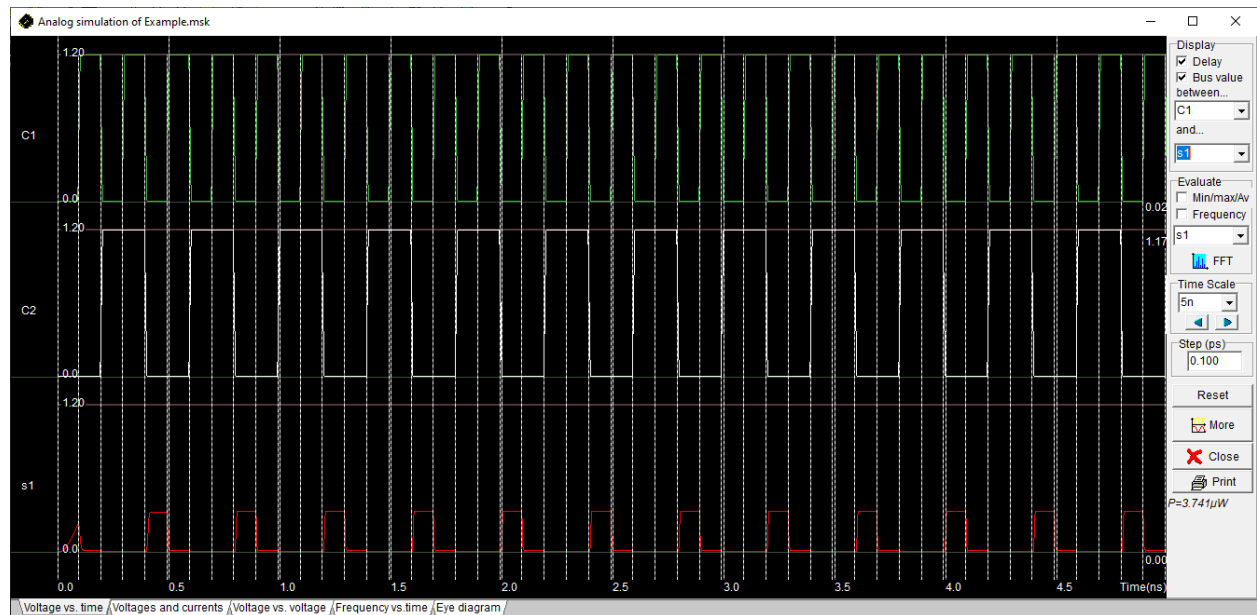


VLSI_pract6

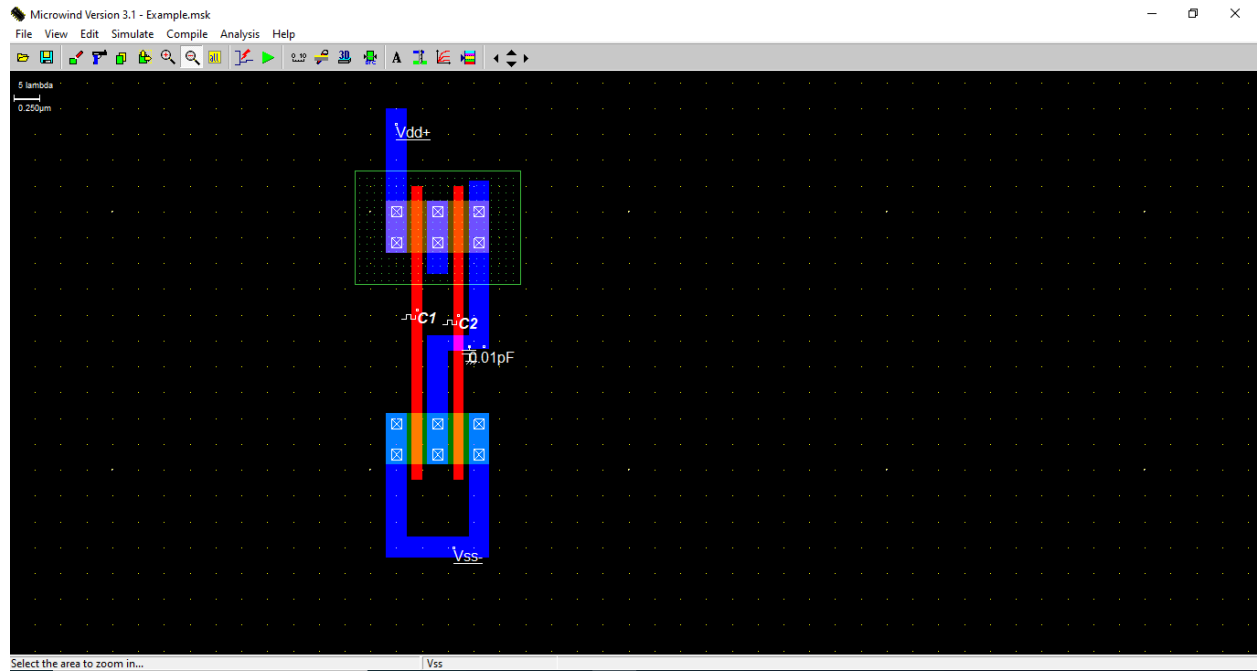
NOR without capacitor -



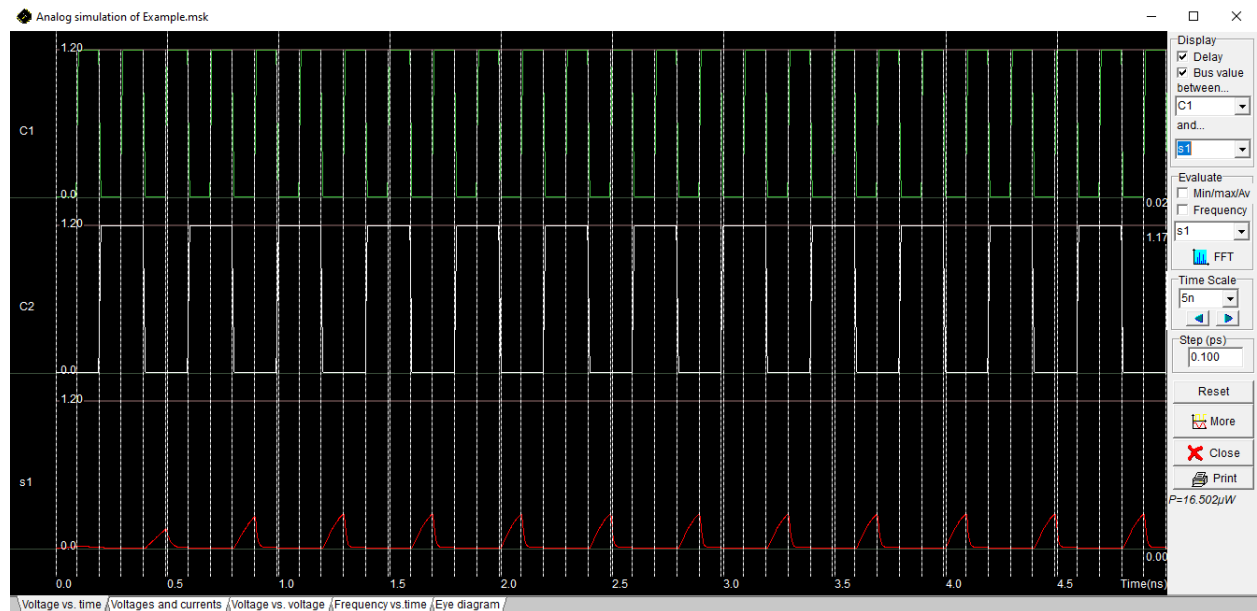
Output -



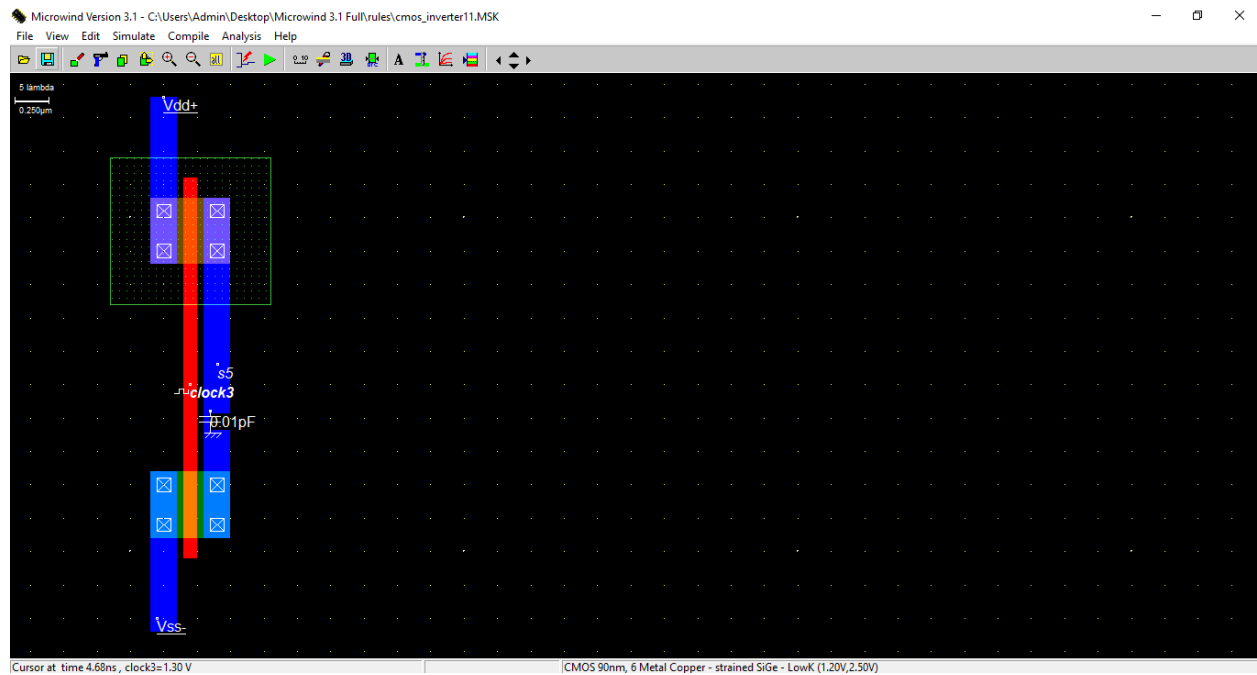
NOR with capacitor -



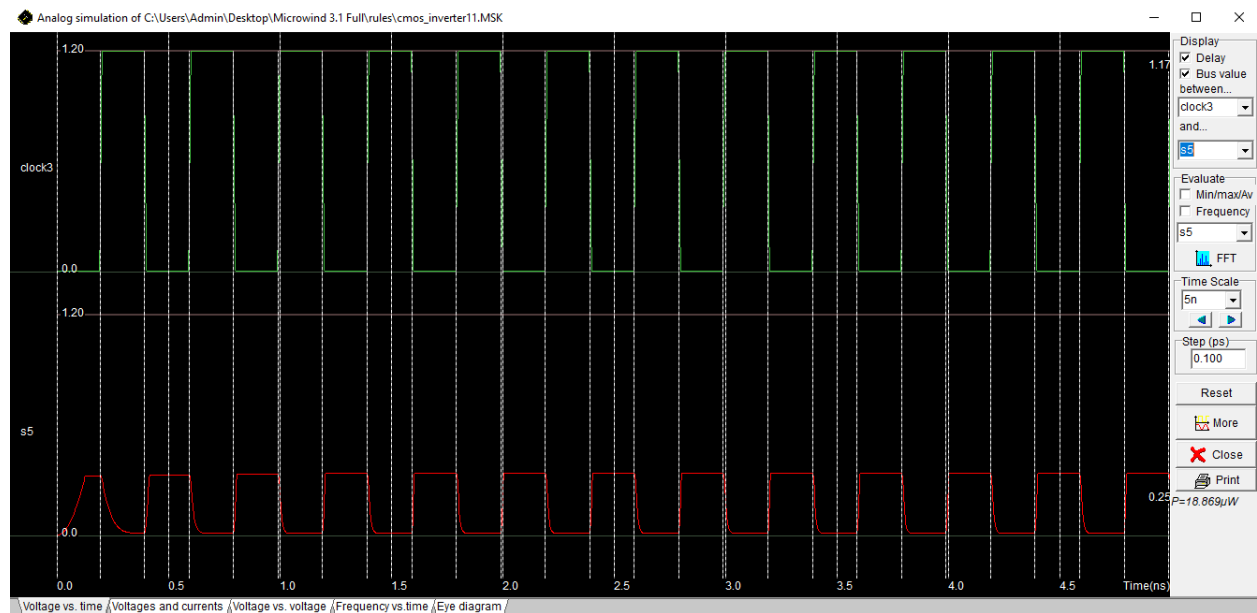
Output -



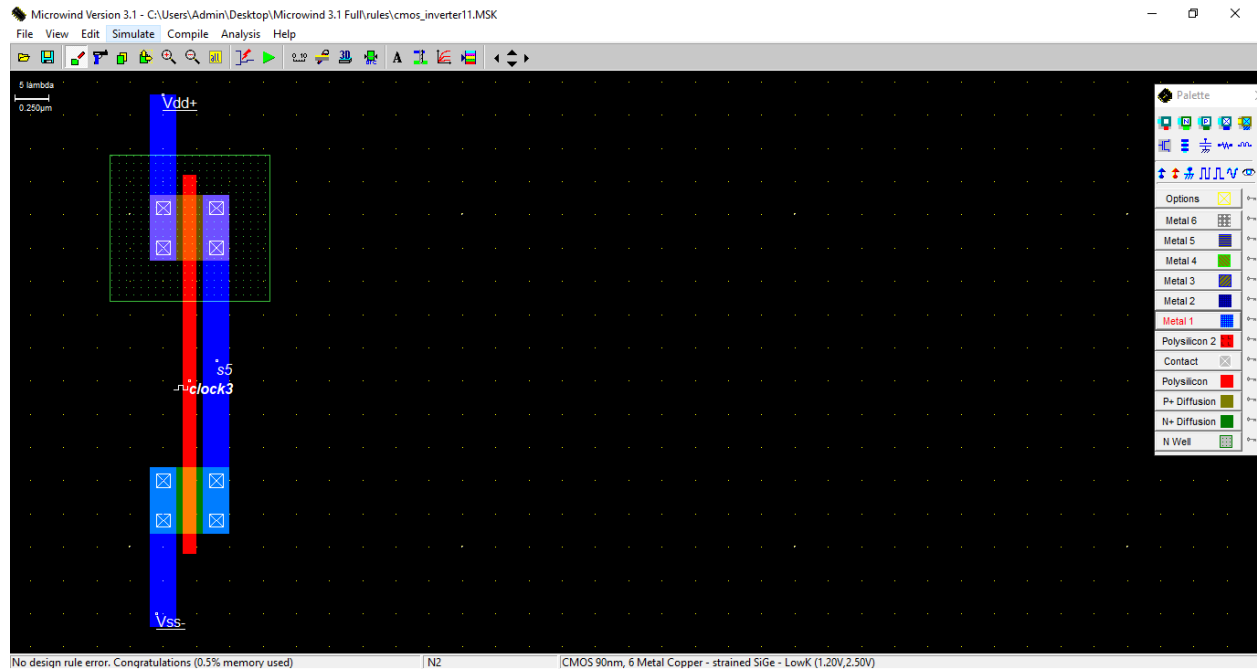
Inverter with capacitor -



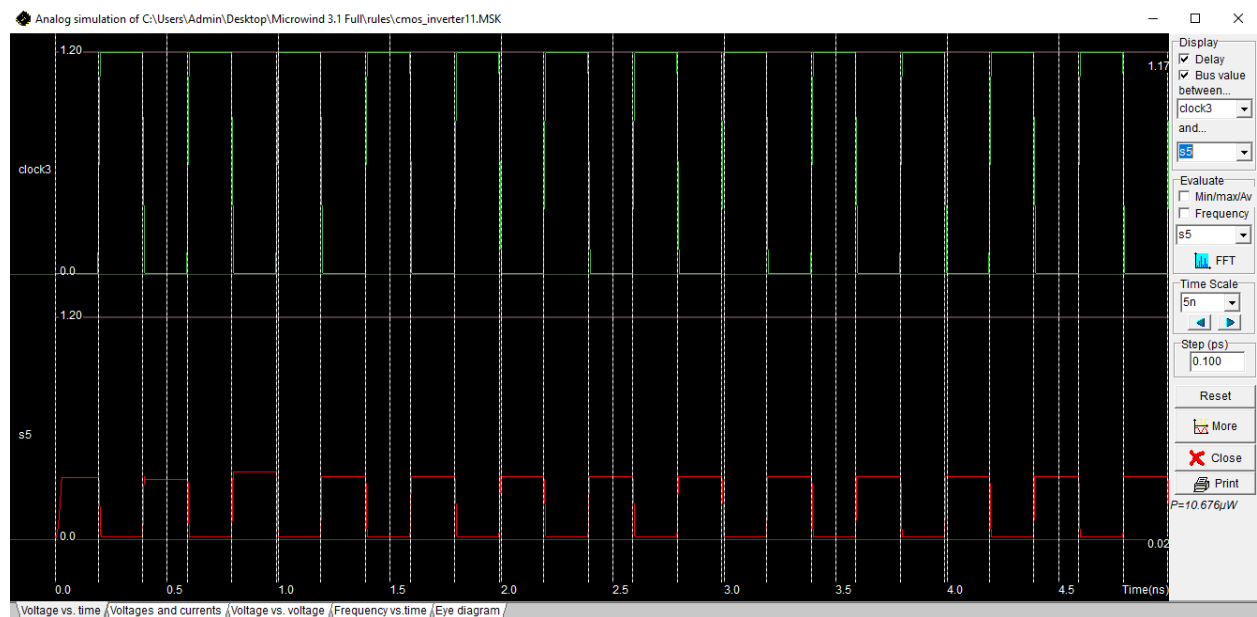
Output -



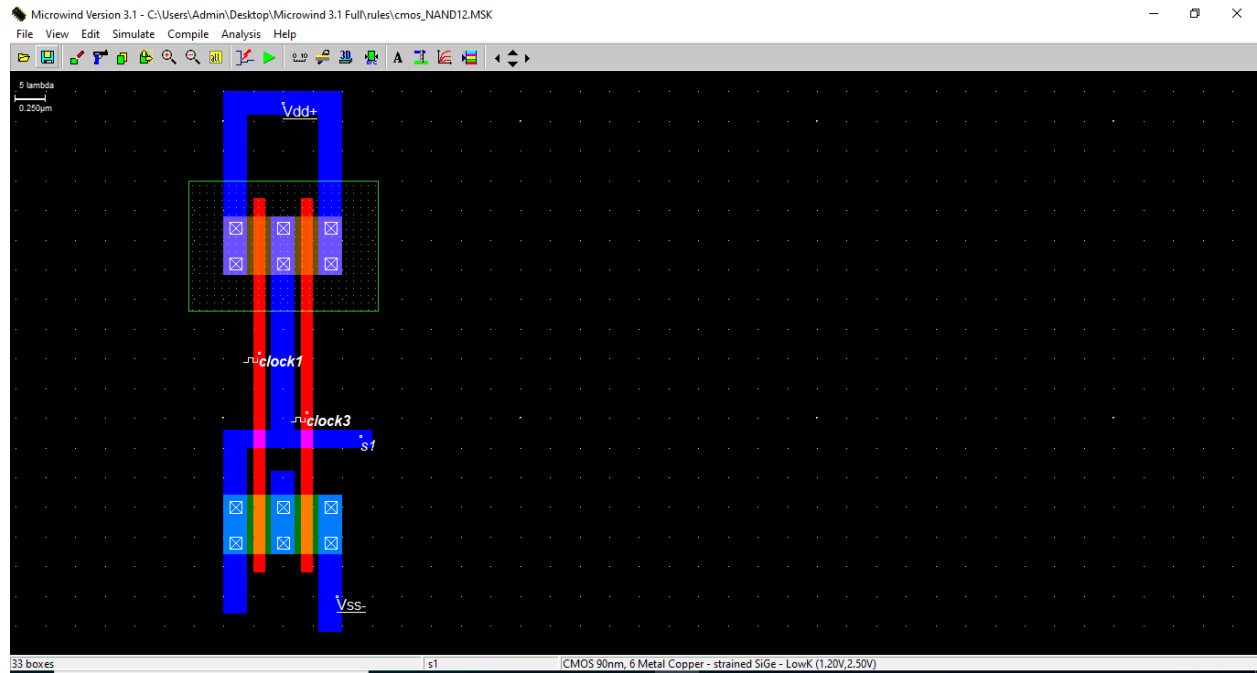
Inverter without capacitor -



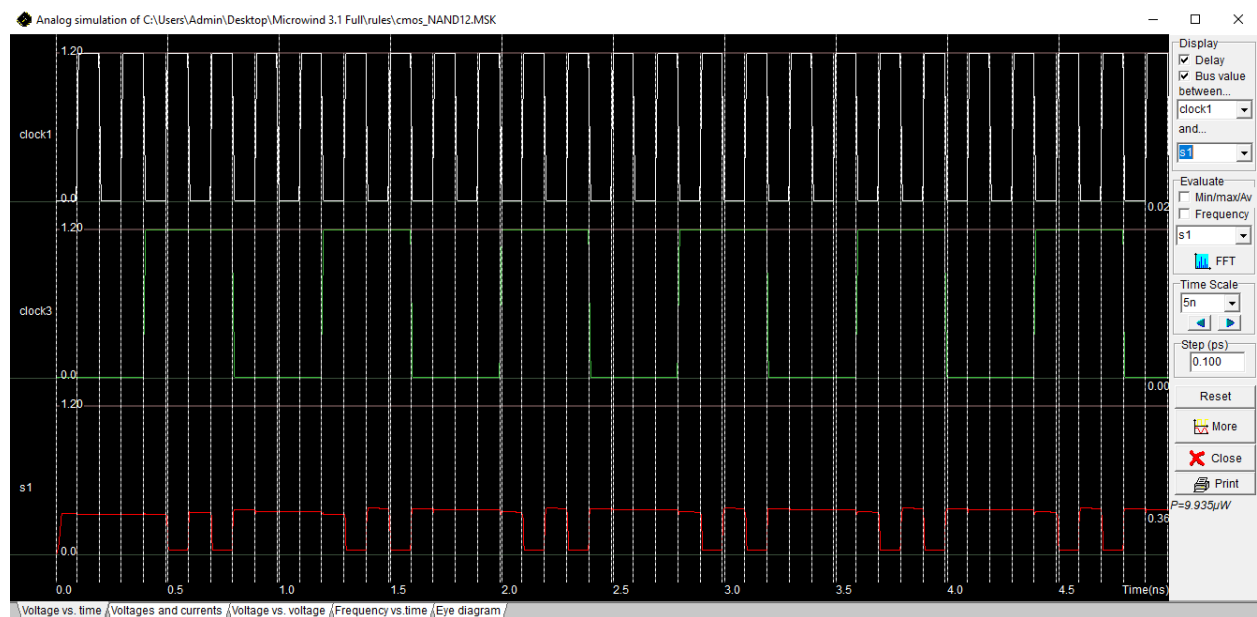
Output -



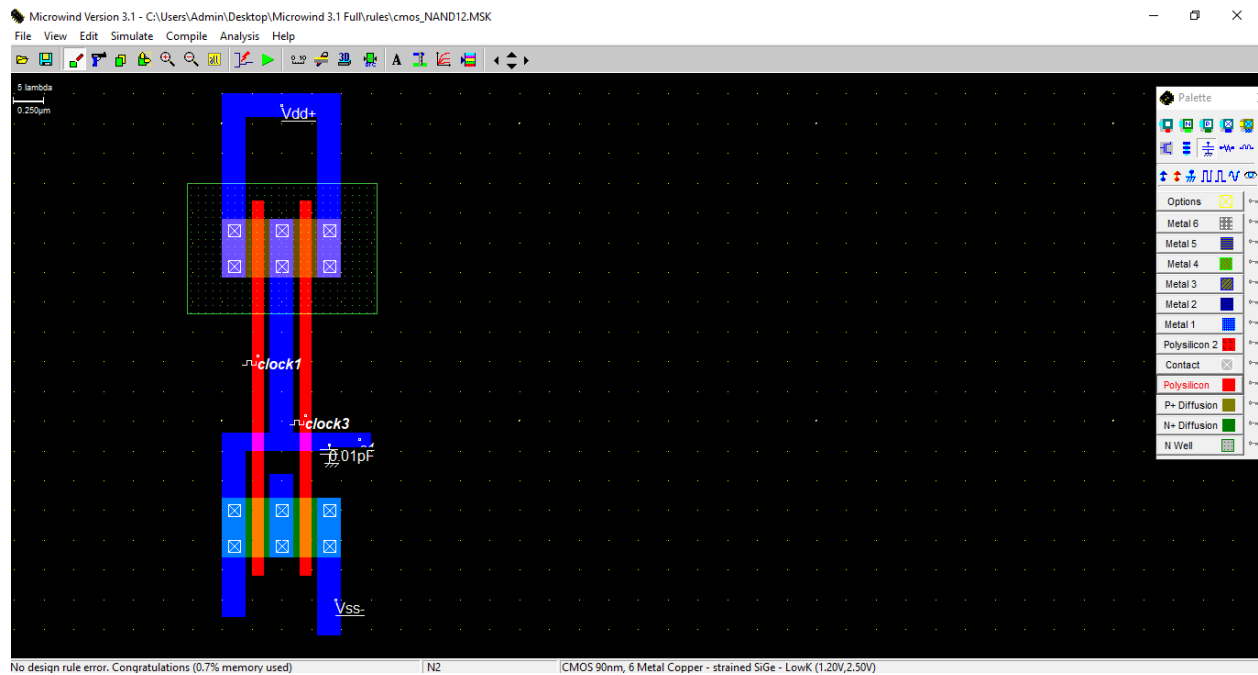
NAND without capacitor -



Output -



NAND with capacitor -



Output -

