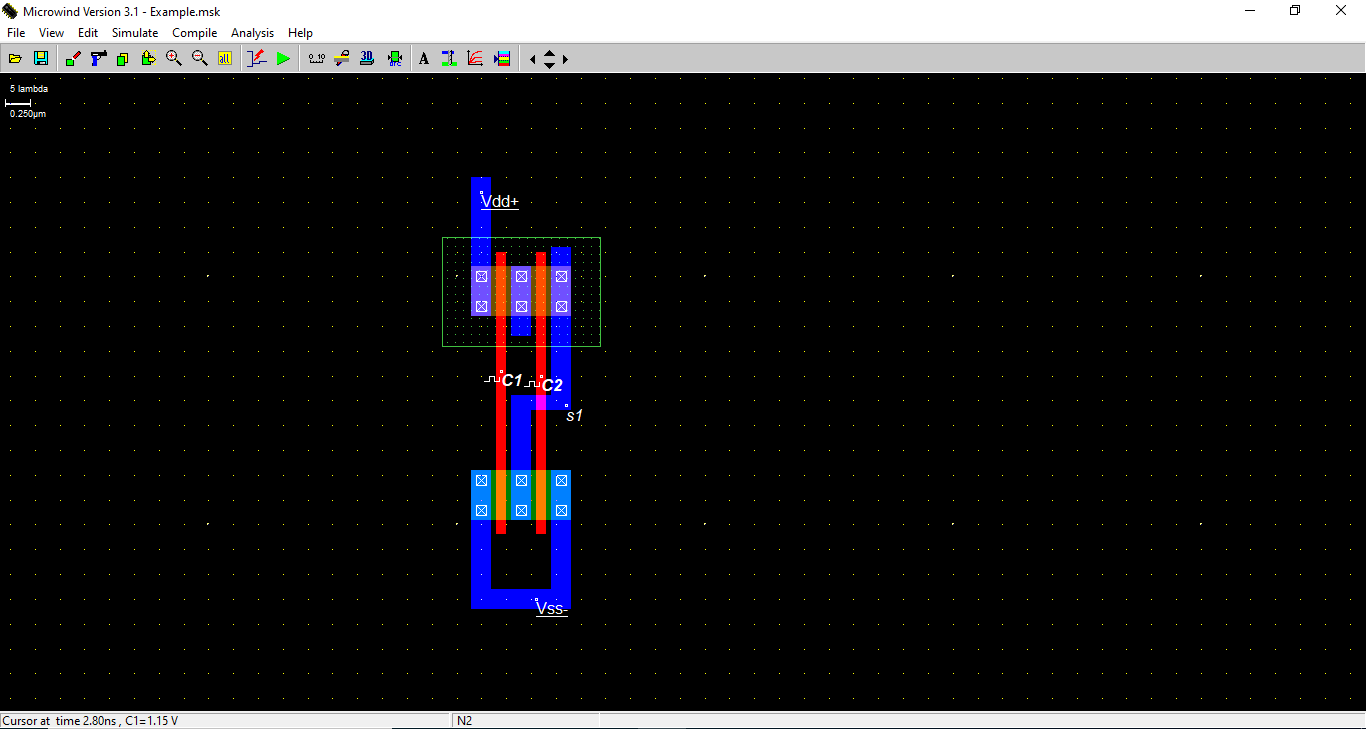
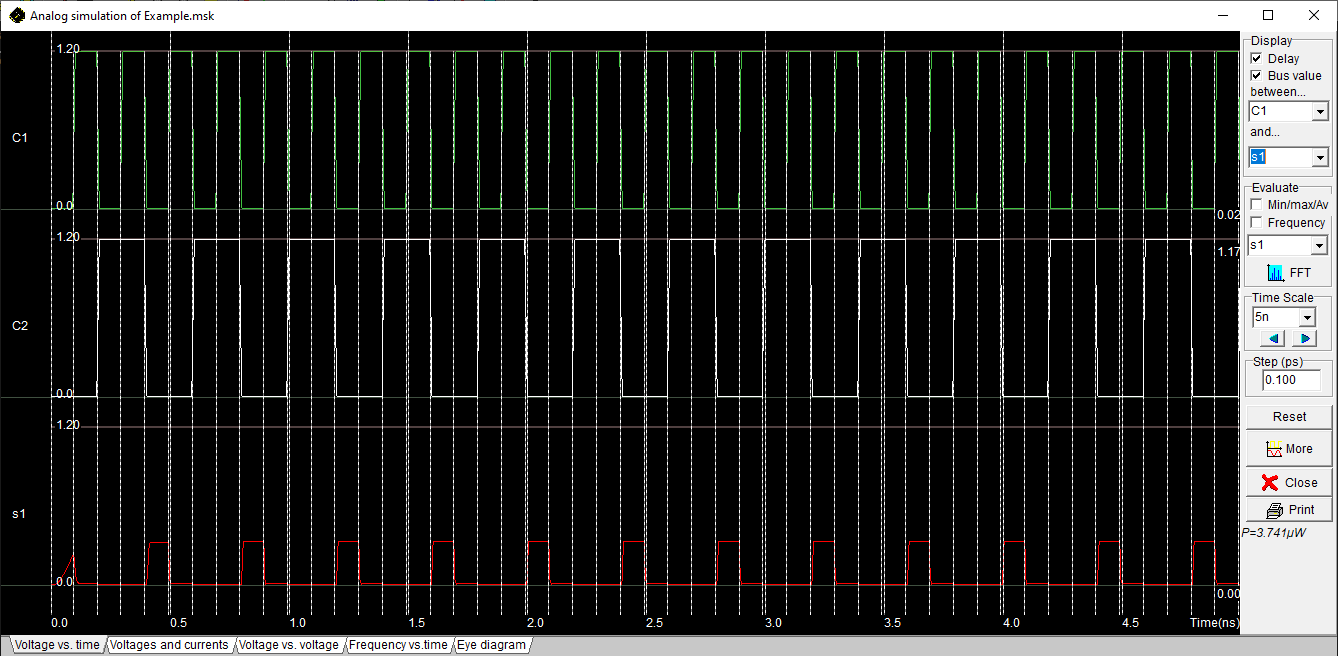
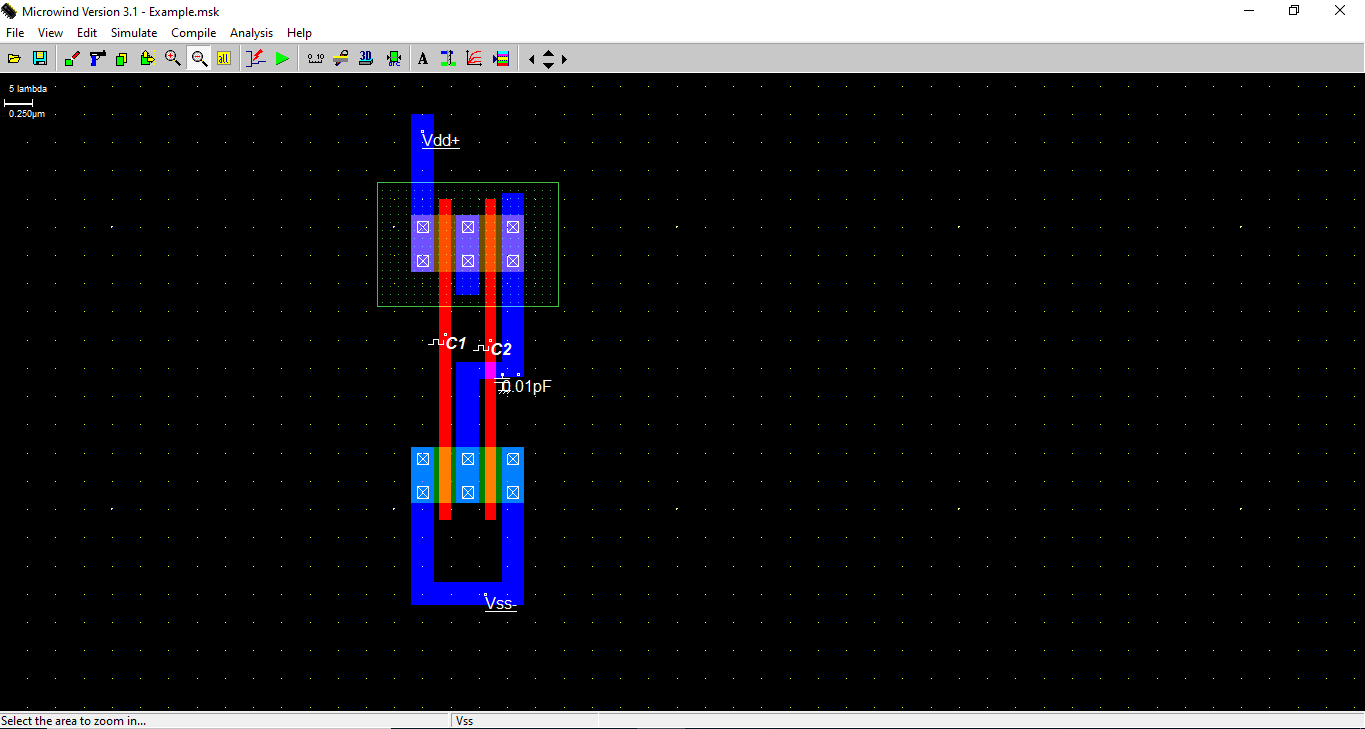
**NOR without capacitor -**

****

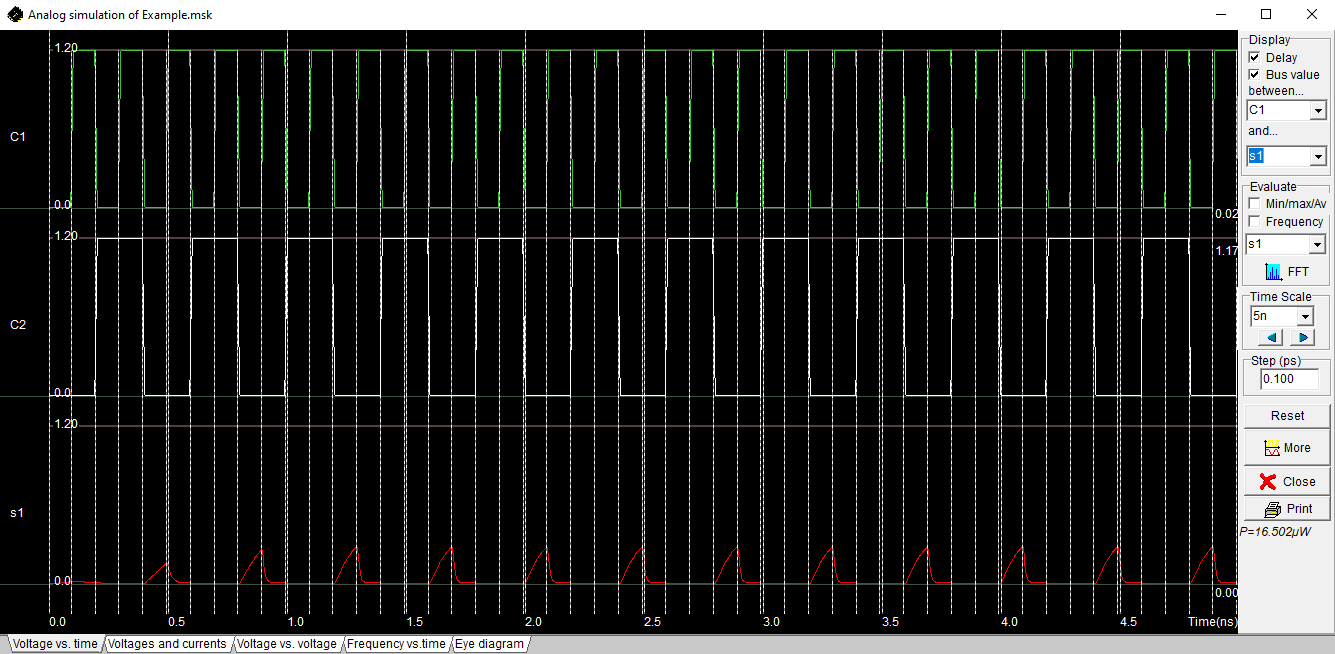
**Output -**

****

**NOR with capacitor -**

****

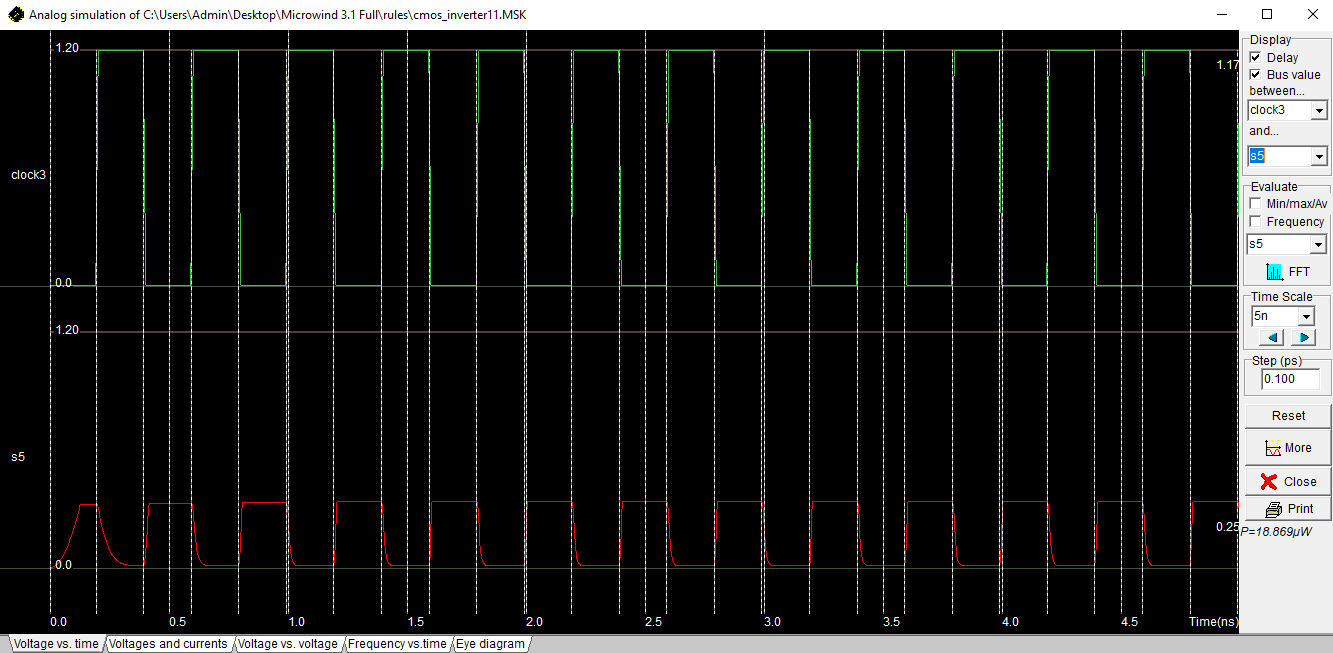
**Output -**

****

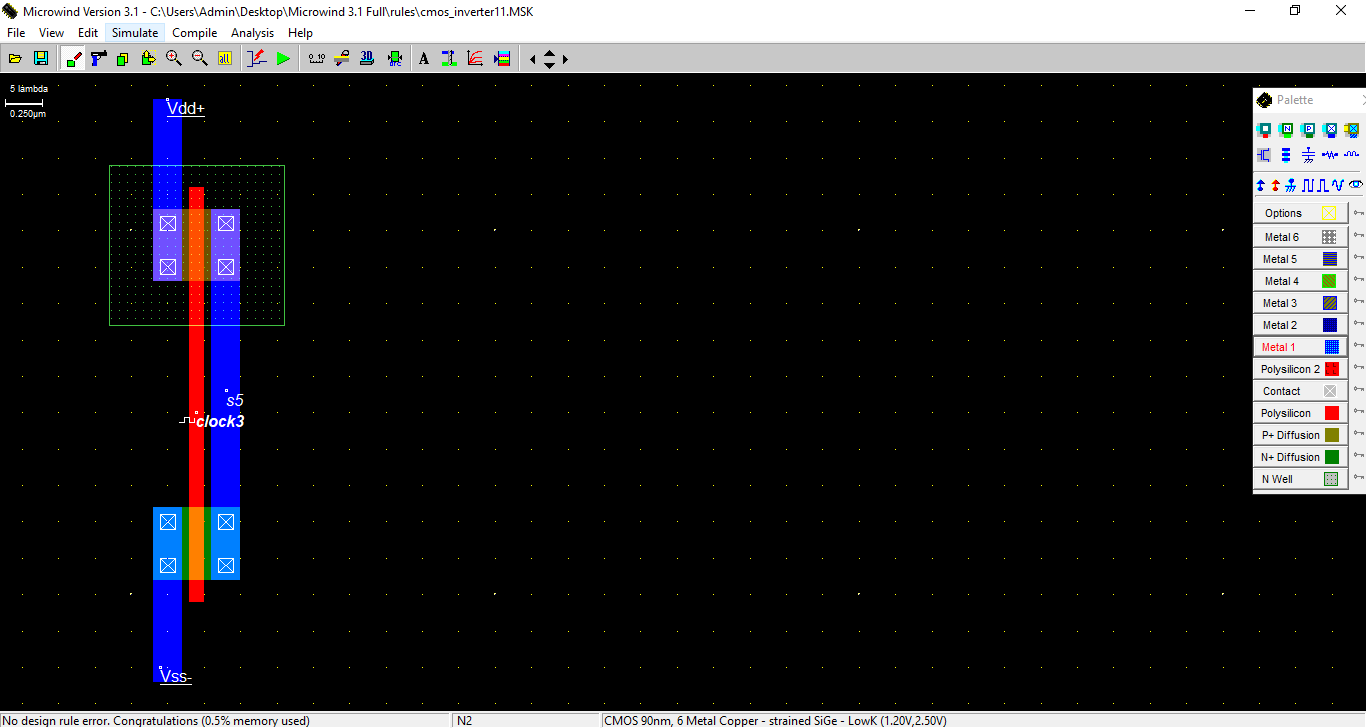
**Inverter with capacitor -**

****

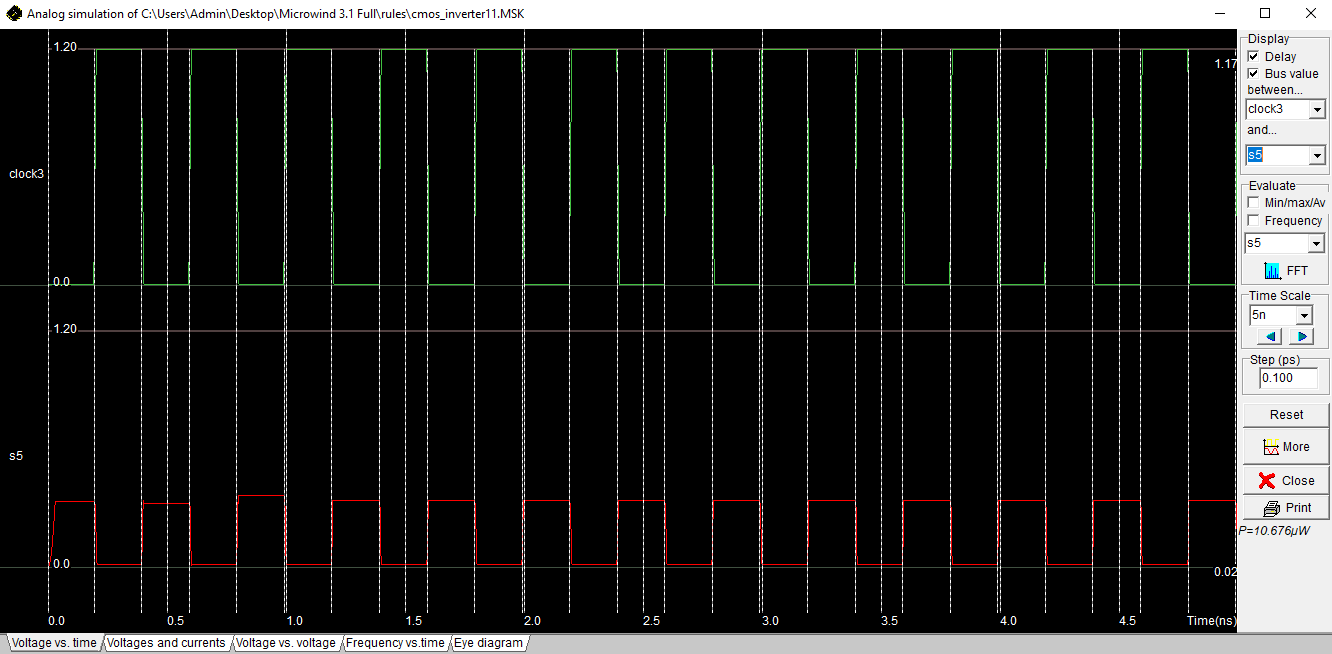
**Output -**

****

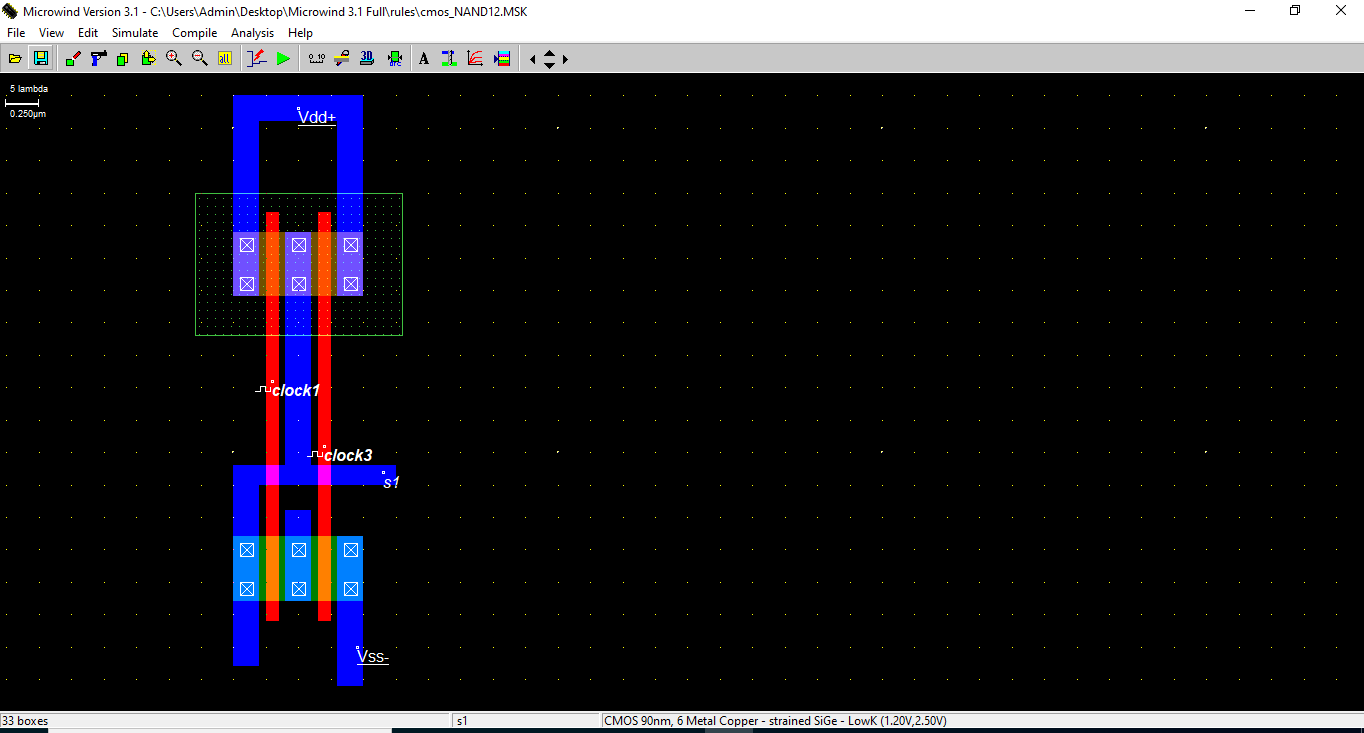
**Inverter without capacitor -**

****

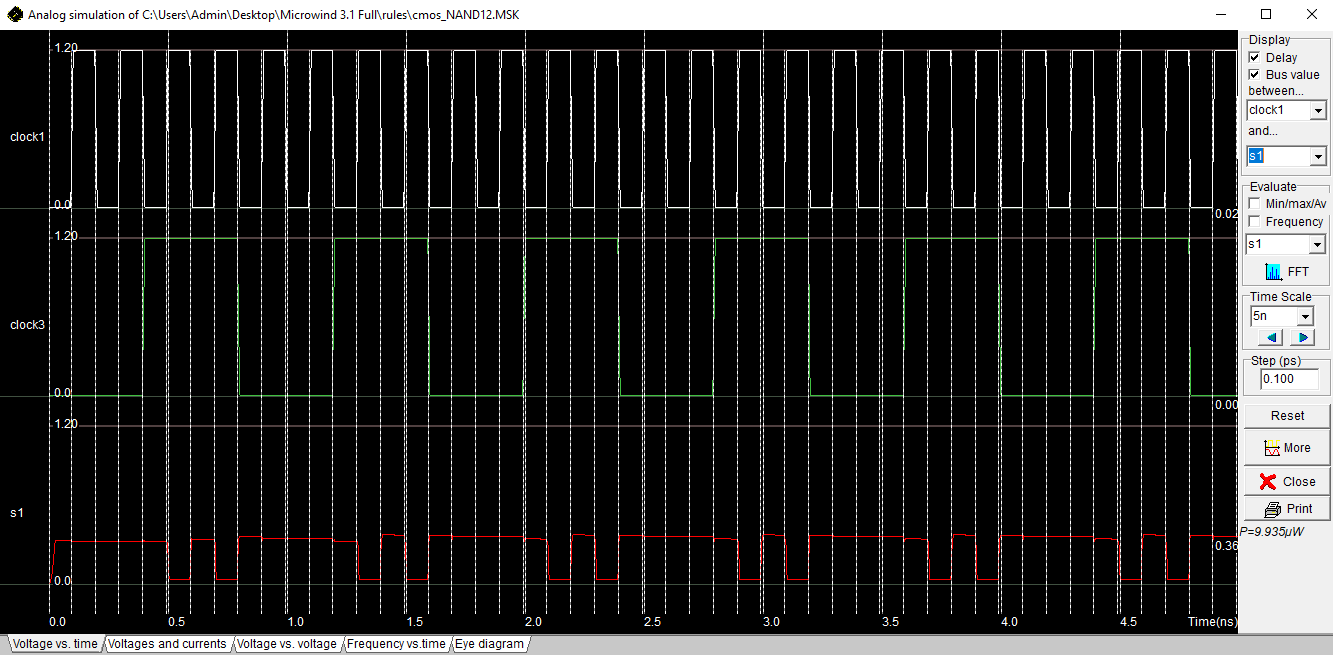
**Output -**

****

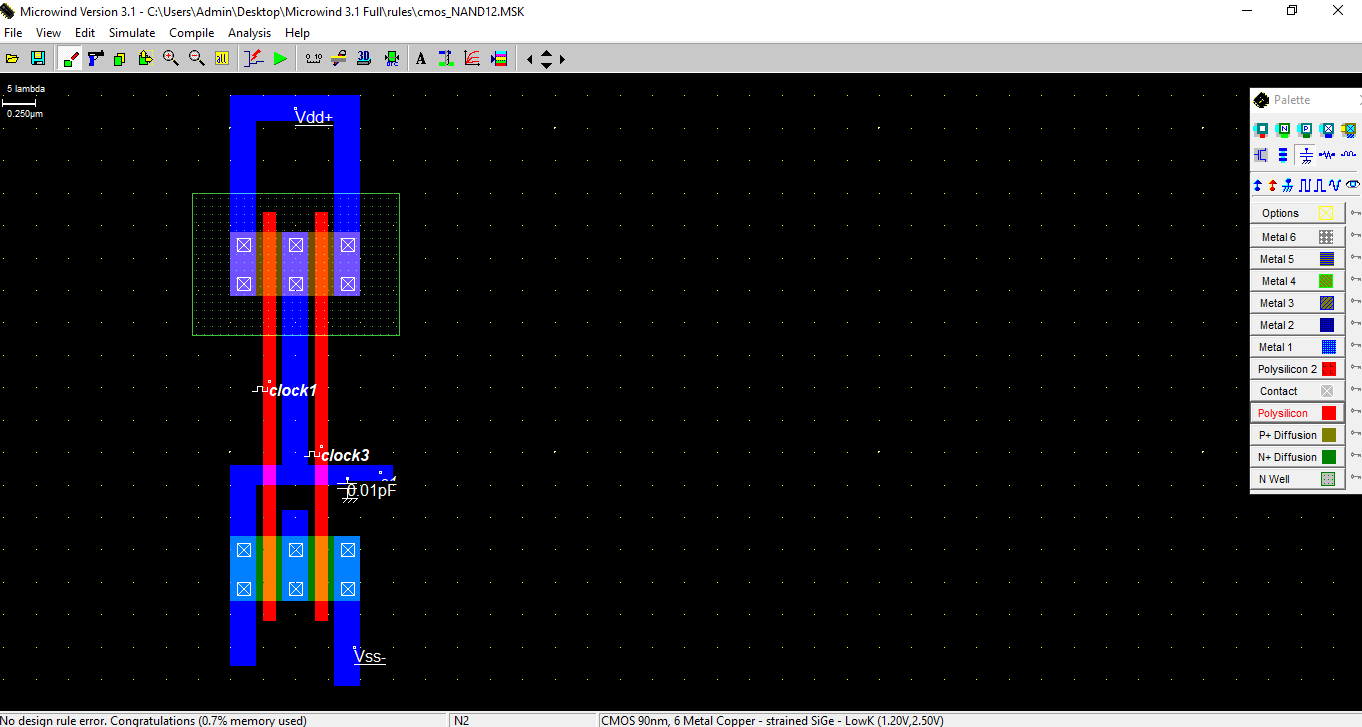
**NAND without capacitor -**

****

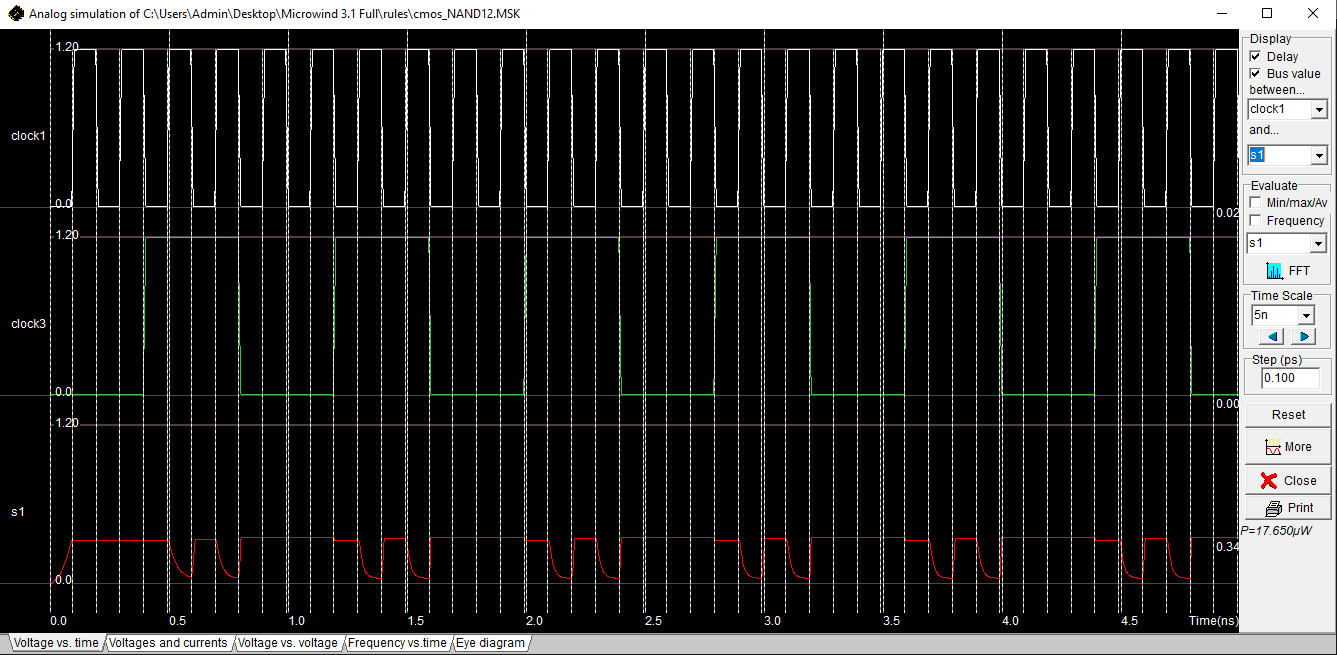
**Output -**

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**NAND with capacitor -**

****

**Output -**

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