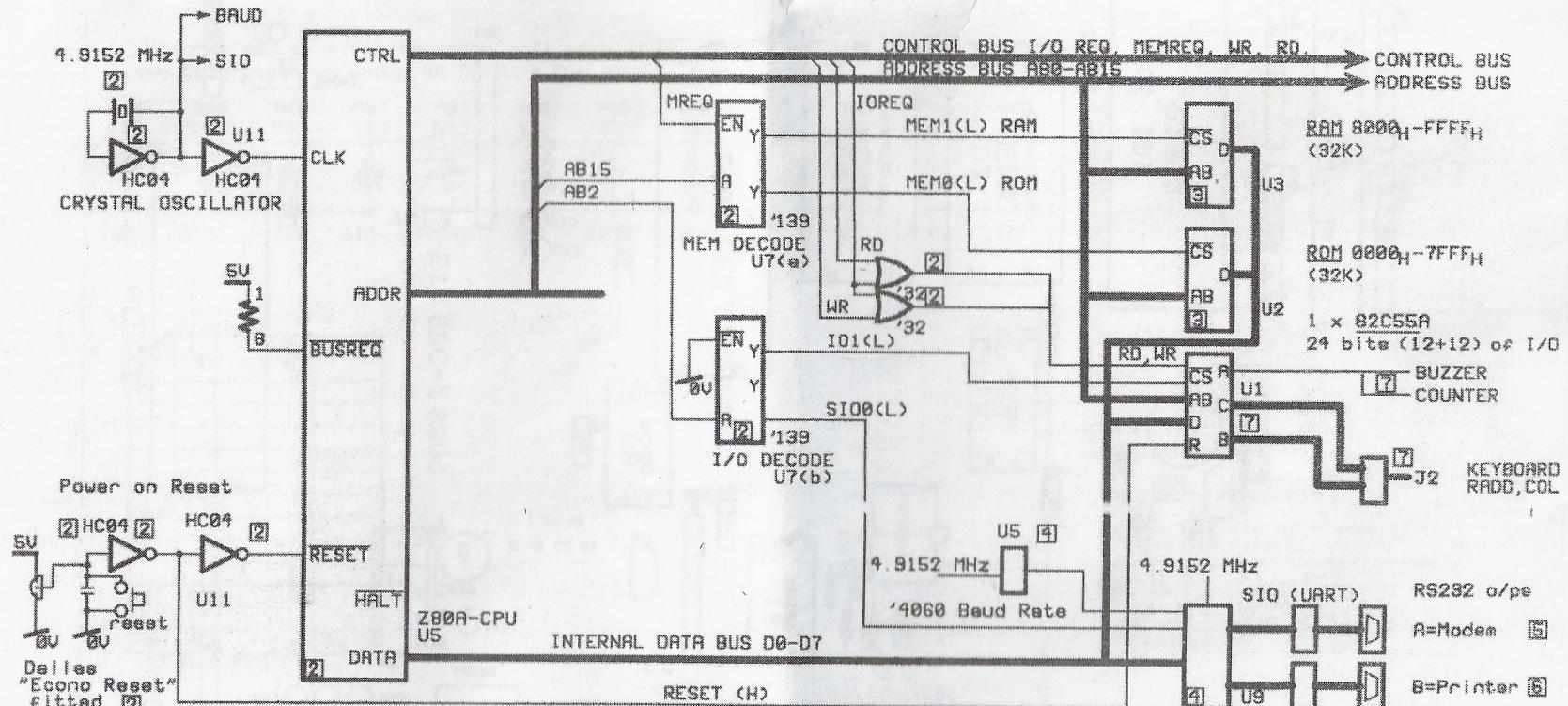


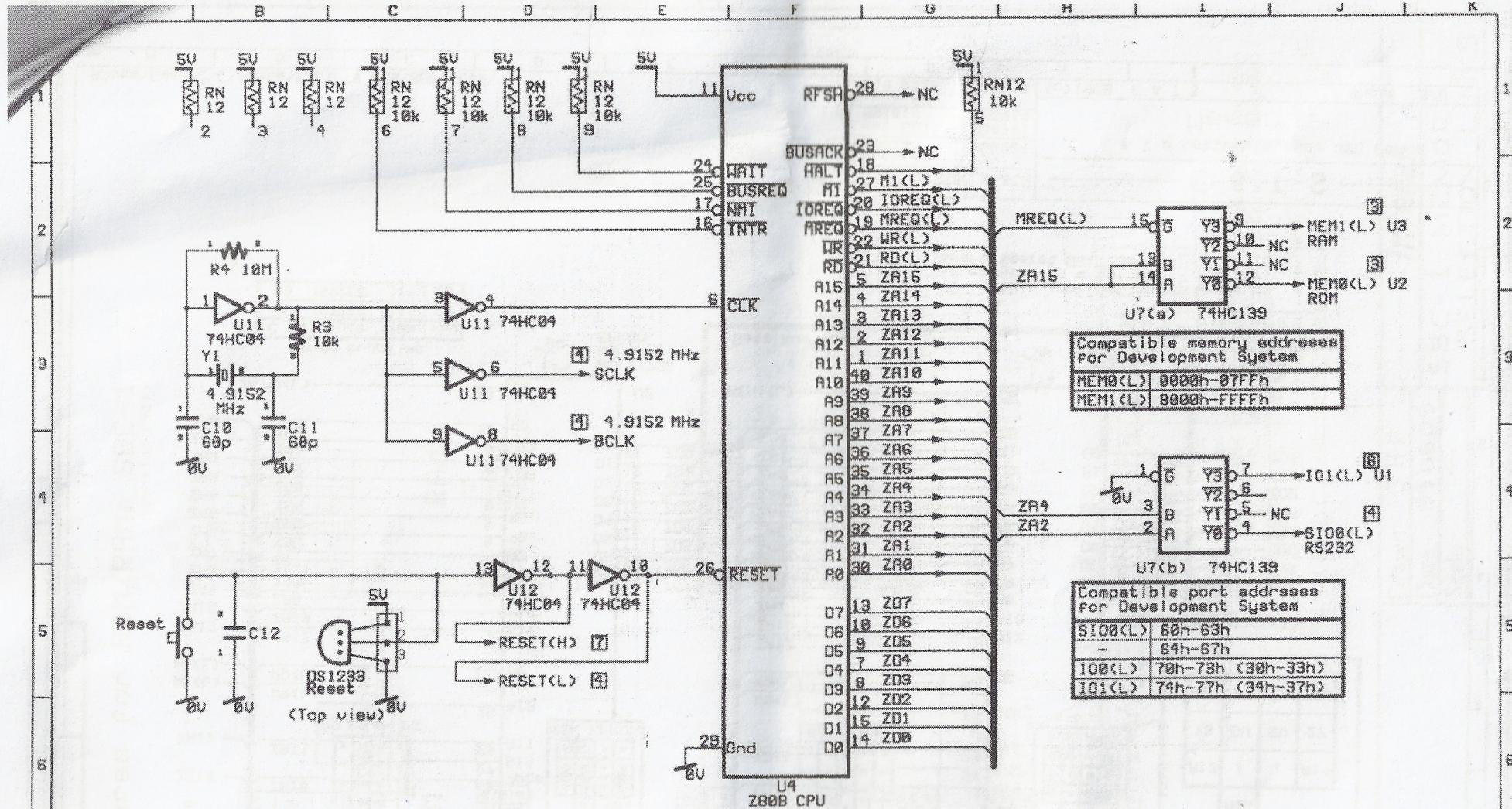
ASSEMBLY DIAGRAM (not for pcb) AND FILED AS ETSSBC3 LAST EDIT 17. 04. 99.
OUTER SILK SCREEN FOR SIDE B (WHITE)



N = Diagram Sheet Number 2 3 4 5 6 7 8 (Power Supplies on Sheet 9)

File: LB5SBC_1.SCH	E & T S
Started: 890525	
960731	
Last Edit: 951004	
981021	
Drawn by: SD45	0020

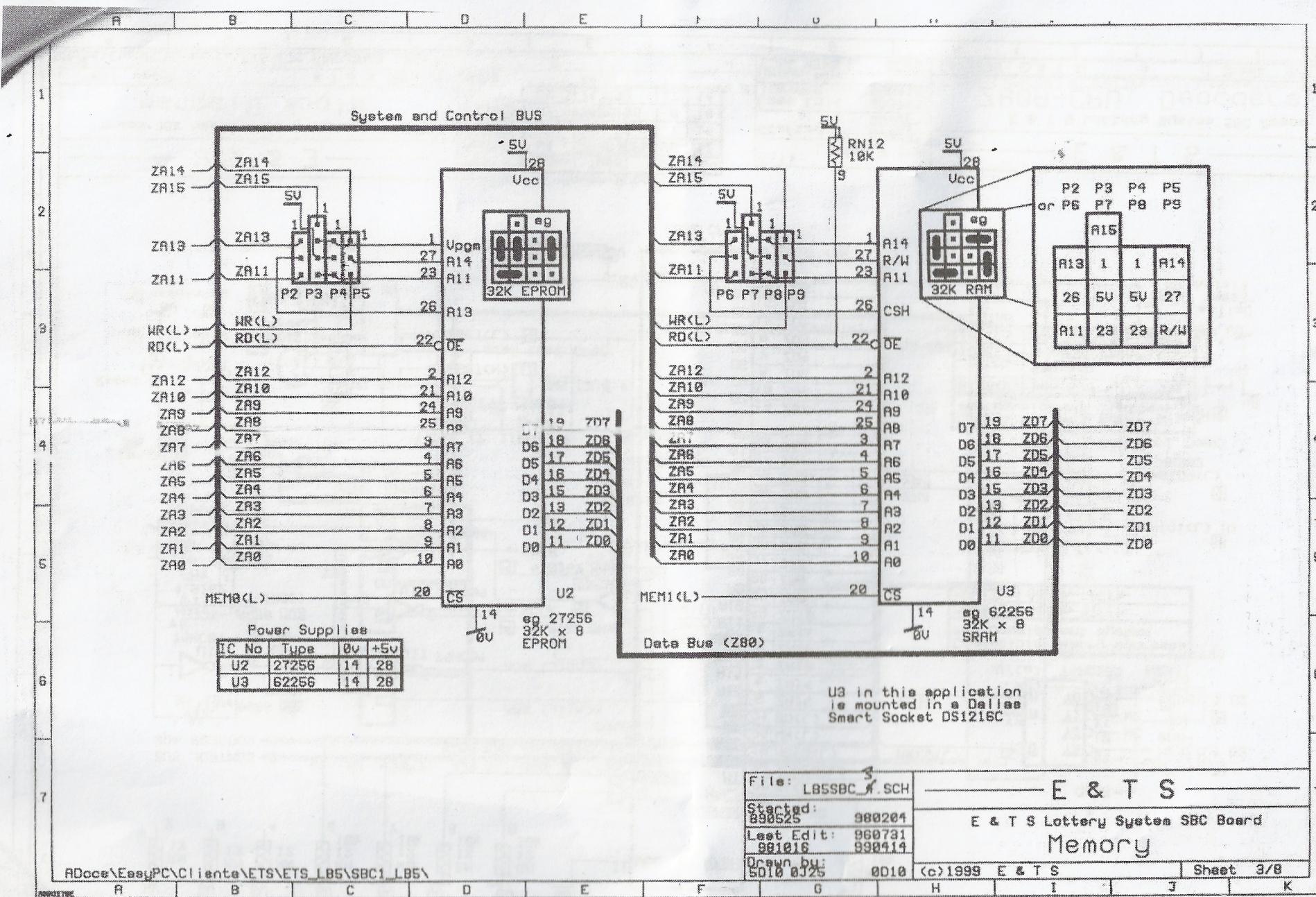
E & T S Lottery Controller SBC Board
Block Diagram



Power Supplies			
IC No	Type	0V	+5V
U4	Z80B-CPU	28	11
U11	74HC04	7	14
U7	74HC139	8	16
U8	74HC32	7	14

File: LB5SBC_2.SCH
Started: 890525
Last Edit: 980731
Drawn by: E & T S
981016 990414

E & T S
E & T S Lottery System SBC Board
Z80A-CPU, Decoders
(c) 1998 E & T S Sheet 2/8



Patch area for ROM/RAMs SBC-1
(Version 8811)

P2 P3 P4 P5

	R15		
R13	1	1	A14
26	SU	SU	27
A11	23	23	R/W

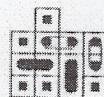
P6 P7 P8 P9

	R15		
R13	1	1	A14
26	SU	SU	27
A11	23	23	R/W

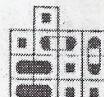
— Represents JLINK connection
— Represents position for spare JLINK

ROM

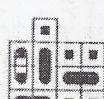
2K
(2716)



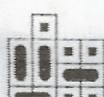
4K
(2732)



PIN 267
8K
(2764)



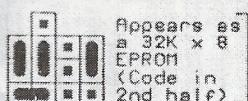
16K
(27128)



32K
(27256)



64K
(27512)



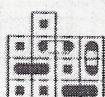
64K
(27512)



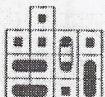
Last Revision 12/04/89

RAM

2K
(6116)



8K
(6264)



32K
(62256)



64K
(62256)



As ROM Socket

	Pins					
	Size	I	23	26	27	
2K	X	Upp	Vcc	X		
4K	X	A11	Vcc	X		
8K	SU	A11	NC	PGM(L)		
16K	SU	A11	A13	PGM(L)		
32K	SU	A11	A13	A14		
64K	SU	A11	A13	A14		
64K	A15	A11	A13	A14		

(Appears as 32K ROM)
(Appears as 64K ROM)

Pin 1 to +5 volts for
Dallas 1216

As RAM Socket

2K	X	R/W	Vcc	X
8K	X	A11	CSH	R/W
32K	A14	A11	A13	R/W

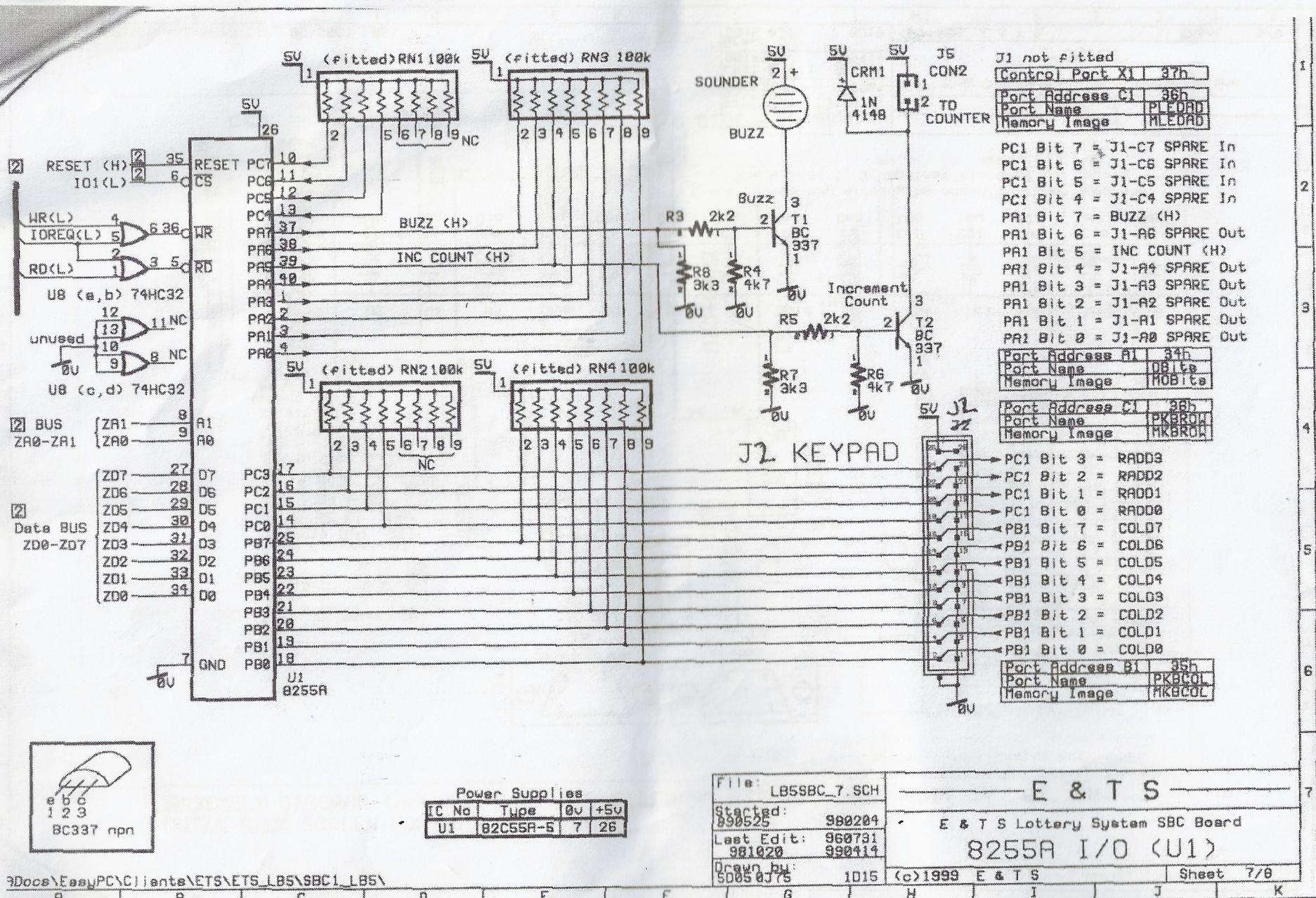
Drawn JHP

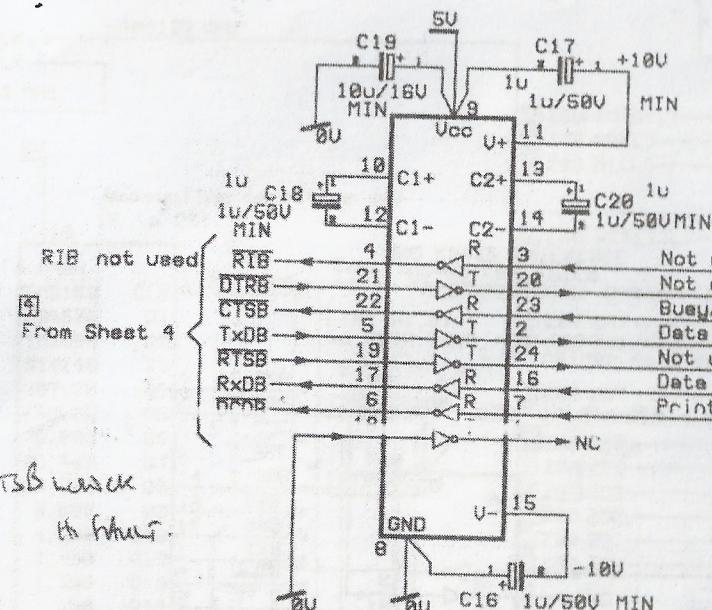
Date 890609

Scale —

Interak

SBC-1 MEMORY
SELECTION LINKS





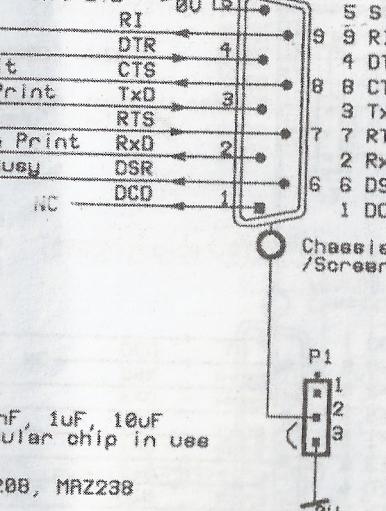
RIB +10V OK
-10V Fault.

PC AT Connections

This table appears as though this board were a PC

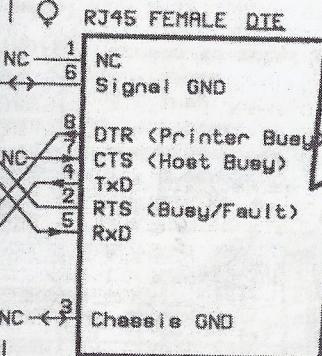
J3 DB9 MALE (=DTE)

All DTE



Printers are DTE (usually)
PCs are DTE
Modems are DCE

EXAMPLE CABLE TO
DH1500 THERMAL
PRINTER
D9 FEMALE
TO RJ45



RJ45 FEMALE

When making up serial cable for printer notice crossovers e.g. TxDb-RxDb, DTR-DSR, RTS-CTS. This is because both ends are DTE.

Connect screen to chassis to maintain earth screen continuity

This means socket metal must make contact with board

File:	LB5SBC_6.sch
Started:	981006
Last Edit:	981020 990414
Drawn by:	2D00 1J00 0015

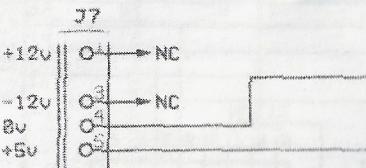
Greenbank Electronics

E & T S Lottery System JS

RS-232 Print Interface

(c)1999 Greenbank Electronics Sheet 6/8

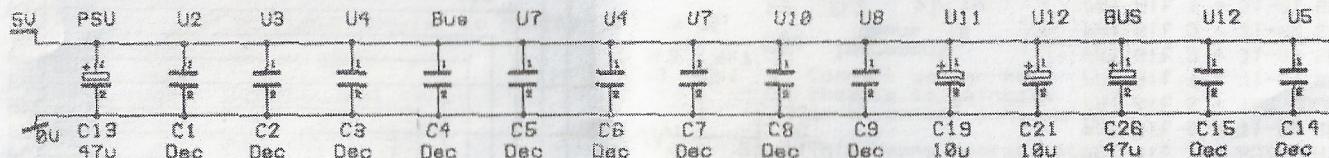
B C D E F G H I J K L M



J7 fitted
(wrong way on
many ETS boards)

Power Supplies

IC No	Type	8v	+5v
U8	74HC32	7	14
U7	74HC139	8	16
U12	74HC14	7	14



Check - some may already be shown on other diagrams
also check ends 1, 2, against finished pcb layout

File: LB55BC_8.SCH
Started: 960731
990525 990204
Last Edit: 990204
991021 990114
Drawn by:
6045 0726 0010

E & T S

E & T S Lottery System SBC Board
Power

(c) 1999 E & T S

Sheet 8/8