SEMINAR REPORT

ON

RASPBERRY PI

SUBMITTED BY

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II SEMESTER MCA

INFORMATION TECHNOLOGY EDUCATION CENTER

NILESHWAR

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INFORMATION TECHNOLOGY EDUCATION CENTER

NILESHWAR

CERTIFICATE

This is to certify that the seminar report entitled

RASPBERRY PI

is a bonafide record of the work done by Mr GEORGE JOHN , OF 2nd SEMESTER MCA under our supervision, in partial fulfilment of the requirements for the award of MCA from Kannur University for the year 2014- 2017.

SEMINAR CO-ORDINATOR HEAD OF THE DEPARTMENT

ABSTRACT

Raspberry Pi is a credit-card sized computer manufactured and designed in the United Kingdom by the Raspberry Pi foundation with the intention of teaching basic computer science to school students and every other person interested in computer hardware, programming and DIY-Do-it Yourself projects.

The Raspberry Pi is manufactured in three board configurations through licensed manufacturing deals with Newark element14 (Premier Farnell), RS Components and Egoman. These companies sell the Raspberry Pi online. Egoman produces a version for distribution solely in China and Taiwan, which can be distinguished from other Pis by their red coloring and lack of FCC/CE marks. The hardware is the same across all manufacturers.

The Raspberry Pi has a Broadcom BCM2835 system on a chip (SoC), which includes an ARM1176JZF-S 700 MHz processor, VideoCore IV GPU and was originally shipped with 256 megabytes of RAM, later upgraded (Model B & Model B+) to 512 MB. It does not include a built-in hard disk or solid-state drive, but it uses an SD card for booting and persistent storage, with the Model B+ using a MicroSD.

The Foundation provides Debian and Arch Linux ARM distributions for download. Tools are available for Python as the main programming language, with support for BBC BASIC (via the RISC OS image or the Brandy Basic clone for Linux), C, Java and Perl.

Raspberry Pi is an innovative product. The sheer number of users and fan base support the fact that the device can see a great future ahead. The device can surely help anyone who really wants to lean electronics and computers. Increasing the processing power can surely help the product in the future. The Raspberry Pi is an amazing piece of hardware because of the combination of the features of a traditional computer and an embedded device.

CONTENTS

1. INTRODUCTION

Raspberry Pi is a credit-card sized computer manufactured and designed in the United Kingdom by the Raspberry Pi foundation with the intention of teaching basic computer science to school students and every other person interested in computer hardware, programming and DIY-Do-it Yourself projects.

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As of February 2014, about 2.5 million boards had been sold.

2. INCEPTION OF RASPBERRY PI

2.1 THE IDEA TO CREATE RASPBERRY PI

The idea behind a tiny and affordable computer for kids came in 2006, when Eben Upton, Rob Mullins, Jack Lang and Alan Mycroft, based at the University of Cambridge's Computer Laboratory, became concerned about the year-on-year decline in the numbers and skills levels of the A Level students applying to read Computer Science. From a situation in the 1990s where most of the kids applying were coming to interview as experienced hobbyist programmers, the landscape in the 2000s was very different; a typical applicant might only have done a little web design.

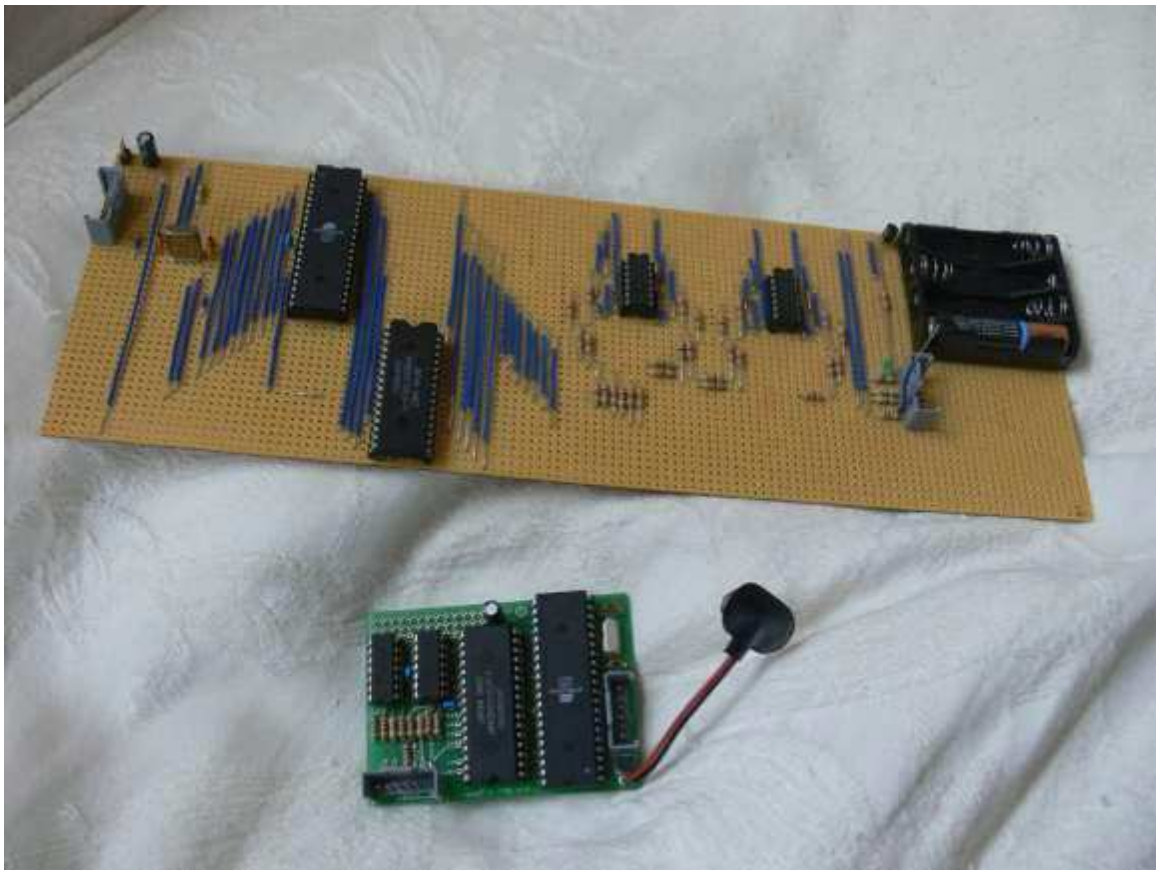
Something had changed the way kids were interacting with computers. A number of problems were identified: majority of curriculums with lessons on using Word and Excel, or writing webpages; the end of the dot-com boom; and the rise of the home PC and games console to replace the Amigas, BBC Micros, Spectrum ZX and Commodore 64 machines that people of an earlier generation learned to program on.

A Complete Commodore 64 System

There isn‘t much any small group of people can do to address problems like an inadequate school curriculum or the end of a financial bubble. But those students felt that they could try to do something about the situation where computers had become so expensive and arcane that programming experimentation on them had to be forbidden by parents; and to find a platform that, like those old home computers, could boot into a programming environment. Thus came the idea of creating the device which kids could buy and learn programming or hardware on – The Raspberry Pi.

2.2 INITIAL DESIGN CONSIDERATIONS

From 2006 to 2008 they created many designs and prototypes of what we now know as the Raspberry Pi. One of the earliest prototypes is shown below:



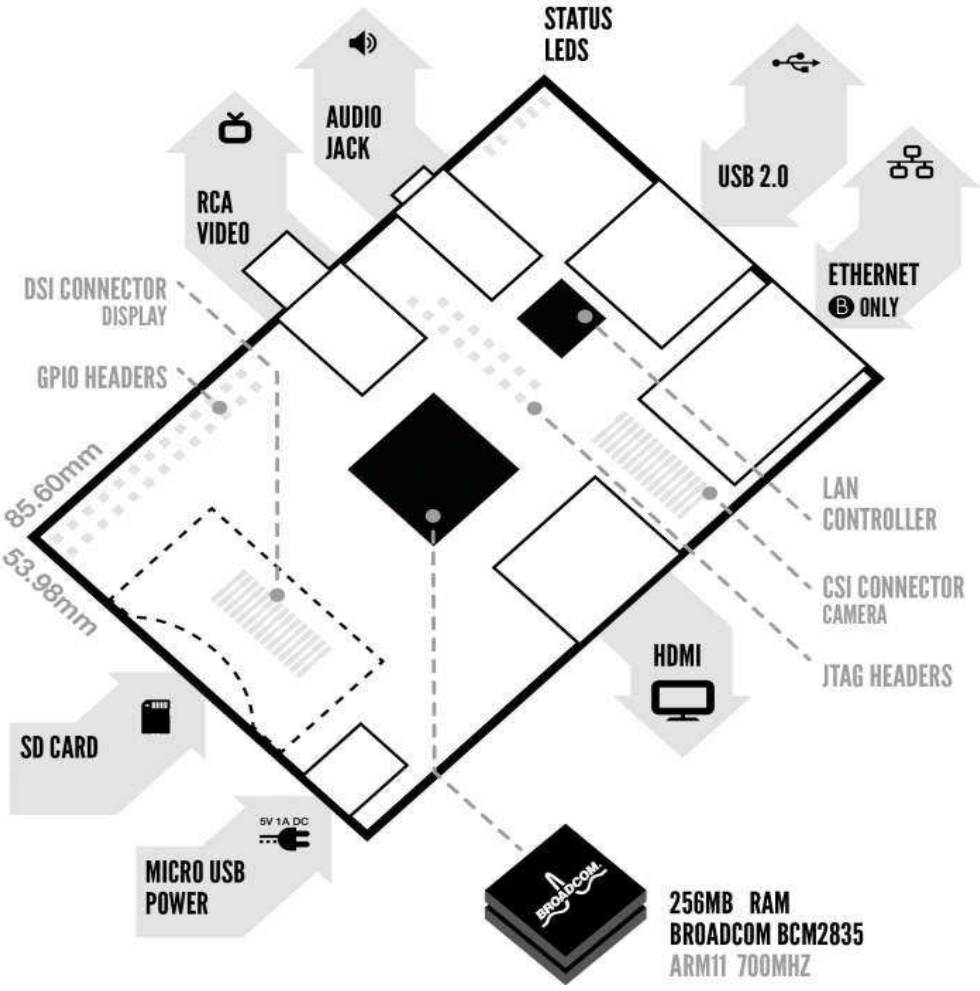
One of the earliest prototypes of the Pi

These boards use an Atmel ATmega644 microcontroller clocked at 22.1MHz, and a 512K SRAM for data and frame buffer storage.

By 2008, processors designed for mobile devices were becoming more affordable, an powerful enough to provide excellent multimedia, a feature which would make the board desirable to kids who wouldn‘t initially be interested in a purely programming-oriented device. The project started to look very realisable and feasible. Eben (now a chip architect at Broadcom), Rob, Jack and Alan, teamed up with Pete Lomas, MD of hardware design and manufacture company Norcott Technologies, and David Braben, co-author of the BBC Micro game Elite, to form the Raspberry Pi Foundation to make it a reality. Three years later, the Raspberry Pi Model B entered mass production through licensed manufacture deals with Element 14/Premier Farnell and RS Electronics, and within two years it had sold over two million units!

3. HARDWARE

3.1 HARDWARE LAYOUT



Block Diagram of Raspberry Pi

3.2 COMPONENTS ON THE PI

- **PROCESSOR/SoC (System on Chip)**

The Raspberry Pi has a Broadcom BCM2835 System on Chip module. It has a ARM1176JZF-S processor

The Broadcom SoC used in the Raspberry Pi is equivalent to a chip used in an old smartphone (Android or iPhone). While operating at 700 MHz by default, the Raspberry Pi provides a real world performance roughly equivalent to the 0.041 GFLOPS. On the CPU level the performance is similar to a 300 MHz Pentium II of 1997-1999, but the GPU, however, provides 1 Gpixel/s, 1.5 Gtexel/s or 24 GFLOPS of general purpose compute and the graphics capabilities of the Raspberry Pi are roughly equivalent to the level of performance of the Xbox of 2001. The Raspberry Pi chip operating at 700 MHz by default, will not become hot enough to need a heatsink or special cooling.

- **POWER SOURCE**

The Pi is a device which consumes 700mA or 3W or power. It is powered by a MicroUSB charger or the GPIO header. Any good smartphone charger will do the work of powering the Pi.

- **SD CARD**

The Raspberry Pi does not have any onboard storage available. The operating system is loaded on a SD card which is inserted on the SD card slot on the Raspberry Pi. The operating system can be loaded on the card using a card reader on any computer.

- **GPIO**

GPIO- General Purpose Input Output

General-purpose input/output (GPIO) is a generic pin on an integrated circuit whose behavior, including whether it is an input or output pin, can be controlled by the user at run time.

GPIO pins have no special purpose defined, and go unused by default. The idea is that sometimes the system designer building a full system that uses the chip might find it useful to have a handful of additional digital control lines, and having these available from the chip can save the hassle of having to arrange additional circuitry to provide them.

GPIO capabilities may include:

- GPIO pins can be configured to be input or output
- GPIO pins can be enabled/disabled
- Input values are readable (typically high=1, low=0)
- Output values are writable/readable
- Input values can often be used as IRQs (typically for wakeup events)

The production Raspberry Pi board has a 26-pin 2.54 mm (100 mil) expansion header, marked as P1, arranged in a 2x13 strip. They provide 8 GPIO pins plus access to I²C, SPI, UART), as well as +3.3 V, +5 V and GND supply lines. Pin one is the pin in the first column and on the bottom row.

GPIO Connector on RPi

- **DSI CONNECTOR**

The Display Serial Interface (DSI) is a specification by the Mobile Industry Processor Interface (MIPI) Alliance aimed at reducing the cost of display controllers in a mobile device. It is commonly targeted at LCD and similar display technologies. It defines a serial bus and a communication protocol between the host (source of the image data) and the device (destination of the image data).

A DSI compatible LCD screen can be connected through the DSI connector, although it may require additional drivers to drive the display.

- **RCA VIDEO**

RCA Video outputs (PAL and NTSC) are available on all models of Raspberry Pi. Any television or screen with a RCA jack can be connected with the RPi.

RCA Video Connector

- **AUDIO JACK**

A standard 3.5 mm TRS connector is available on the RPi for stereo audio output. Any headphone or 3.5mm audio cable can be connected directly. Although this jack cannot be used for taking audio input, USB mics or USB sound cards can be used.

- **STATUS LEDs**

There are 5 status LEDs on the RPi that show the status of various activities as follows:

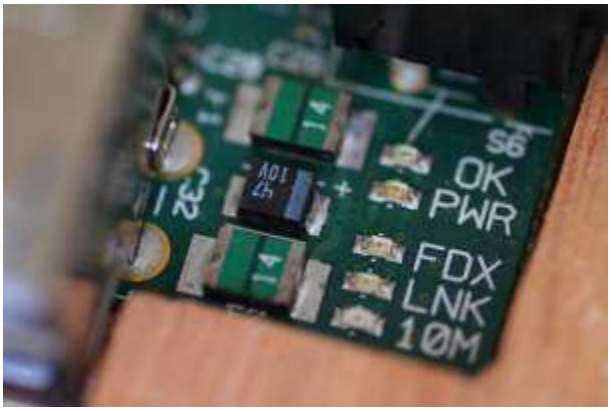
"OK"- SDCard Access (via GPIO16) - labelled as "OK" on Model B Rev1.0 boards and "ACT" on Model B Rev2.0 and Model A boards.

"POWER" - 3.3 V Power - labelled as "PWR" on all boards

"FDX" - Full Duplex (LAN) **(Model B)** - labelled as "FDX" on all boards

"LNK" - Link/Activity (LAN) **(Model B)** - labelled as "LNK" on all boards

"10M/100" - 10/100Mbit (LAN) **(Model B)** - labelled (incorrectly) as "10M" on Model B Rev1.0 boards and "100" on Model B Rev2.0 and Model A boards



Status LEDs

- **USB 2.0 PORT**

USB 2.0 ports are the means to connect accessories such as mouse or keyboard to the Raspberry Pi. There is 1 port on Model A, 2 on Model B and 4 on Model B+. The number of ports can be increased by using an external powered USB hub which is available as a standard Pi accessory.

- **ETHERNET**

Ethernet port is available on Model B and B+. It can be connected to a network or internet using a standard LAN cable on the Ethernet port. The Ethernet ports are controlled by Microchip LAN9512 LAN controller chip.

- **CSI CONNECTOR**

CSI – Camera Serial Interface is a serial interface designed by MIPI (Mobile Industry Processor Interface) alliance aimed at interfacing digital cameras with a mobile processor.

The RPi foundation provides a camera specially made for the Pi which can be connected with the Pi using the CSI connector.

- **JTAG HEADERS**

JTAG is an acronym for _Joint Test Action Group', an organization that started back in the mid 1980's to address test point access issues on PCB with surface mount devices. The organization devised a method of access to device pins via a serial port that became known as the TAP (Test Access Port). In 1990 the method became a recognized international standard (IEEE Std 1149.1). Many thousands of devices now include this standardized port as a feature to allow test and design engineers to access pins.

- **HDMI**

  HDMI – High Definition Multimedia Interface
  HDMI 1.3 a type A port is provided on the RPi to connect with HDMI screens.

3.3 SPECIFICATIONS

	Model A	**Model B**	**Model B+**
Target Price	US $25	US $35	
Soc	Broadcom BCM2835 (CPU, GPU, DSP, SDRAM, and single USB port)		
CPU	700 MHz ARM1176JZF-S core (ARM11 family, ARMv6 instruction set)		
GPU	Broadcom VideoCore IV @ 250 MHz		
Memory (SDRAM)	256 MB (shared with GPU)	512 MB (shared with GPU) as of 15 October 2012	
USB 2.0 Ports	1 (direct from BCM2835 chip)	2 (via the on-board 3- port USB hub)	4 (via the on-board 5-port USB hub)
Video Input	15-pin MIPIcamera interface (CSI) connector, used with the Raspberry Pi Camera Addon.		
Video Outputs	Composite RCA (PAL and NTSC) –in model B+ via 4-pole 3.5 mm jack, HDMI (rev 1.3 & 1.4), raw LCD Panels via DS		
Audio Outputs	3.5 mm jack, HDMI, and, as of revision 2 boards, I²S audio (also potentially for audio input)		
Onboard Storage	SD / MMC / SDIO card slot (3.3 V card power support only)		MicroSD
Onboard Network	None	10/100 Mbit/sEthernet (8P8C) USB adapter on the third/fifth port of the USB hub	
Low-Level Peripherals	8× GPIO, UART, I²C bus, SPI bus with two chip selects, I²S audio +3.3 V, +5 V, ground		17*GPIO

Power Ratings	300 mA (1.5 W)	700 mA (3.5 W)	600 mA (3.0 W)
Power Source	5 V via MicroUSB or GPIO header		
Size	85.60 mm × 56 mm (3.370 in × 2.205 in) – not including protruding connectors		
Weight	45 g (1.6 oz)		

3.4 BRIEF DESCRIPTION ON SYSTEM ON CHIP (SoC)

Since smartphones and tablets are basically smaller computers, they require pretty much the same components we see in desktops and laptops in order to offer us all the amazing things they can do (apps, music and video playing, 3D gaming support, advanced wireless features, etc).

But smartphones and tablets do not offer the same amount of internal space as desktops and laptops for the various components needed such as the logic board, the processor, the RAM, the graphics card, and others. That means these internal parts need to be as small as possible, so that device manufacturers can use the remaining space to fit the device with a long-lasting battery life.

Thanks to the wonders of miniaturization, SoC manufacturers, like Qualcomm, Nvidia or Texas Instruments, can place some of those components on a single chip, the System on a Chip that powers smartphones.

A system on a chip or system on chip (SoC or SOC) is an integrated circuit (IC) that integrates all components of a computer or other electronic system into a single chip. It may contain digital, analog, mixed-signal, and often radio-frequency functions—all on a single chip substrate. SoCs are very common in the mobile electronics market because of their low power consumption. A typical application is in the area of embedded systems. The contrast with a microcontroller is one of degree. Microcontrollers typically have under 100 kB of RAM (often just a few kilobytes) and often really are single-chip-systems, whereas the term SoC is typically used for more powerful processors, capable of running software such as the desktop versions of Windows and Linux, which need external memory chips (flash, RAM) to be useful, and which are used with various external peripherals. In short, for larger systems, the term system on a chip is a hyperbole, indicating technical direction more than reality: increasing chip integration to reduce manufacturing costs and to enable smaller systems. Many interesting systems are too complex to fit on just one chip built with a process optimized for just one of the system's tasks.

A typical SoC consists of:

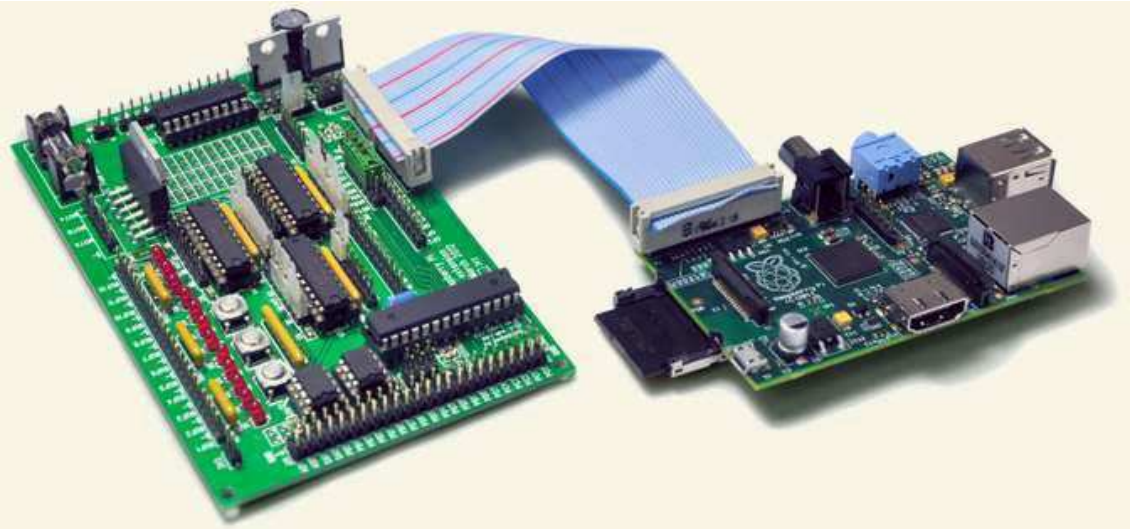
- A microcontroller, microprocessor or DSP core(s). Some SoCs—called *multiprocessor system on chip* (MPSoC)—include more than one processor core.
- Memory blocks including a selection of ROM, RAM, EEPROM and flash memory
- Timing sources including oscillators and phase-locked loops
- Peripherals including counter-timers, real-time timers and power-on reset generators
- External interfaces, including industry standards such as USB, FireWire, Ethernet, USART, SPI
- Analog interfaces including ADCs and DACs
- Voltage regulators and power management circuits
- A bus - either proprietary or industry-standard such as the AMBA bus from ARM Holdings - connects these blocks. DMA controllers route data directly between external interfaces and memory, bypassing the processor core and thereby increasing the data throughput of the SoC.

3.5 ACCESSORIES

Raspberry Pi being a very cheap computer has attracted millions of users around the world. Thus it has a large user base. Many enthusiasts have created accessories and peripherals for the Raspberry Pi. This range from USB hubs, motor controllers to temperature sensors. There are some official accessories for the RPi as follows:

***Camera*:** On 14 May 2013, the foundation and the distributors RS Components & Premier Farnell/Element 14 launched the Raspberry Pi camera board with a firmware update to support it. The Raspberry Pi camera board contains a 5 MPixel sensor, and connects via a ribbon cable to the CSI connector on the Raspberry Pi. In Raspbian support can be enabled by the installing or upgrading to the latest version of the OS and then running Raspi-config and selecting the camera option. The cost of the camera module is 20 EUR in Europe (9 September 2013). and supports 1080p, 720p, 640x480p video. The footprint dimensions are 25 mm x 20 mm x 9 mm.

***Gertboard*:** A Raspberry Pi Foundation sanctioned device designed for educational purposes, and expands the Raspberry Pi's GPIO pins to allow interface with and control of LEDs, switches, analog signals, sensors and other devices. It also includes an optional Arduino compatible controller to interface with the Pi. The Gertboard can be used to control motors, switches etc. for robotic projects.



Gertboard (Left) and Raspberry Pi (Right)

***USB Hub*:** Although not an official accessory, it is a highly recommended accessory for the Pi. A powered USB Hub with 7 extra ports is available at almost all online stores. It is compulsory to use a USB Hub to connect external hard disks or other accessories that draw power from the USB ports, as the Pi cannot give power to them.

4. SOFTWARE

4.1 OPERATING SYSTEM

The Raspberry Pi primarily uses Linux kernel-based operating systems. The ARM11 is based on version 6 of the ARM which is no longer supported by several popular versions of Linux, including Ubuntu. The install manager for Raspberry Pi is NOOBS.

The OSs included with NOOBS are:

- Archlinux ARM
- OpenELEC
- Pidora (Fedora Remix)
- Raspbmc and the XBMC open source digital media center
- RISC OS – The operating system of the first ARM-based computer
- Raspbian (recommended) – Maintained independently of the Foundation; based on ARM hard-float (armhf)-Debian 7 'Wheezy' architecture port, that was designed for a newer ARMv7 processor whose binaries would not work on the Rapberry Pi, but Raspbian is compiled for the ARMv6 instruction set of the Raspberry Pi making it work but with slower performance. It provides some available deb software packages, pre-compiled software bundles. A minimum size of 2 GB SD card is required, but a 4 GB SD card or above is recommended. There is a Pi Store for exchanging programs. The 'Raspbian Server Edition (RSEv2.4)', is a stripped version with other software packages bundled as compared to the usual desktop computer oriented Raspbian.

4.2 BOOT PROCESS

The Raspberry Pi does not boot as a traditional computer. The VideoCore i.e. the Graphics processor actually boots before the ARM CPU.

The boot process of the Raspberry Pi can be explained as follows:

- When the power is turned on, the first bits of code to run is stored in a ROM chip in the SoC and is built into the Pi during manufacture. This is the called the **first-stage bootloader**.

- The SoC is hardwired to run this code on startup on a small RISC Core (**R**educed **I**nstruction **S**et **C**omputer). It is used to mount the FAT32 boot partition in theSDCard so that the **second-stage bootloader** can be accessed. So what is this ‘second-stage bootloader‘ stored in the SD Card? It‘s

'bootcode.bin'. This file can be seen while mount process of an operating system on the SD Card in windows.

- Now here's something tricky. The first-stage bootloader has not yet initialized the ARM CPU (meaning CPU is in **reset**) or the RAM. So, the second-stage bootloader also has to run on the GPU. The bootloader.bin file is loaded into the 128K 4 way set associative L2 cache of the GPU and then executed. This enables the RAM and loads **start.elf** which is also in the SD Card. This is the **third-stage bootloader** and is also the most important. It is the firmware for the GPU, meaning it contains the settings or in our case, has instructions to load the settings from **config.txt** which is also in the SD Card. We can think of the config.txt as the 'BIOS settings'.

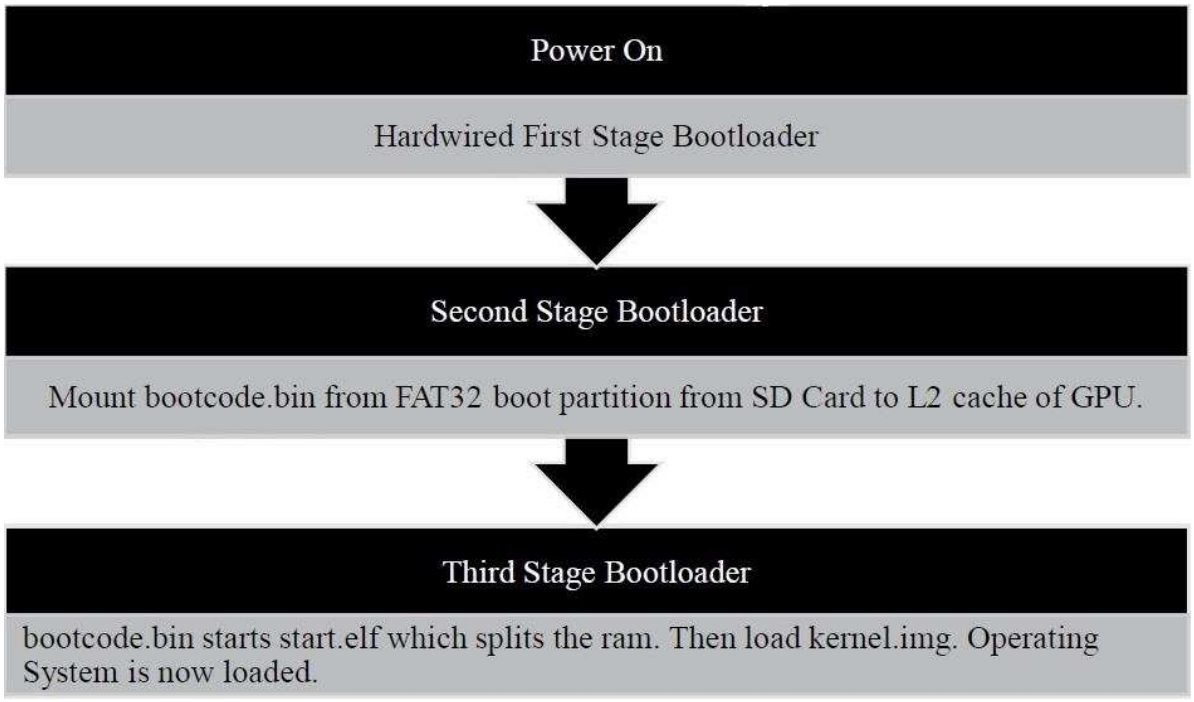
- The **start.elf**also splits the RAM between the GPU and the ARM CPU. The ARM only has access the to the address space left over by the GPU address space. For example, if the GPU was allocated addresses from 0x000F000 – 0x0000FFFF, the ARM has access to addresses from 0x00000000 - 0x0000EFFF.

- The physical addresses perceived by the ARM core is actually mapped to another address in the VideoCore (0xC0000000 and beyond) by the MMU (Memory Management Unit) of the VideoCore**.**

- The config.txt is loaded afterthe split is done so the splitting amounts cannot be specified in the config.txt. However, different .elf files having different splits exist in the SD Card. So, depending on the requirement, the file can be renamed to start.elf and boot the Pi. In the Pi, the GPU is King!

- Other than loading config.txt and splitting RAM, the **start.elf**also loads **cmdline.txt** if it exists. It contains the command line parameters for whatever kernel that is to be loaded. This brings us to the final stage of the boot process. The **start.elf** finally loads **kernel.img** which is the binary file containing the OS kernel and releases the **reset** on the CPU. The ARM CPU then executes whatever instructions in the **kernel.img** thereby loading the operating system.

- After starting the operating system, the GPU code is not unloaded. In fact, start.elf is not just firmware for the GPU, It is a proprietary operating system called VideoCore OS (VCOS). When the normal OS (Linux) requires an element not directly accessible to it, Linux communicates with VCOS using the mailbox messaging system.



Boot Process of Raspberry Pi

4.3 THE NOOBS INSTALLER

The Raspberry Pi package only comes with the main board and nothing else. It does not come shipped with an operating system. Operating systems are loaded on a SD card from a computer and then the SD card is inserted in the Pi which becomes the primary boot device. Installing operating system can be easy for some enthusiasts, but for some beginners working with image files of operating systems can be difficult. So the Raspberry Pi foundation made a software called NOOBS – New Out Of Box Software which eases the process of installing an operating system on the Pi. The NOOBS installer can be downloaded from the official website. A user only needs to connect a SD card with the computer and just run the setup file to install NOOBS on the SD card. Next, insert the card on the Raspberry Pi. On booting the first time, the NOOBS interface is loaded and the user can select from a list of operating systems to install. It is much convenient to install the operating system this way. Also once the operating system is installed on the card with the NOOBS installer, every time the Pi boots, a recovery mode provided by the NOOBS can be accessed by holding the shift key during boot. It also allows editing of the config.txt file for the operating system

4.4 RASPBERRY PI COMPATIBLE OPERATING SYSTEMS

DISTRIBUTION	TYPE	MEMORY FOOTPRINT	PACKAGES
Arch Linux ARM	Linux		8,700
BerryTerminal	Linux		
Bodhi Linux	Raspbian		35,000+ ARMHF
Debian ARM	Linux		20,000+
Fedora Remix	Linux		16,464?
Gentoo Linux	Linux	~23 MiB	
IPFire	Linux	~20 MiB	144
I2PBerry	Linux		20,000+
Meego MER + XBMC	Linux (embedded)	~34 MiB + XBMC	~320 (core)
Moebius	Raspbian	~20 MiB	(core) + Raspbian Repositories
nOS	Linux	~90 MiB	35,000+
openSUSE	Linux 3.11	28 MiB (inc. X11)	6300
OpenWRT	Linux	3,3MiB	3358
PiBang Linux	Linux_3.6.11 &SystemD		
PwnPi	Linux		20,000+
QtonPi	Linux		
VPNbian	Linux	~40 MiB w/o desktop	35,000+
Raspbian	Linux	~30 MiB w/o desktop	35,000+
OpenELEC	Linux 3.10.16 (embedded)	95 MiB (incl. XBMC)	~140 (+ 7 via xbmc)
XBian	Raspbian		35,000+
raspbmc	Raspbian		20,000+
RISC OS	RISC OS		
Aros hosted on Raspbian Limited Demo	Mixed Debian6 and Aros	<~50 MiB	
Plan9	Plan 9		
SlaXBMCRPi	Linux 3.10.36+		476 (+ Official SlackwareARM 14.1 Packages)
PiMAME	Linux		
PiBox	Linux/Buildroot		
pipaOS	Raspbian	~32 MiB	37.500
Raspberry WebKiosk	Raspbian		
Volumio	Raspbian		
Nard SDK	Embedded Linux	~40 MB	

List of supported Operating Systems

5. APPLICATIONS

The major aim behind the Raspberry Pi was to educate people, especially children and teenagers, towards programming and basic hardware interfacing. The open body structure of the Raspberry Pi makes it a machine on which one can learn computer concepts.

Applications of the Raspberry Pi can be given as follows:

- Teaching programming concepts.
- Teaching hardware interfacing.
- Raspberry Pi being very cost effective can be deployed in large numbers in underdeveloped and developing countries like Africa, India, China, Brazil etc. to schools and colleges and to everyone who is interested in computers and electronics.
- It can be used in robotics for controlling motors, sensors, etc.
- It can be used as a downloading machine replacing desktop computers. It consumes very low power and also can be accessed remotely.
- It can be used as a media center at home. Any television can be converted to a smart TV with internet capabilities with the Pi.
- It can be used for designing prototypes of DIY projects and certain embedded devices. It becomes very cheap option for testing and evaluation purpose.
- Can be used in creating and handling small servers.
- It can be used for making digital photo frames, tablets etc at home

5.1 EXAMPLES FOR PROJECTS USING RASPBERRY PI

Home Automation

With a Raspberry Pi, switches, web server, enthusiasts have created home automation systems that can control fans and lights of a home from the Pi or even a smartphone.

An example of a home automation system using RPi.

Robots

Simple robots can be developed using Raspberry Pi boards and various other components that can be connected to it.

Raspberry Pi inside a Robot

6. MERITS & DEMERITS

6.1 ADVANTAGES

It is important for customers and business owners that want to get the Raspberry Pi to consider whether it fits with their business strategy and are willing to go through the process of putting it together and tailoring the product to their own needs. The benefits that this products offers beside the low price point are:

- This microcomputer is useful for small or home based businesses that run on a smaller budget than bigger companies for you are not required to purchase any special licenses from the Raspberry Pi Foundation to use their product or if you invent new technology that embeds the product. Small business owners can use it to automate any small task, such as using the Pi to run a website (as long as it does not have a lot of traffic), or use it as a small database and media server

- The product does not require the user to have extensive programming experience since it is aimed for the younger generation to learn about programming. Python, the programming language that the Pi uses, is less complex than other languages available.

- The product also gives you a lot of room to experiment and turn it into something else that is entirely different. The SD cards on the board can be easily switched, which allows you to change the functions of the device without spending a lot of time reinstalling the software.

- The Raspberry Pi is perfect for adaptive technology: it is able to display images or play videos at 1080p high definition resolution to building systems such as digital jukeboxes or prototyping embedded systems. This product makes it possible to build complex and effective products at a cheaper price.

- The product is energy efficient and provides a greener ethical alternative to small businesses. This small credit card sized product makes it easy to recycle and does not release as much carbon dioxide emissions into the environment, unlike big servers that require lots of energy and extensive cooling systems.

6.2 DISADVANTAGES

- It does not replace your computer, since the Ethernet is only a 10/100 and the processor is not as fast, it is time consuming to download and install software and is unable to do any complex multitasking.

- Not compatible with other operating systems such as Windows (There are currently 1.3 billion Windows users around the world.)

- To use the Raspberry Pi,it will take more than just 35 dollars to get it to do what you need through buying extra accessories such as the SD card, USB power supply, keyboard..etc and if you take into account the acquisition cost of the product.

- This product will not be useful for bigger businesses that already have big servers, which would already do everything that the Raspberry Pi does, so it would not be worth it to take the time to get someone to put it together.

7. CONCLUSION

Raspberry Pi is an innovative product. The sheer number of users and fan base support the fact that the device can see a great future ahead. The device can surely help anyone who really wants to lean electronics and computers.

Increasing the processing power can surely help the product in the future. Also supplying a case and a proper instruction manual will improve the product. Also currently Windows operating systems are not compatible because of the ARM processor. If the processor is improved or any workaround is found to run Windows directly on the Raspberry Pi, then it can be a great step for the Pi.

The Raspberry Pi is an amazing piece of hardware because of the combination of the features of a traditional computer and an embedded device. Supporting computer operating systems like Linux and providing simple input/output lines i.e. the GPIO makes it perfect for controlling almost anything. Programming the GPIO is much easy and intuitive then an traditional FPGA or microprocessor.

Finally it can be said that Raspberry Pi can be effectively used if its processing power is kept in mind. It can work as a personal computer but cannot replace it.

8. REFERENCES

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