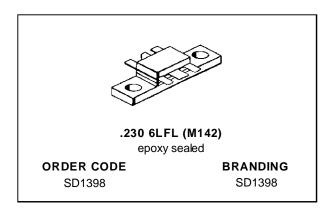


SD1398

RF & MICROWAVE TRANSISTORS 850-960 MHz APPLICATIONS

- 850 960 MHZ
- 24 VOLTS
- COMMON EMITTER
- OVERLAY GEOMETRY
- GOLD METALLIZATION
- Pout = 6.0 W MIN. WITH 10.0 dB GAIN



PIN CONNECTION

3. Emitter

1. Collector

2. Base

DESCRIPTION The SD1398 is a gold metallized epitaxial silicon

NPN transistor designed for high linearity Class AB operation cellular base station applications. The SD1398 can also be operated Class C.

The SD1398 is internally input matched and can be used as a driver for the SD1423 or SD1424.

ABSOLUTE MAXIMUM RATINGS $(T_{case} = 25^{\circ}C)$

Symbol	Parameter Value		Unit
V _{CBO}	Collector-Base Voltage	50	V
V _{CES}	Collector-Emitter Voltage	35	V
V _{EBO}	Emitter-Base Voltage	3.5	V
lc	Device Current	2.4	А
Poiss	Power Dissipation	53	W
TJ	Junction Temperature	+200	°C
T _{STG}	Storage Temperature	- 65 to +150	°C

THERMAL DATA

R _{TH(j-c)} Junction-Case Thermal Resistance	3.3	°C/W
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1/6 September 8, 1993

ELECTRICAL SPECIFICATIONS (Tcase = 25°C)

STATIC

Symbol	Test Conditions	Value			Unit		
	rest Conditions		Min.	Тур.	Max.		
ВУсво	I _C = 5mA	$I_E = 0mA$		50			V
BVCEO	I _C = 5mA	$I_B = 0mA$		24			V
BV _{EBO}	I _E = 5mA	$I_C = 0mA$		3.5		_	V
I _{CEO}	V _{CE} = 24V	$I_E = 0mA$				1.0	mA
Ісво	V _{CB} = 24V	I _E = 0mA		_	_	1.0	mA
hfE	V _{CE} = 10V	I _C = 0.1A		20	_	100	_

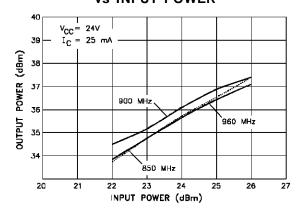
DYNAMIC

Symbol	Test Conditions		Value			Unit	
Symbol	rest Conditions			Min.	Тур.	Max.	
Pout	f = 850 — 960 MHz	$V_{CE} = 24 V$	$I_{CQ} = 25 \text{ mA}$	6	_		W
ης	f = 850 — 960 MHz	$V_{CE} = 24 V$	$I_{CQ} = 25 \text{ mA}$	_	50		%
G _P	f = 850 — 960 MHz	V _{CE} = 24 V	$I_{CQ} = 25 \text{ mA}$	10	12	_	dB
Сов	f = 1 MHz	$V_{CB} = 24 V$		_	7.5	8.5	рF

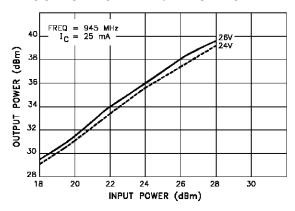
Note: $P_{IN} = 0.60w$

TYPICAL PERFORMANCE

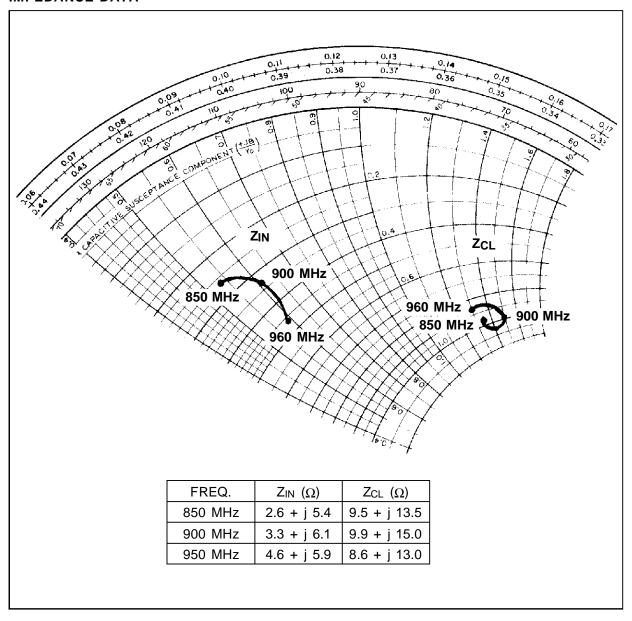
CLASS AB BROADBAND OUTPUT POWER vs INPUT POWER



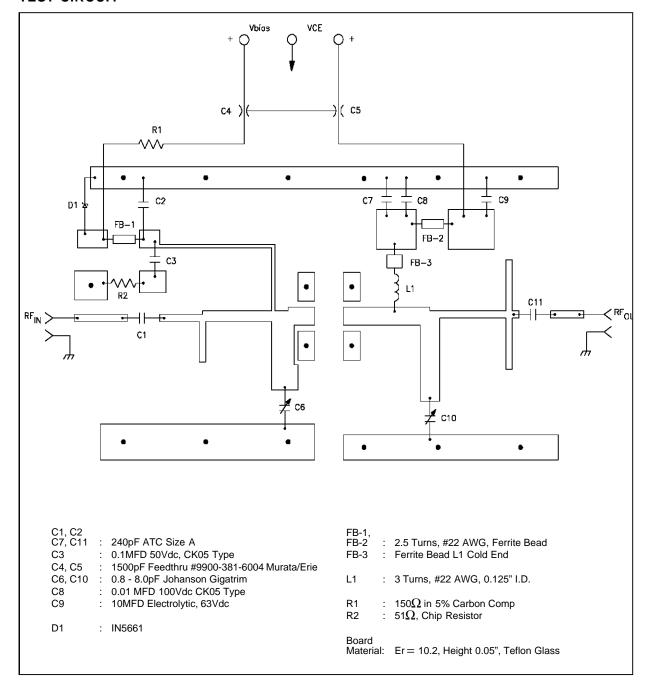
OUTPUT POWER vs INPUT POWER



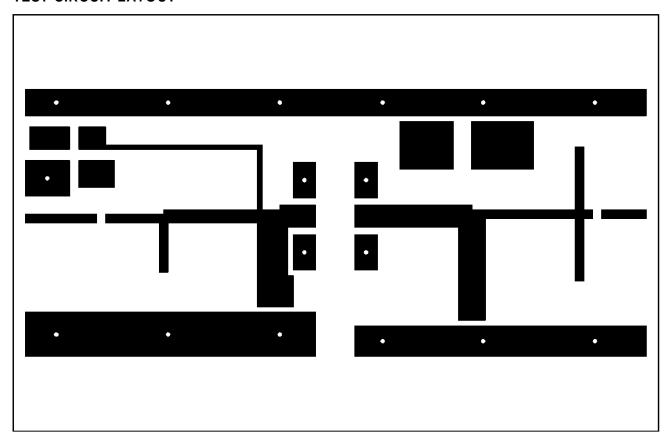
IMPEDANCE DATA



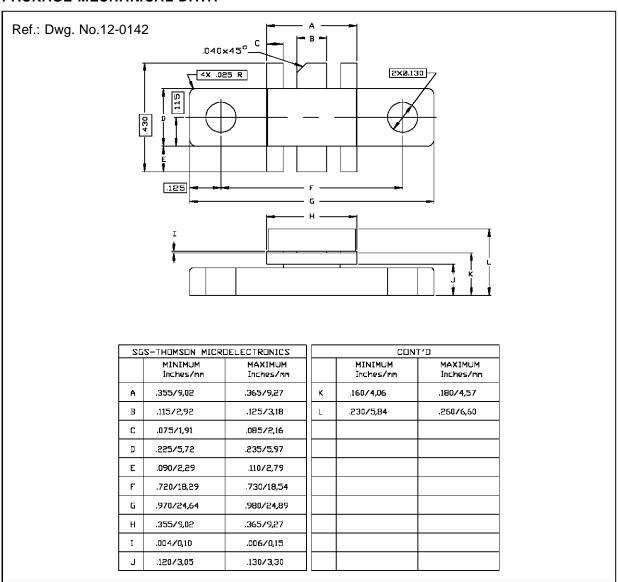
TEST CIRCUIT



TEST CIRCUIT LAYOUT



PACKAGE MECHANICAL DATA



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