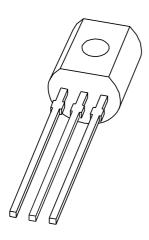
DISCRETE SEMICONDUCTORS

DATA SHEET



BF240NPN medium frequency transistor

Product specification
Supersedes data of 1998 Dec 02

1999 Apr 21





NPN medium frequency transistor

BF240

FEATURES

- Low current (max. 25 mA)
- Low voltage (max. 40 V).

APPLICATIONS

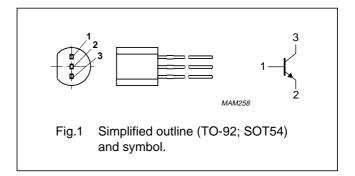
- AM mixers
- IF amplifiers in AM/FM receivers.

DESCRIPTION

NPN medium frequency transistor in a TO-92; SOT54 plastic package.

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	40	V
V _{CEO}	collector-emitter voltage	open base	_	40	٧
V_{EBO}	emitter-base voltage	open collector	_	4	٧
Ic	collector current (DC)		_	25	mA
I _{CM}	peak collector current		_	25	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	300	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

Philips Semiconductors Product specification

NPN medium frequency transistor

BF240

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	420	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = 20 V	_	_	100	nA
		$I_E = 0$; $V_{CB} = 20 \text{ V}$; $T_{amb} = 150 ^{\circ}\text{C}$	_	_	4	μΑ
I _{EBO}	emitter cut-off current	$I_C = 0; V_{EB} = 4 V$	_	_	100	nA
h _{FE}	DC current gain	I _C = 1 mA; V _{CE} = 10 V	67	_	220	
V _{BE}	base-emitter voltage	I _C = 1 mA; V _{CE} = 10 V	675	725	775	mV
C _{re}	feedback capacitance	I _C = 0; V _{CB} = 10 V; f = 1 MHz	_	_	0.5	pF
f _T	transition frequency	I _C = 1 mA; V _{CE} = 10 V; f = 100 MHz	150	_	_	MHz

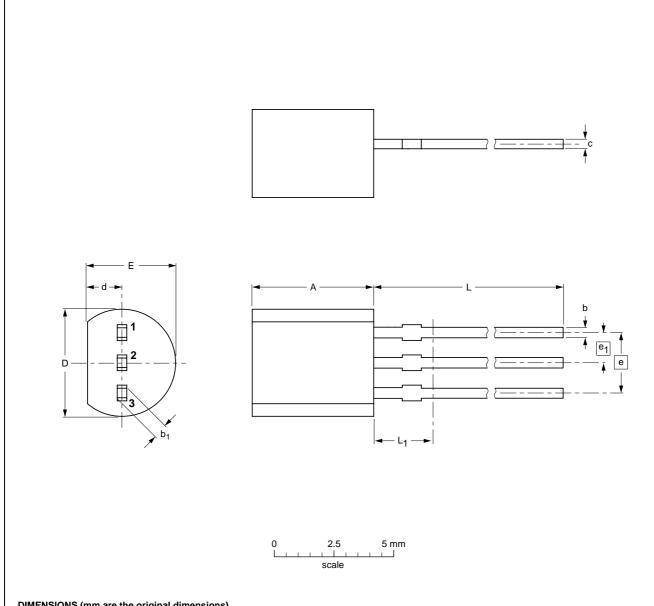
NPN medium frequency transistor

BF240

PACKAGE OUTLINE

Plastic single-ended leaded (through hole) package; 3 leads

SOT54



DIMENSIONS (mm are the original dimensions)

UNIT	A	b	b ₁	С	D	d	E	е	e ₁	L	L ₁ ⁽¹⁾
mm	5.2 5.0	0.48 0.40	0.66 0.56	0.45 0.40	4.8 4.4	1.7 1.4	4.2 3.6	2.54	1.27	14.5 12.7	2.5

Note

1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

OUTLINE		REFERENCES			EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT54		TO-92	SC-43			97-02-28

Product specification Philips Semiconductors

NPN medium frequency transistor

BF240

DEFINITIONS

Data Sheet Status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

Limiting values

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.

1999 Apr 21 5 Philips Semiconductors Product specification

NPN medium frequency transistor

BF240

NOTES

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NPN medium frequency transistor

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Printed in The Netherlands

Date of release: 1999 Apr 21

Document order number: 9397 750 05687

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