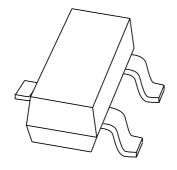
DISCRETE SEMICONDUCTORS

DATA SHEET



PMBT3906 PNP switching transistor

Product specification Supersedes data of 1997 May 05 1999 Apr 27





PNP switching transistor

PMBT3906

FEATURES

- Low current (max. 100 mA)
- Low voltage (max. 40 V).

APPLICATIONS

• Telephony and professional communication equipment.

DESCRIPTION

PNP switching transistor in a SOT23 plastic package. NPN complement: PMBT3904.

MARKING

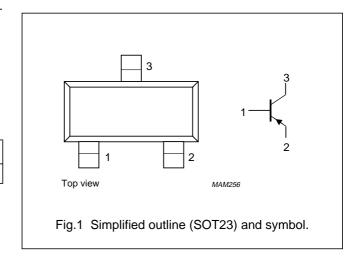
TYPE NUMBER	MARKING CODE(1)
PMBT3906	*2A

Note

* = p : Made in Hong Kong.
 * = t : Made in Malaysia.

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	-40	V
V _{CEO}	collector-emitter voltage	open base	_	-40	V
V _{EBO}	emitter-base voltage	open collector	_	-6	V
Ic	collector current (DC)		_	-100	mA
I _{CM}	peak collector current		_	-200	mA
I _{BM}	peak base current		_	-100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	250	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	+150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

Philips Semiconductors Product specification

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

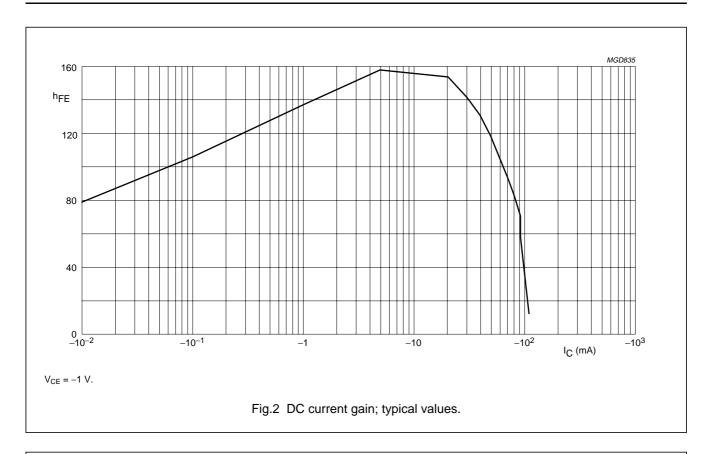
 T_{amb} = 25 $^{\circ}C$ unless otherwise specified.

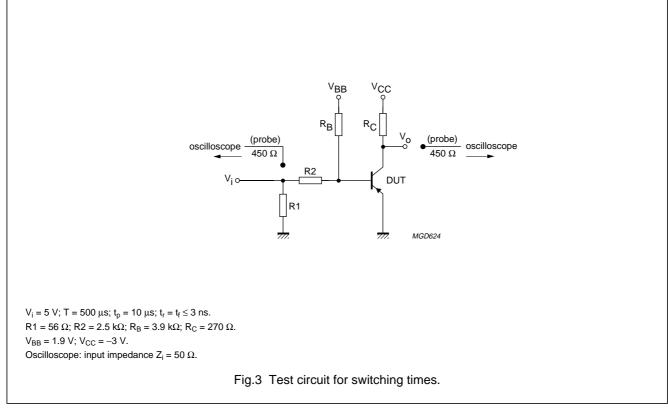
SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = -30 V	_	-50	nA
I _{EBO}	emitter cut-off current	$I_C = 0; V_{EB} = -6 \text{ V}$	_	-50	nA
h _{FE}	DC current gain	V _{CE} = −1 V; (see Fig.2)			
		$I_{C} = -0.1 \text{ mA}$	60	-	
		$I_C = -1 \text{ mA}$	80	-	
		I _C = −10 mA	100	300	
		$I_C = -50 \text{ mA}$	60	-	
		$I_{C} = -100 \text{ mA}$	30	_	
V _{CEsat}	collector-emitter saturation voltage	$I_C = -10 \text{ mA}; I_B = -1 \text{ mA}$	_	-200	mV
		$I_C = -50 \text{ mA}; I_B = -5 \text{ mA}$	_	-200	mV
V_{BEsat}	base-emitter saturation voltage	$I_C = -10 \text{ mA}; I_B = -1 \text{ mA}$	_	-850	mV
		$I_C = -50 \text{ mA}; I_B = -5 \text{ mA}$	_	-950	mV
C _c	collector capacitance	$I_E = i_e = 0$; $V_{CB} = -5$ V; $f = 1$ MHz	_	4.5	pF
C _e	emitter capacitance	$I_C = I_C = 0$; $V_{EB} = -500 \text{ mV}$; $f = 1 \text{ MHz}$	_	10	pF
f _T	transition frequency	$I_C = -10 \text{ mA}$; $V_{CE} = -20 \text{ V}$; $f = 100 \text{ MHz}$	250	_	MHz
F	noise figure	$I_C = -100 \mu A$; $V_{CE} = -5 V$; $R_S = 1 kΩ$; $f = 10 Hz$ to 15.7 kHz	_	4	dB
Switching t	imes (between 10% and 90% levels); (see Fig.3)			
t _{on}	turn-on time	$I_{Con} = -10 \text{ mA}; I_{Bon} = -1 \text{ mA}; I_{Boff} = 1 \text{ mA}$	_	65	ns
t _d	delay time		_	35	ns
t _r	rise time		_	35	ns
t _{off}	turn-off time		_	300	ns
ts	storage time		_	225	ns
t _f	fall time		_	75	ns

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PNP switching transistor

PMBT3906





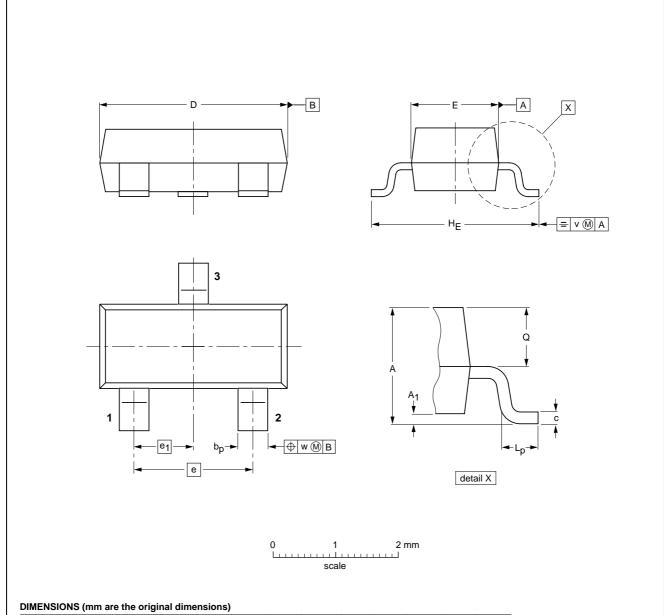
PNP switching transistor

PMBT3906

PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT23



UNIT	Α	A ₁ max.	bp	С	D	E	е	e ₁	HE	Lp	Q	٧	w
mm	1.1 0.9	0.1	0.48 0.38	0.15 0.09	3.0 2.8	1.4 1.2	1.9	0.95	2.5 2.1	0.45 0.15	0.55 0.45	0.2	0.1

OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT23						97-02-28

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PNP switching transistor

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DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.

PNP switching transistor

PMBT3906

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