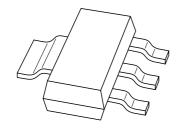
## **DISCRETE SEMICONDUCTORS**

# DATA SHEET



## BSP50; BSP51; BSP52 NPN Darlington transistors

Product specification Supersedes data of 1997 Apr 22 1999 Apr 23





## **NPN Darlington transistors**

**BSP50**; **BSP51**; **BSP52** 

#### **FEATURES**

- High current (max. 1 A)
- Low voltage (max. 80 V)
- Integrated diode and resistor.

#### **APPLICATIONS**

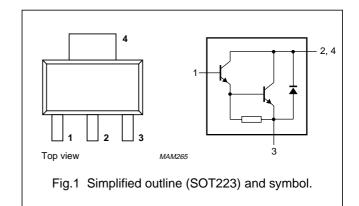
· Industrial high gain amplification.

#### **DESCRIPTION**

NPN Darlington transistor in a SOT223 plastic package. PNP complements: BSP60, BSP61 and BSP62.

#### **PINNING**

PIN	DESCRIPTION
1	base
2,4	collector
3	emitter



#### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter			
	BSP50		_	60	V
	BSP51		_	80	V
	BSP52		_	90	V
V <sub>CES</sub>	collector-emitter voltage	$V_{BE} = 0$			
	BSP50		_	45	V
	BSP51		_	60	V
	BSP52		_	80	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	5	V
I <sub>C</sub>	collector current (DC)		_	1	Α
I <sub>CM</sub>	peak collector current		_	2	Α
I <sub>B</sub>	base current (DC)		_	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C; note 1	_	1.25	W
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

#### Note

1. Device mounted on a printed-circuit board, single sided copper, tinplated, mounting pad for collector 1 cm<sup>2</sup>. For other mounting conditions, see *"Thermal considerations for the SOT223 in the General Part of associated Handbook"*.

## NPN Darlington transistors

BSP50; BSP51; BSP52

#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th j-a</sub>	thermal resistance from junction to ambient	note 1	96	K/W
R <sub>th j-s</sub>	thermal resistance from junction to solder point		17	K/W

#### Note

1. Device mounted on a printed-circuit board, single sided copper, tinplated, mounting pad for collector 1 cm<sup>2</sup>. For other mounting conditions, see "Thermal considerations for the SOT223 in the General Part of associated Handbook".

#### **CHARACTERISTICS**

 $T_j = 25$  °C unless otherwise specified.

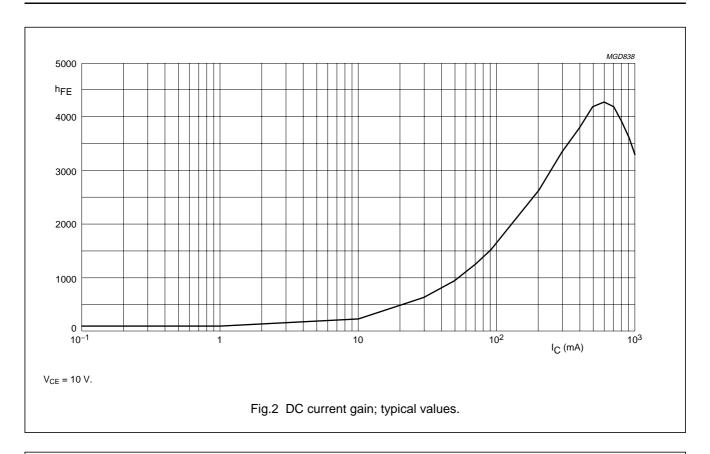
SYMBOL	PARAMETER	PARAMETER CONDITIONS			MAX.	UNIT	
I <sub>CES</sub>	collector cut-off current						
	BSP50	V <sub>BE</sub> = 0; V <sub>CE</sub> = 45 V	_	-	50	nA	
	BSP51	V <sub>BE</sub> = 0; V <sub>CE</sub> = 60 V	_	-	50	nA	
	BSP52	V <sub>BE</sub> = 0; V <sub>CE</sub> = 80 V	_	_	50	nA	
I <sub>EBO</sub>	emitter cut-off current	I <sub>C</sub> = 0; V <sub>EB</sub> = 4 V	_	_	50	nA	
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 10 V; note 1; see Fig.2					
		I <sub>C</sub> = 150 mA	1000	_	_		
		I <sub>C</sub> = 500 mA	2000	-	_		
V <sub>CEsat</sub>	collector-emitter saturation	$I_C = 500 \text{ mA}; I_B = 0.5 \text{ mA}$	_	_	1.3	V	
	voltage	$I_C = 500 \text{ mA}; I_B = 0.5 \text{ mA};$ $T_j = 150 \text{ °C}$	_	_	1.3	V	
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_C = 500 \text{ mA}; I_B = 0.5 \text{ mA}$	_	_	1.9	V	
f <sub>T</sub>	transition frequency	$I_C = 500 \text{ mA}; V_{CE} = 5 \text{ V}; f = 100 \text{ MHz}$	_	200	_	MHz	
Switching ti	Switching times (between 10% and 90% levels); see Fig.3						
t <sub>on</sub>	turn-on time	I <sub>Con</sub> = 500 mA; I <sub>Bon</sub> = 0.5 mA;	_	500	_	ns	
t <sub>off</sub>	turn-off time	$I_{Boff} = -0.5 \text{ mA}$	_	1300	_	ns	

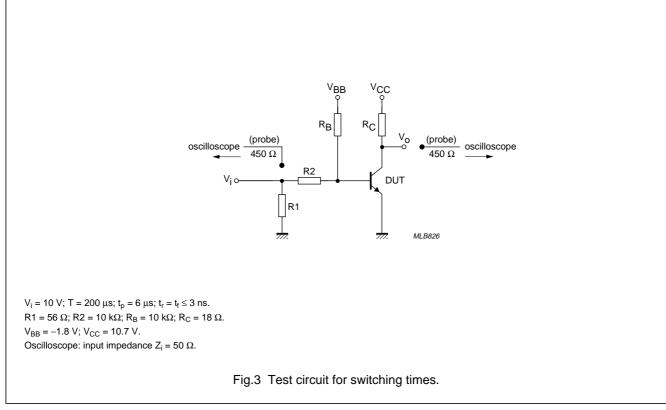
#### Note

1. Pulse test:  $t_p \le 300 \ \mu s; \ \delta \le 0.02.$ 

## NPN Darlington transistors

## BSP50; BSP51; BSP52





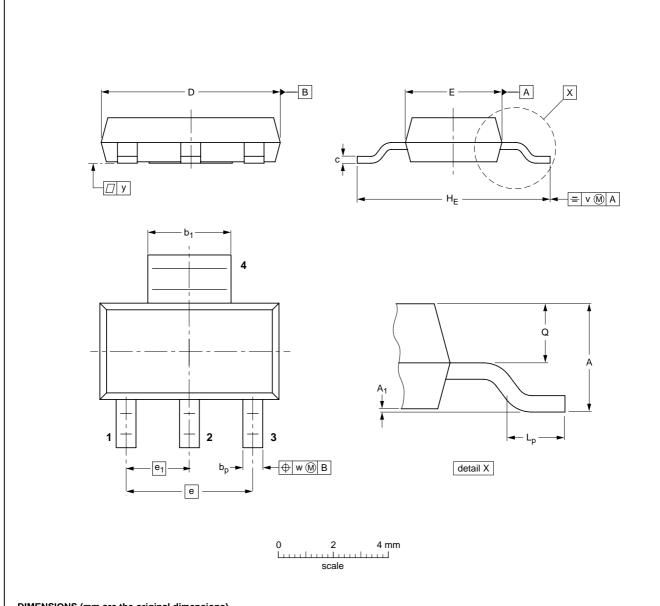
## NPN Darlington transistors

BSP50; BSP51; BSP52

#### **PACKAGE OUTLINE**

Plastic surface mounted package; collector pad for good heat transfer; 4 leads

**SOT223** 



#### DIMENSIONS (mm are the original dimensions)

UNIT	A	A <sub>1</sub>	bp	b <sub>1</sub>	С	D	E	е	e <sub>1</sub>	HE	Lp	Q	٧	w	у
mm	1.8 1.5	0.10 0.01	0.80 0.60	3.1 2.9	0.32 0.22	6.7 6.3	3.7 3.3	4.6	2.3	7.3 6.7	1.1 0.7	0.95 0.85	0.2	0.1	0.1

OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	EIAJ		PROJECTION	
SOT223						<del>96-11-11</del> 97-02-28

### NPN Darlington transistors

BSP50; BSP51; BSP52

#### **DEFINITIONS**

Data Sheet Status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

#### **Application information**

Where application information is given, it is advisory and does not form part of the specification.

#### LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.

## NPN Darlington transistors

BSP50; BSP51; BSP52

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