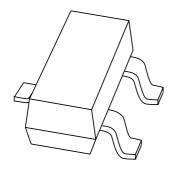
DISCRETE SEMICONDUCTORS

DATA SHEET



BF550PNP medium frequency transistor

Product specification Supersedes data of 1997 Jul 07 1999 Apr 15





PNP medium frequency transistor

BF550

FEATURES

- Low current (max. 25 mA)
- Low voltage (max. 40 V).

APPLICATIONS

Medium frequency applications in thick and thin film circuits.

DESCRIPTION

PNP medium frequency transistor in a SOT23 plastic package.

MARKING

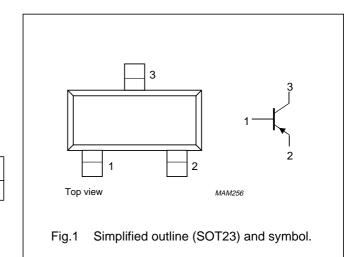
TYPE NUMBER	MARKING CODE ⁽¹⁾		
BF550	LA*		

Note

* = p : Made in Hong Kong.
 * = t : Made in Malaysia.

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	-40	V
V _{CEO}	collector-emitter voltage	open base	_	-40	V
V_{EBO}	emitter-base voltage	open collector	_	-4	٧
I _C	collector current (DC)		_	-25	mA
I _{CM}	peak collector current		_	-25	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	250	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

Philips Semiconductors Product specification

PNP medium frequency transistor

BF550

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = -30 V	_	_	-50	nA
I _{EBO}	emitter cut-off current	$I_C = 0; V_{EB} = -3 \text{ V}$	_	_	-100	nA
h _{FE}	DC current gain	$I_C = -1 \text{ mA}; V_{CE} = -10 \text{ V}$	50	_	_	
V_{BE}	base-emitter voltage	$I_C = -1 \text{ mA}; V_{CE} = -10 \text{ V}$	_	750	_	mV
C _{re}	feedback capacitance	$I_C = -1 \text{ mA}; V_{CB} = -10 \text{ V}; f = 1 \text{ MHz}$	_	0.5	_	pF
f _T	transition frequency	$I_C = -1 \text{ mA}; V_{CE} = -10 \text{ V}; f = 100 \text{ MHz}$	_	325	_	MHz

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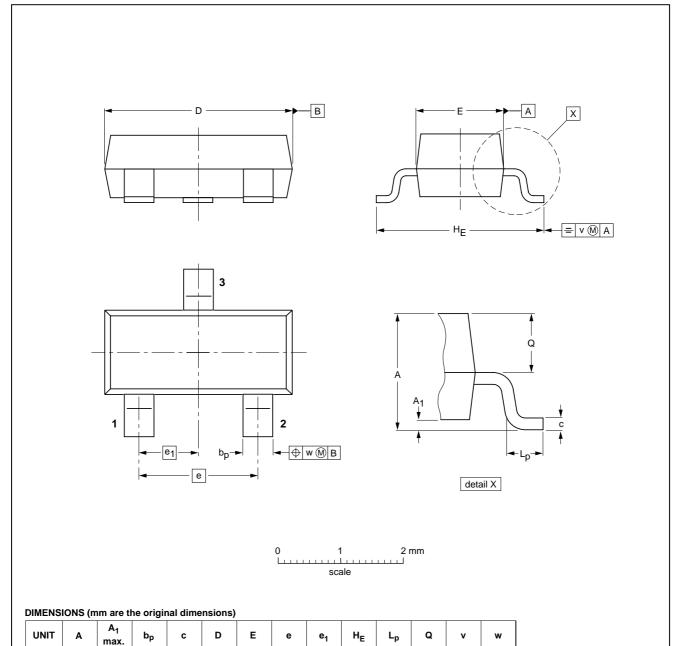
PNP medium frequency transistor

BF550

PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT23



OUTLINE		REFERENCES		EUROPEAN	ICCUE DATE	
VERSION	IEC	JEDEC	EIAJ		PROJECTION ISSUE DATE	
SOT23						97-02-28

1.9

0.45 0.15 0.55 0.45

0.1

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0.48 0.38

0.1

3.0 2.8

0.15

0.09

1.1 0.9

mm

Product specification Philips Semiconductors

PNP medium frequency transistor

BF550

DEFINITIONS

Data Sheet Status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.

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PNP medium frequency transistor

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NOTES

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Printed in The Netherlands

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115002/00/03/pp8

Date of release: 1999 Apr 15

Document order number: 9397 750 05664

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