


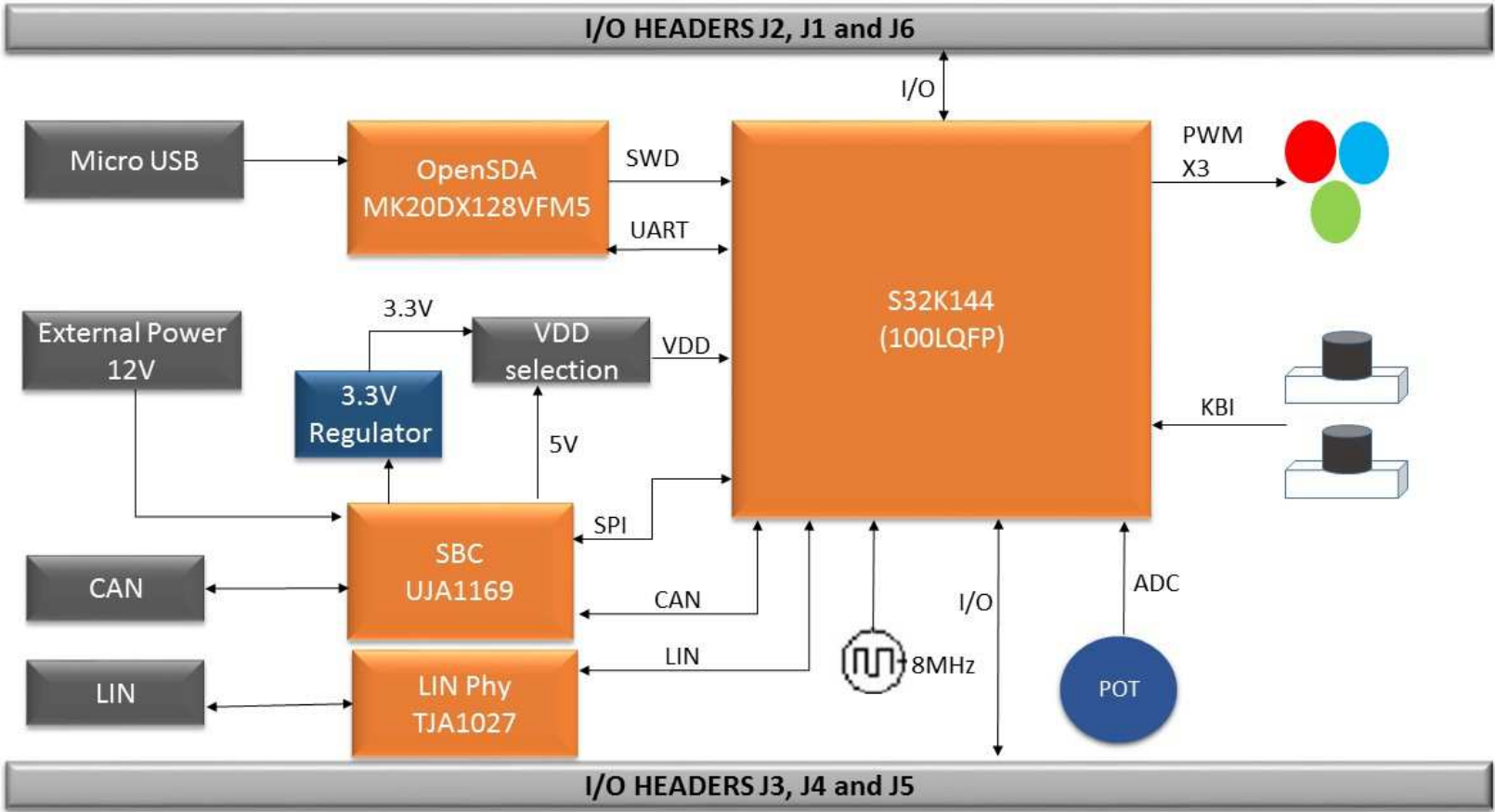
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2	Table of Contents	
2	Notes and Block Diagram	
3	S32K144 MCU	
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5	Power Supply/SWD	
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	1		
Revisions			
Rev	Description	Date	Approved
XA	Initial Release	APR-13-2016	O. Romero
A	Prototype Production	APR-14-2016	O. Romero
AX1	Development	AUG-16-2016	O. Romero
B	2nd Release	SEP-02-2016	O. Romero
B1	Update BOM	DEC-16-2016	O. Romero
C	Update BOM	MAR-1-2018	A. Gonzalez

S32K144EVB-Q100

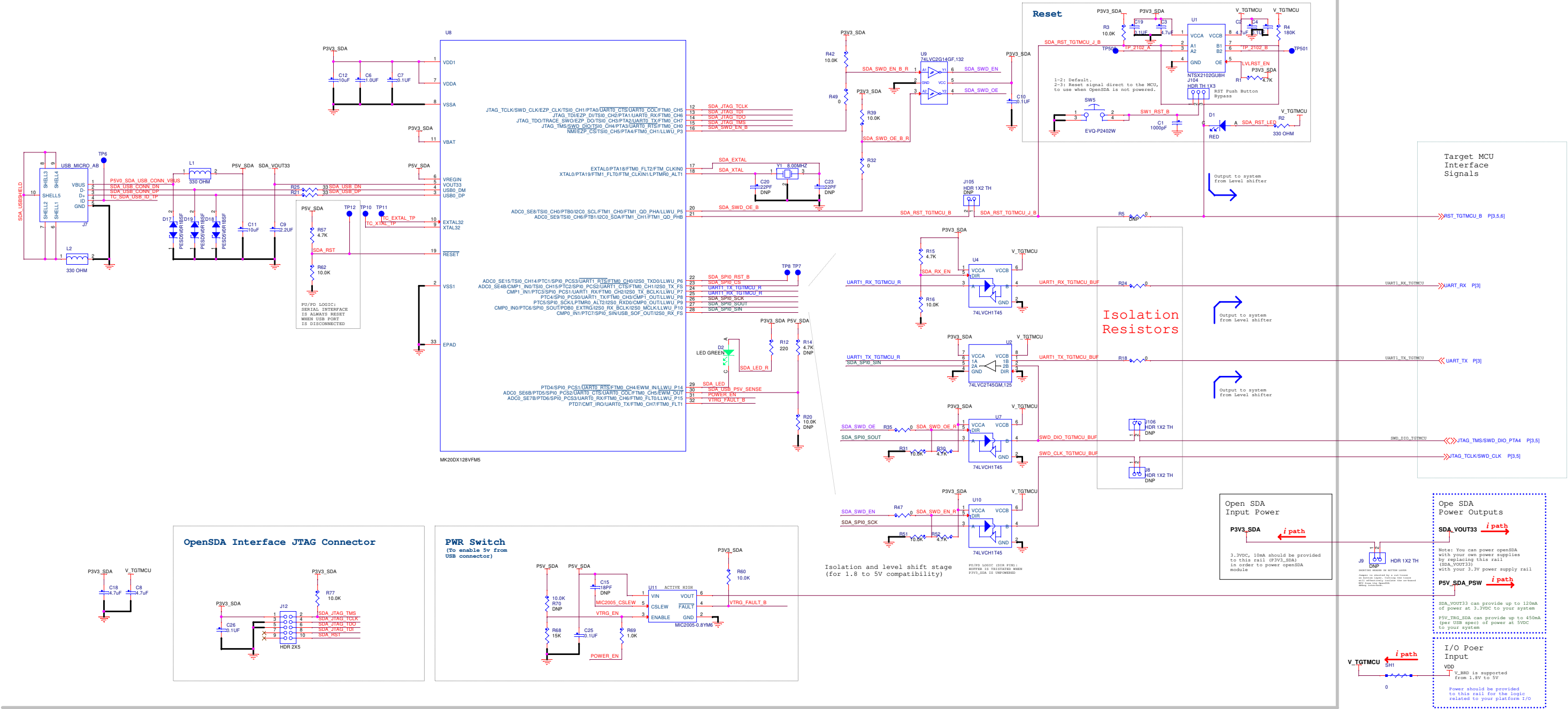
		Automotive Product Group 6501 William Cannon Drive West Austin, TX 78735-8598	
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ICAP Classification:		CP:	IUC: PUBI: X
Designer: Osvaldo Romero	Drawing Title: S32K144EVB-Q100		
Drawn by: Osvaldo Romero	Page Title: TITLE PAGE		
Approved: APPROVER	Size C	Document Number SCH-29248 PDF: SPF-29248	Rev C
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1. Unless Otherwise Specified:
All resistors are in ohms, 1% and 5 %
All capacitors are in uF, 10% , 20 % and 5%
All voltages are DC
All polarized capacitors are aluminum electrolytic
2. Interrupted lines coded with the same letter or letter combinations are electrically connected.
3. Device type number is for reference only. The number varies with the manufacturer.
4. Special signal usage:
_B Denotes - Active-Low Signal
<> or [] Denotes - Vectored Signals
5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

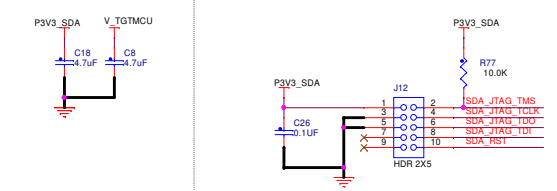


ICAP Classification: CP: IVO: PUBI: X			
Drawing Title: S32K144EVb-Q100			
Page Title: Notes and Block Diagram			
Size C	Document Number SCH-29248 PDF: SPF-29248	Rev C	
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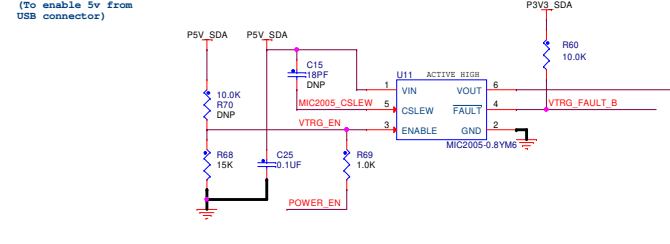
OpenSDA Interface



OpenSDA Interface JTAG Connector

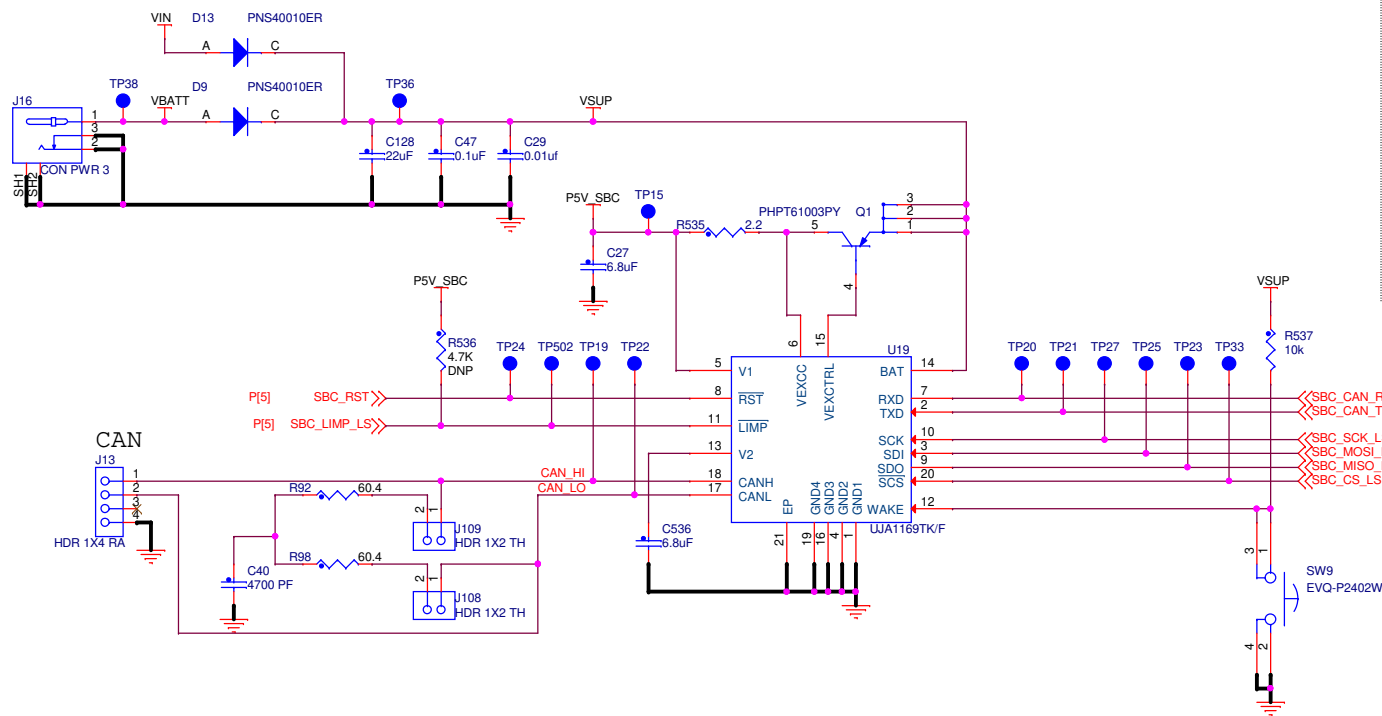


PWR Switch

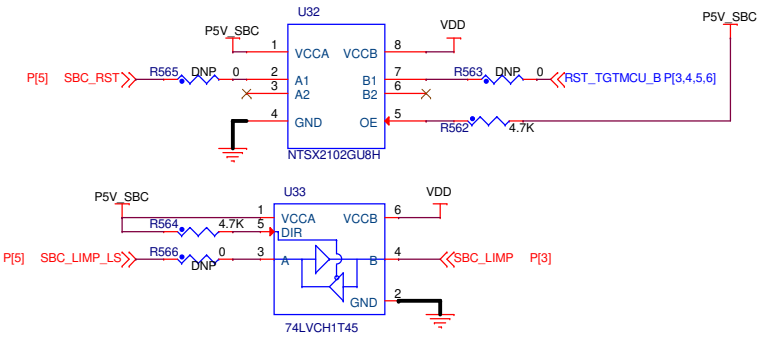
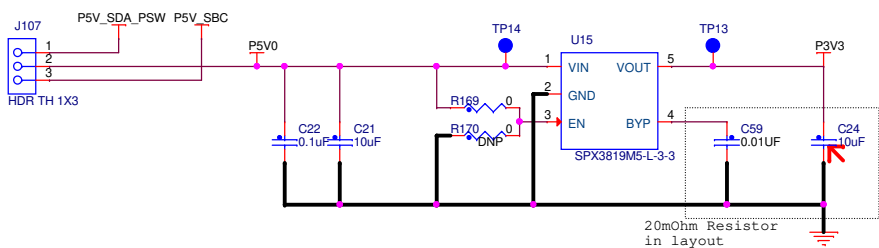


SDA_SPI0_RST_B R41 SDA_SWO_EN
SDA_SPI0_CS R38 SDA_SWO_OE
(For Enablement Purposes Only)

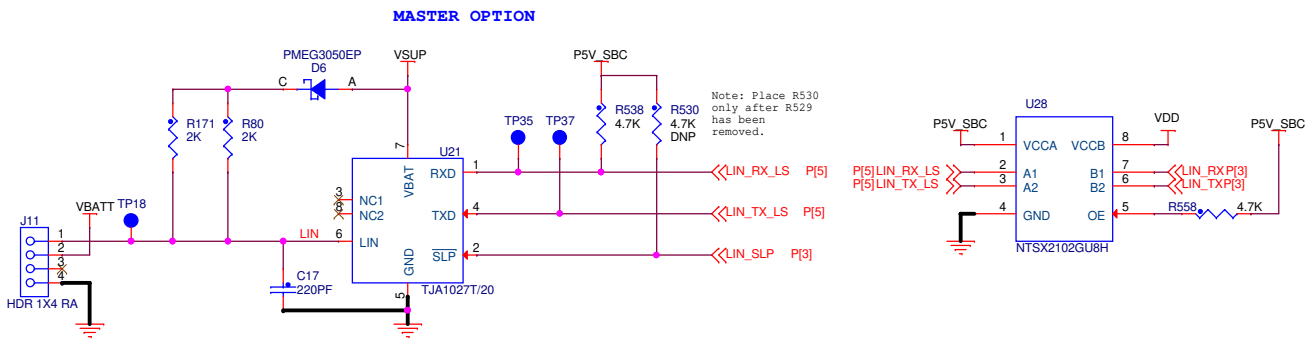
CAN Physical Layer



3.3V LDO Power Supply



LIN Physical Layer



Level Shifters

