

## Contact

[www.linkedin.com/in/sravanthi-vunnam-50536946](https://www.linkedin.com/in/sravanthi-vunnam-50536946) (LinkedIn)

## Top Skills

Physical Design

Static Timing Analysis

Tcl-Tk

# sravanthi vunnam

Master's degree at International Technological University (ITU)  
San Jose, California

## Summary

Experience in physical design using EDI Innovus, ICC2 and IBM tools

–Floor Planning, Place & Route, and clock tree synthesis

–Signoff Timing

– Physical Verification

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## Experience

Eximius Design

Physical Design Engineer - Intern

May 2017 - Present

san jose, california

Understanding complexities in lower technology nodes

Learning scripting languages to implement ASIC backend Physical Implementation flows.

Working on Synopsis ICC2, Red hawk and Caliber tools

Wafer Space

Senior Physical Design Engineer

July 2015 - July 2016 (1 year 1 month)

Bengaluru Area, India

Infotech Enterprises Ltd

Senior Physical Design Engineer

July 2011 - July 2015 (4 years 1 month)

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## Education

International Technological University (ITU)

Master's degree, Electrical and Electronics Engineering · (2016 - 2018)

Jawaharlal Nehru Technological University

B.Tech, ECE · (2007 - 2011)

