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A

Sheetname: MainBoard-CPU\_Decode

File: mainboard.kicad\_sch

MainBoard-MMU

File: mb\_mmu.kicad\_sch

ExpIFC-SIOCTC

File: exp-ifc.kicad\_sch

LOGIC\_PWR\_SPARES

File: logic\_pwr\_spr.kicad\_sch

F

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Sheet: /

File: SD-MSZ.kicad\_sch

Title: SilkyDesign MicroSystem Z80 w/Front Panel

Size: User

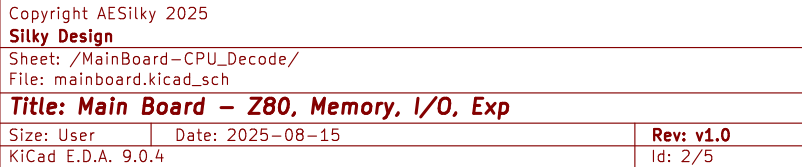
Date: 2025-08-15

Rev: v1.0

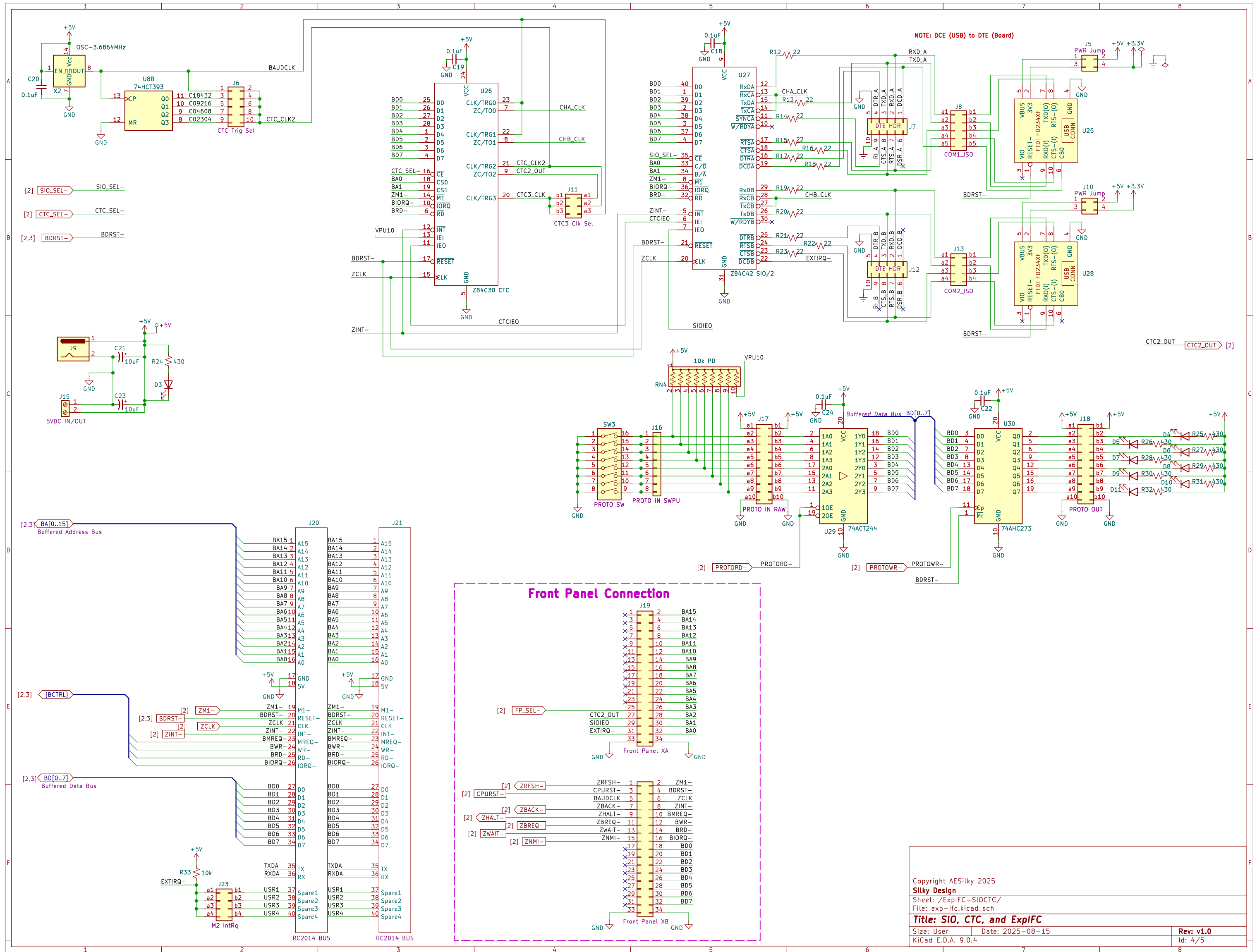
KiCad E.D.A. 9.0.4

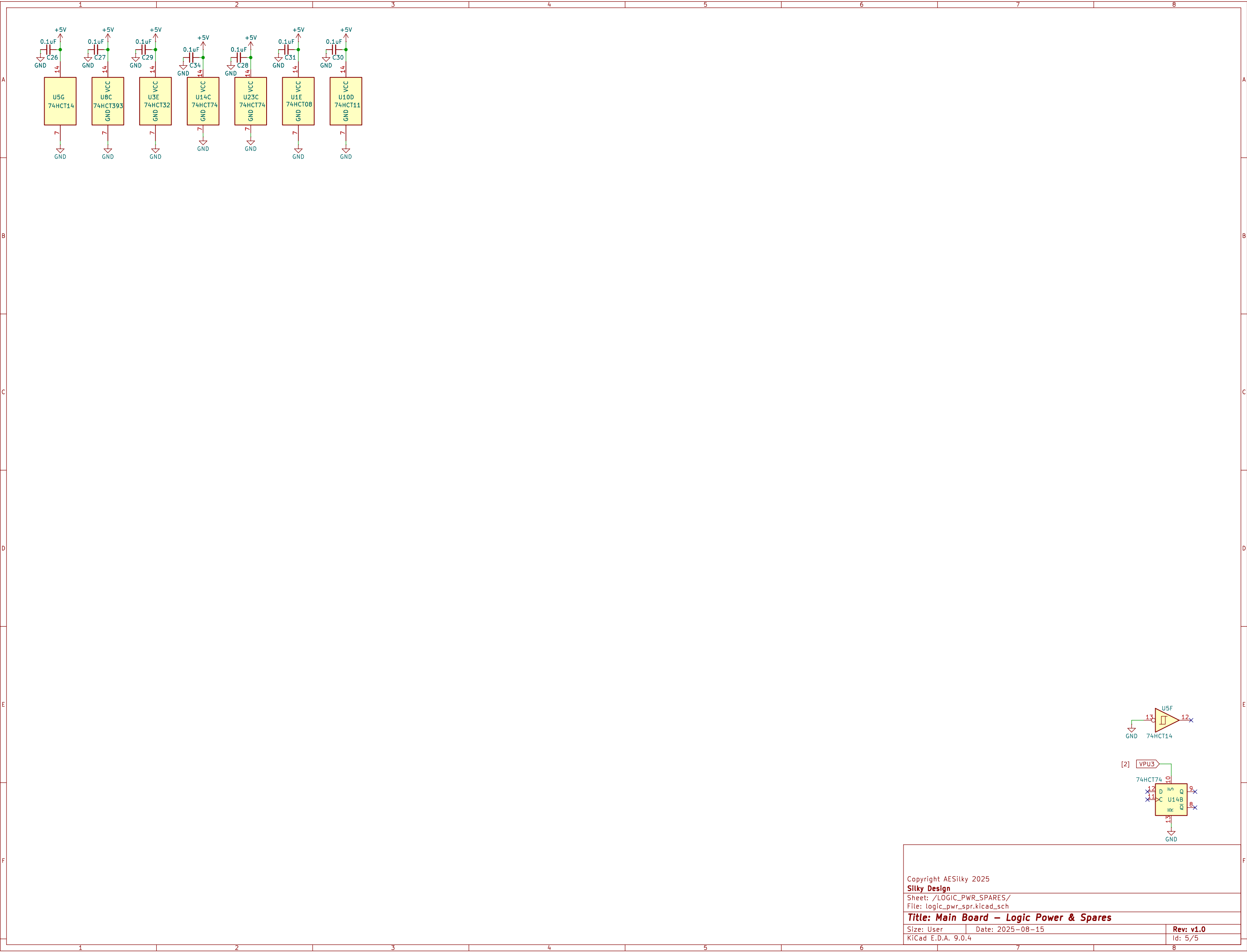
Id: 1/5

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Sheet: /LOGIC\_PWR\_SPARES/

File: logic\_pwr\_spr.kicad\_sch

**Title: Main Board – Logic Power & Spares**

Size: User

Date: 2025-08-15

Rev: v1.0

KiCad E.D.A. 9.0.4

Id: 5/5