PORT READ USING: IN A, (C)

Timing diagram signals: CLK2, CLK, A₀-A₁₅, MREQ-, IORQ-, RD-, WR-, M1-, D₀-D₇, RFSH-

Labels visible in diagram:
- T1, 2, 3, 4, T1, 2, 3, 4, TWA, T3, T1, T1
- A₀-A₁₅: PC, RA, PC, RA, PC, Port Addr, PC
- IORQ-: SWITCH TO CLK2, SWITCH TO CLK
- M1-: IF₁, IF₂, IN A, (C)
- D₀-D₇: ED, 78, DATA, NOP
- 295 ns