		 				8
Sheetname: MainBoard-CPU_Decode	MainBoard-MMU					
File: mainboard.kicad_sch	File: mb_mmu.kicad_sch					
ExpIFC – SIOCTC	LOGIC_PWR_SPARES					
File: exp-ifc.kicad_sch	File: logic_pwr_spr.kicad_sch					
			MicroSystem	Z80 w/Front Panel & RC2014 Bus		
			Copyright 20 <b>SilkyDESIGN</b> Sheet: / File: SD-MSZ	ZO AESILKY		
			Sheet: /	<del></del>		
			File: SD-MSZ	2.kicad_sch	& DC201/4 Duc	
			Cizo Hos	roSystem Z80 w/Front Panel	C KCZUI4 DUS	Dav.
			KiCad F D A	Date: 2025-09-22		<b>Rev: v</b> Id: 1/







