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Kassow Robots PROFINET

Version: 1.0.0 EN1

Kassow Robots: Profinet

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1 Introduction

Profinet (usually styled as PROFINET, as a portmanteau for Process Field Net) is an industry technical standard for data communication over Industrial Ethernet, designed for collecting data from, and controlling equipment in industrial systems, with a particular strength in delivering data under tight time constraints. The standard is maintained and supported by Profibus & Profinet International [de], an umbrella organization headquartered in Karlsruhe, Germany.

KassowRobots RCs provide the access to this realtime field bus by the use of specialised hardware extension (Hilscher cifX), available only for the dedicated controllers (it is not a part of the standard product KR RC).

The Profinet extension handles all necessary realtime requirements for the fieldbus and allows to integrate KassowRobots controllers with the existing Profinet infrastructures.

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2 Kassow Profinet

2.1 Structure

2.1.1 IO Modules

The robot Profinet data are organized into **12** modules. **7** modules serve for sharing the data from the robot to a PLC while **5** modules can be used to send data from a PLC to the robot. All modules are optional, but are fixed to a specific slot.

Slot	Name	Direction	Description
1	System State	Robot → PLC	Monitors robot and safety state
2	Joints Monitor	Robot → PLC	Monitors joint positions and dynamics
3	TCP Monitor	Robot → PLC	Monitors TCP kinematics and dynamics
4	IO Monitor	Robot → PLC	Monitors IO Board and Tool IO state
5	64x Bit Input	Robot → PLC	Input bit register allocated for user data
6	24x Int Input	Robot → PLC	Input integer register allocated for user data
7	24x Real Input	Robot → PLC	Input real register allocated for user data
8	System Control	PLC → Robot	Controls master speed
9	IO Control	PLC → Robot	Controls IO Board and Tool IO outputs
10	64x Bit Output	PLC → Robot	Output bit register allocated for user data
11	24x Int Output	PLC → Robot	Output integer register allocated for user data
12	24x Real Output	PLC → Robot	Output real register allocated for user data

Detailed information about each of the modules can be found in Appendix A.

2.1.2 User defined types

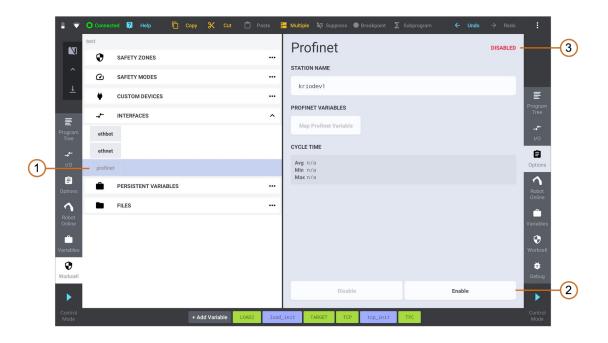
In order to provide easier integration with PLCs, the structure of IO modules has been also expressed via user defined types (UDT). Once the user imports KR datastruct into PLC's programming environment, each module can be mapped into appropriate data type.

Note: For the *Profinet_-KR-_Datastruct.udt file* file, please check with our online documentation or get in touch with our support line.

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2.2 Profinet activation

Profinet needs to be enabled through the teach pendant UI prior its usage.



- 1. Select Profinet interface in Workcell.
- 2. Click enable to activated the Profinet.
- 3. Wait until the status is changed to Connected.



If Profinet interface is not available, your robot is probably not equipped with optional Profinet HW.

2.3 Profinet variables

The robot allows mapping of 6 Profinet modules (bit, int and real user data) into system variables. Each of these modules is represented as an array variable with one column of number data type.

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System Variable	Data Type	Module	Direction	Length
Profinet Bit Output	Array	PLC → Robot	PLC → Robot	24
Profinet Int Output	Array	PLC → Robot	PLC → Robot	64
Profinet Float Output	Array	PLC → Robot	PLC → Robot	64
Profinet Bit Input	Array	Robot → PLC	Robot → PLC	24
Profinet Int Input	Array	Robot → PLC	Robot → PLC	64
Profinet Float Input	Array	Robot → PLC	Robot → PLC	64

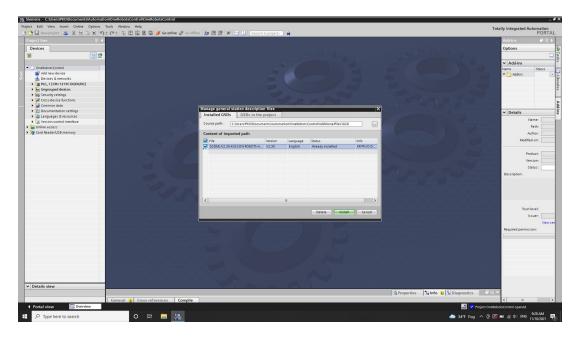
2.3.1 Map Profinet variable

- 1. Select Profinet interface int Workcell.
- 2. Click Map Profinet Variable.
- 3. Select desired mapping, enter variable name and click Create.

2.4 Profinet usage in TIA portal

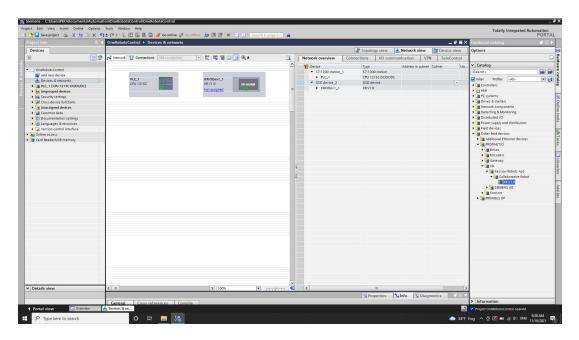
1. Install Kassow Robots GSD file to TIA Portal.

Note: For the *gsdml-v2.35-kassow_robots-axxx-20210602.xml* file, please check with our online documentation or get in touch with our support line.

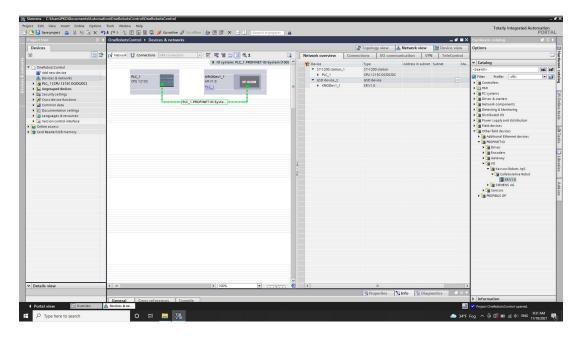


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2. Add KRv1.0 device to network.

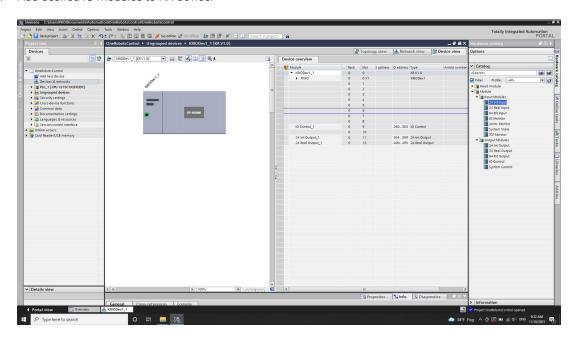


3. Connect KR device to PLC.

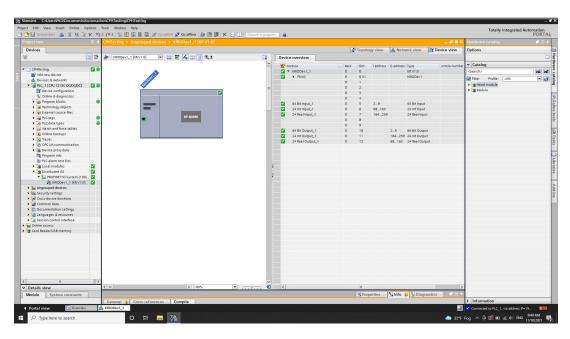


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4. Add desired IO modules to KR device.



- 5. Compile HW configuration.
- 6. Download HW configuration to PLC.
- 7. Go online and check status.



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A Appendix - KR Profinet network frame

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System State

Bit	0		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
0				Majo	or versi	on (uin	t8_t)					Mino	or vers	ion (uin	t8_t)										Rese	erved							
32							-	Robot r	millisec	onds (u	int16_t)								Robe	ot seco	nds (ui	nt8_t)					Rob	ot minu	rtes (uir	1t8_t)		
64				Rot	ot hou	ırs (uin	t8_t)						Rese	erved										Rol	bot day	s (uint	16_t)						
96																Rob	ot cum	ent [A] (float)														
128				Rot	ot mo	de (uin	t8_t)					Rob	bot sta	te (uint	B_t)					Prog	gram st	ate (uir	nt8_t)						Rese	rved			
160	EB	P	В	TB	BB		Rese	rved													Rese	erved											
192																Ma	ster Sp	peed (fle	oat)														
224				Safe	ety mo	de (uin	t8_t)										Rese	erved								ES	PS	SS		F	leserve	d	

Joints Monitor

Bit	0	1	2	3	4	5	6	7	8	9	10	11	12	13	1	14 15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
0															Joi	int 1 posit	ion [rad]	(float))													
32															Joi	int 2 posit	ion [rad]	(float))													
64															Joi	int 3 posit	ion [rad]	(float))													
96															Joi	int 4 posit	ion [rad]	(float))													
128															Joi	int 5 posit	ion [rad]	(float))													
160															Joi	int 6 posit	ion [rad]	(float))													
192															Joi	int 7 posit	ion [rad]	(float))													
224															Join	int 1 veloci	ty [rad/s] (floa	t)													
256															Join	int 2 veloci	ty [rad/s] (floa	t)													
288																int 3 veloci																
320																int 4 veloci																
352																int 5 veloci																
384																int 6 veloci																
416																int 7 veloci			t)													
448																Joint 1 curi																
480																Joint 2 curr																
512																Joint 3 curi																
544																Joint 4 curi																
576																Joint 5 curi																
608																Joint 6 curr																
640																Joint 7 curi																
672																nt 1 tempe																
704																nt 2 tempe																
736																nt 3 tempe																
768																nt 4 tempe																
800																nt 5 tempe																
832																nt 6 tempe																
864															Join	nt 7 tempe	rature [°	C] (flo	at)													

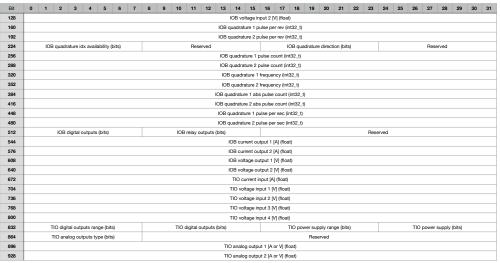
TCP Monitor

Bit	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	4 15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
0															TC	P positio	n X [m]	(float)														
32															TC	P positio	n Y [m]	(float)														
64															TC	P positio	n Z [m]	(float)														
96															TCP	position	OR [ra	f] (float))													
128															TCP	position	OP [rai	[] (float))													
160															TCP	position	OY [rai	[] (float))													
192															TCI	P velocity	/ X [m/s	(float)														
224															TCI	P velocity	/ Y [m/s	(float)														
256															TCI	P velocity	/ Z [m/s	(float)														
288															TCP	velocity	OR [rad	's] (floa	t)													
320															TCP	velocity	OP [rad	's] (floa	t)													
352															TCP	velocity	OY [rad	's] (floa	t)													
384															Т	TCP force	X [N] (loat)														
416															Т	CP force	Y [N] (1	loat)														
448															Т	TCP force	Z [N] (loat)														
480															TC	P torque	X [N.m	(float)														
512															TC	P torque	Y [N.m	(float)														
544															TC	P torque	Z [N.m	(float)														
576															TCI	P force s	calar [N	(float)														

IO Monitor

Bit	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
0							IOE	digita	l inputs	(bits)							IOB s	afe inp	uts (bits	i)								Rese	rved			
32															IOB cu	rent in	put 1 [/	A] (float)													
64															IOB cu	rent in	put 2 [A	A] (float)													
96															IOB vo	tage in	put 1 [\	V] (float	t)													

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64 Bit Input

Bit	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
0															0 to 31	bit inpu	ıt regis	ter (bits	s)													
32														3	32 to 63	bit inp	ut regis	ster (bits	s)													

24 Int Input

Bit	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
0																Int 0 (int32_t)															
32																Int 1 (int32_t)															
64																Int 2 (int32_t)															
96																Int 3 (int32_t)															
128																Int 4 (int32_t)															
160																Int 5 (int32_t)															
192																Int 6 (int32_t)															
224																Int 7 (int32_t)															
256																Int 8 (int32_t)															
288																Int 9 (int32_t)															
320																Int 10	(int32_t)															
352																	(int32_t)															
384																Int 12	(int32_t)															
416																	(int32_t)															
448																	(int32_t)															
480																	(int32_t)															
512																	(int32_t)															
544																	(int32_t)															
576																	(int32_t)															
608																	(int32_t)															
640																	(int32_t)															
672																	(int32_t)															
704																	(int32_t)															
736																Int 23	(int32_t)															

24 Real Input

Bit	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
0																Real 0	(int32_t)															
32																Real 1	(int32_t))														
64																Real 2	(int32_t))														
96																Real 3	(int32_t))														
128																Real 4	(int32_t))														
160																Real 5	(int32_t))														
192																Real 6	(int32_t))														
224																Real 7	(int32_t))														
256																Real 8	(int32_t)	1														
288																Real 9	(int32_t))														

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Bit	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
320																Real 10) (int32_	t)														
352																Real 1	1 (int32_	t)														
384																Real 12	2 (int32_	t)														
416																Real 13	3 (int32_	t)														
448																Real 1	4 (int32_	t)														
480																Real 1	5 (int32_	t)														
512																Real 16	6 (int32_	t)														
544																Real 1	7 (int32_	t)														
576																Real 1	3 (int32_	t)														
608																Real 19	9 (int32_	t)														
640																Real 20) (int32_	t)														
672																Real 2	1 (int32_	t)														
704																Real 2	2 (int32_	t)														
736																Real 2	3 (int32_	t)														

System Control

Bit	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
0	SSM															F	Reserve	d														
32																Master	Speed															

IO Control

Bit	0	1	2	3		4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	7 28	29	30	31
0			IOB di	gital out	tput	s masi	k (bits	5)				IOB rela	ay out	puts ma	sk (bits)				IOB	digital	output	(bits)					IOB	relay	outputs	(bits)		
32			IOB cu	rrent ou	tpu	ts mas	sk (bit	s)			- 1	OB volta	ige ou	ıtputs m	ask (bit	s)									Rese	erved							
64															k	ОВ си	rrent ou	tput 1	[A] (flo	oat)													
96	IOB current output 2 [A] (float) IOB voltage output 1 [V] (float)																																
128		IOB voltage output 1 [V] (float)																															
160															Ю)B vo	ltage ou	tput 2	[V] (flc	oat)													
192			TIO di	gital out	tput	s masl	k (bits	s)				TIO pov	ver su	pply ma	sk (bits)				TIO dig	ital out	puts ra	nge (bit	s)				TIO pov	ver s	supply ran	ge (bits	i)	
224			TIC	digital	out	puts (b	oits)					TIO	power	supply	(bits)										Rese	erved							
256			TIO an	alog ou	tput	s mas	k (bit	3)				TIO ana	log ou	tputs ty	pe (bits)									Rese	erved							
288															TIC	anal	og outpi	ıt 1 [A	or V] ((float)													
320															TIC	anal	og outpi	ıt 2 [A	or V] ((float)													

64 Bit Output

Bit	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
0														C	to 31 b	oit outp	ut regi:	ster (bit	s)													
32														3	2 to 63	bit outp	out regi	ister (bi	ts)													

24 Int Output

Bit	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
0																Int 0 (in	nt32_t)															
32																Int 1 (in	nt32_t)															
64																Int 2 (in	nt32_t)															
96																Int 3 (in	nt32_t)															
128																Int 4 (in	nt32_t)															
160																Int 5 (in	nt32_t)															
192																Int 6 (in	nt32_t)															
224																Int 7 (in	nt32_t)															
256																Int 8 (in	nt32_t)															
288																Int 9 (in	nt32_t)															
320																Int 10 (int32_t)															
352																Int 11 (int32_t)															
384																Int 12 (
416	_															Int 13 (
448	_															Int 14 (
480																Int 15 (
512																Int 16 (
544																Int 17 (
576																Int 18 (
608	_															Int 19 (
640																Int 20 (
672																Int 21 (
704																Int 22 (
736																Int 23 (int32_t)															

24 Real Output

ыт	U	1	2	3	4	5	ь	-	8	9	10	11	12	13	14	15	16	1/	18	19	20	21	22	23	24	25	26	21	28	29	30	31
0																Real 0 (int32_t	t)														

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Bit	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
32																Real 1	(int32_t	,														
64																Real 2	(int32_t)														
96																Real 3	(int32_t)														
128																Real 4	(int32_t)														
160																Real 5	(int32_t)														
192																Real 6	(int32_t)														
224																Real 7	(int32_t	1														
256																Real 8	(int32_t)														
288																Real 9	(int32_t)														
320																Real 10		_														
352																Real 11		_														
384																Real 12		_														
416																Real 13																
448																Real 14																
480																Real 15																
512																Real 16																
544																Real 17																
576																Real 18		_														
608																Real 19																
640																Real 20																
672																Real 21																
704																Real 22																
736																Real 23	(int32_	t)														