Revison 1.0, Mrz 2020 - Entwurf P.T. auf Grundlage SmartEVSE Revision 1.1, Aug 2020

- Diverse kleinere Änderungen (Lock Widerstände, Pin-Belegungen, mehr Überspannungsdioden, Taster Vorwiderstand, etc) - Layout/Bestückung (ESP32 auf der Unterseite, getrennte Schraubklemmen) Revision 1.2, Feb 2021 - Tasterbeschaltung, Trenndiode für das Lock Brownouterkennung Sheet: Mains and Power Circuitry Sheet: ATmega4808 and Peripherals Sheet: Analog Section and Low Voltage Outputs RS485 A >RS485 A RS485_B◊ >RS485_B Lock_B< GLock_B Mains Input Connector SSR_L1< SSR_L1 Lock_RD >Lock_R 12V Power Supply 3.3V DC Converter SSR_L2 assr_L2 >Lock_W Lock_WD -12V Voltage Inverter SSR_L3 SSR_L3 Analog Section
-Opamp Buffer
- Comparator
Low Voltage I/O Connector ATmega4808 ESP32 RS485 Transceiver Lock Actuator >LED □Button Button< BOTE >BOT ДPР Signal Relays CPK аср File: mains_and_power.sch PWM_OutD >PWM_Out >Signal_Relay Signal_RelayD CT1< аст2 CT2< **СТ3** CT3< File: atmega4808_and_peripherals.sch File: analog_section_low_voltage.sch Sheet: / File: agccs-ctrl22.sch Title: AGCCS-Ctrl22 Size: A4 Date: 2021-02-15 Rev: 1.2 KiCad E.D.A. kicad (5.1.9-0-10_14) ld: 1/4





