Institut Universitaire des Sciences (IUS)

Faculté des Sciences et Technologies (FST)

RAPPORT SUR LE TRAVAIL DE LABORATOIRE № 3

Cours : Cisco Packet Tracer (Reseau 1)

Soumis au Chargé de cours : Ismael SAINT AMOUR

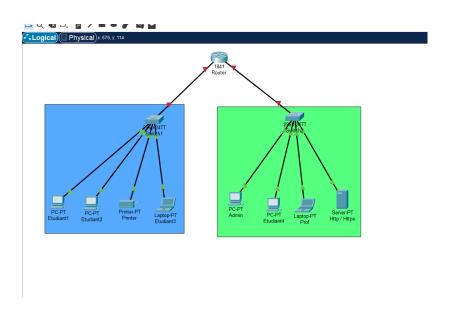
Niveau L3

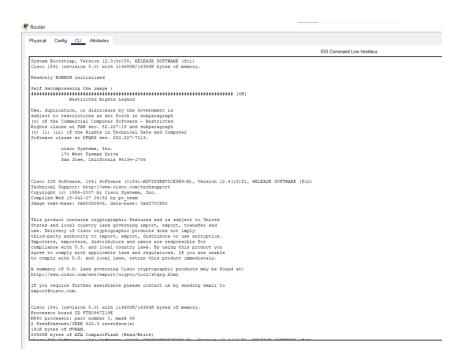
Préparé par : Robaldo BADIO

Date: Le 17/11/2024

Exécution du TD 3 #1

1. Processus sur quelques commandes de base et utilitaires sur Cisco Packet Tracer.





```
Physical Config CL Attributes

If you require further assistance please contact us by sending email to export@closs.com.

Cisco 1891 (revision 5.0) with 114688K/16384K bytes of memory.
Processor board ID FTX0947218E

MR60 processor board ID FTX0947218E

MR60 processor part number 0, mask 49
2 FastEtherner/IEEE 80.3 interface(s)
3 Statutherner/IEEE 80.3 interface(s)
4 Statutherner/IEEE 80.3 interface(s)
4 Statutherner/IEEE 80.3 interface(s)
5 Stotware, 1891 Software (1691-AUVIPSENVICESK9-H), Version 12.4(15)T1, RELEASE SOFTWARE (fc2)
1 Technical Support inter/Now.icac.com/cebnsport
Compute Wed 5-Roll-07 90151b pyr_lease

--- System Configuration Dialog ---

Would you like to enser the initial configuration dialog? [yes/no]: no

Press RETURN to get started!

Router-weakle
Route
```

P Switch1

```
flashfs[2]: 0 orphaned files, 0 orphaned directories
flashfs[2]: Total bytes: 32514048
flashfs[2]: Bytes used: 11952128
flashfs[2]: Bytes available: 20561920
 flashfs[2]: flashfs fsck took 2 seconds.
 flashfs[2]: Initialization complete....done Initializing flashfs.
Checking for Bootloader upgrade..
Boot Loader upgrade not required (Stage 2)
POST: CPU MIC register Tests : Begin
POST: CPU MIC register Tests : End, Status Passed
POST: CPU MIC register Tests: End, Status Passed
POST: POTLASIC Memory Tests: Begin
POST: POTLASIC Memory Tests: End, Status Passed
POST: CPU MIC interface Loopback Tests: Begin
POST: CPU MIC interface Loopback Tests: End, Status Passed
POST: POTLASIC RingHoopback Tests: End, Status Passed
POST: POTLASIC RingHoopback Tests: Begin
POST: POTLASIC CAM Subsystem Tests: End, Status Passed
POST: POTLASIC CAM Subsystem Tests: End, Status Passed
POST: POTLASIC POTL Loopback Tests: End;
POST: POTLASIC POTL Loopback Tests: End;
POST: POTLASIC POTL Loopback Tests: End, Status Passed
Waiting for Port download...Commetee
 Waiting for Port download...Complete
 This product contains cryptographic features and is subject to United
States and local country laws governing import, export, transfer and use. Delivery of Cisco cryptographic products does not imply
third-party authority to import, export, distribute or use encryption. 
Importers, exporters, distributors and users are responsible for compliance with U.S. and local country laws. By using this product you agree to comply with applicable laws and regulations. If you are unable
to comply with U.S. and local laws, return this product immediately.

A summary of U.S. laws governing Cisco cryptographic products may be found at:
http://www.cisco.com/wwl/export/crypto/tool/stqrg.html
If you require further assistance please contact us by sending email to
 export@cisco.com.
 cisco WS-C2960-24TT-L (PowerPC405) processor (revision B0) with 65536K bytes of memory.
Processor board ID FOC1010X104
Last reset from power-on
 l Virtual Ethernet interface
24 FastEthernet interfaces
2 Gigabit Ethernet interfaces
 The password-recovery mechanism is enabled.
64K bytes of flash-simulated non-volatile configuration memory.
Base ethernet MAC Address : 00:40:08:A0:82:88
 Motherboard assembly number
                                                                  : 73-10390-03
Power supply part number
Motherboard serial number
                                                                  : 341-0097-02
: FOC10093R12
 Power supply serial number
                                                                  : AZS1007032H
Model revision number
Motherboard revision number
                                                                  : B0
Model number
                                                                 : WS-C2960-24TT-L
System serial number
                                                               : FOC1010X104
```

Physical Config CLI Attributes

```
Switch Ports Model
                                      SW Version
                                                                SW Image
      1 26
              WS-C2960-24TT-L 15.0(2)SE4
                                                                C2960-LANBASEK9-M
Cisco IOS Software, C2960 Software (C2960-LANBASEK9-M), Version 15.0(2)SE4, RELEASE SOFTWARE (fcl) Technical Support: http://www.cisco.com/techsupport Copyright (c) 1986-2013 by Cisco Systems, Inc. Compiled Wed 26-Jun-13
Press RETURN to get started!
%LINK-5-CHANGED: Interface FastEthernet0/2, changed state to up
%LINEPROTO-5-UPDOWN: Line protocol on Interface FastEthernet0/2, changed state to up
%LINK-5-CHANGED: Interface FastEthernet0/3, changed state to up
%LINEPROTO-5-UPDOWN: Line protocol on Interface FastEthernet0/3, changed state to up
 %LINK-5-CHANGED: Interface FastEthernet0/4, changed state to up
%LINEPROTO-5-UPDOWN: Line protocol on Interface FastEthernet0/4, changed state to up
%LINK-5-CHANGED: Interface FastEthernet0/5, changed state to up
%LINEPROTO-5-UPDOWN: Line protocol on Interface FastEthernet0/5, changed state to up
%LINK-5-CHANGED: Interface FastEthernet0/1, changed state to up
%LINEPROTO-5-UPDOWN: Line protocol on Interface FastEthernet0/1, changed state to up
 Switch>enable
SwitchFeonit
Enter configuration commands, one per line. End with CNTL/Z.
Switch(config) #hostname Sl
Sl(config) #interface vlan l
S1(config-if) #ip address 192.168.1.2 255.255.255.0 S1(config-if) #no shutdown
%LINK-5-CHANGED: Interface Vlan1, changed state to up
%LINEPROTO-5-UPDOWN: Line protocol on Interface Vlanl, changed state to up
Sl(config-if) #exit
Sl(config) #
```

```
Physical Config CLI Attributes
```

```
C2960 Boot Loader (C2960-HBOOT-M) Version 12.2(25r)FX, RELEASE SOFTWARE (fc4)
Cisco WS-C2960-24TT (RC32300) processor (revision C0) with 21039K bytes of memory.
  Cisco WS-C2960-24TT (RC32300) processor (revision CO) 2960-24TT starting...

Base ethernet MAC Address: 0001.425B.0814

Mondem file system is available.

Initializing Flash...
flashfs[0]: 1 orphaned files, 0 orphaned directories flashfs[0]: 0 orphaned files, 0 orphaned fileshfs[0]: Directories flashfs[0]: Bytes used: 467045S
flashfs[0]: Bytes used: 467045S
flashfs[0]: Bytes available: 59345929
flashfs[0]: flashfs fsck took 1 seconds....done Initializing Flash.
     Boot Sector Filesystem (bs:) installed, fsid: 3
Parameter Block Filesystem (pb:) installed, fsid: 4
  Using 6 percent ionem. [OME/$12Mb]

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Software clause at DFARS sec. 522.227-7013.

170 West Tamman Drive San Jose, California 95134-1706

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Copyright (c) 1996-2013 by Cisco Systems, Inc.

Compiled Wed 2e-Jun-13 02:49 by mnguyen Initializing flashfs...

fosk Disable shadow buffering due to heap fragmentation. flashfs[2]: 2 files, 1 directories flashfs[2]: 1 ovphaned files, 0 orphaned directories flashfs[2]: Bytes used: 1952128 flashfs[2]: flashfs fskc took 2 seconds. flashfs[2]: flashfs fskc took 2 seconds. flashfs[2]: Initialization complete...done Initializing flashfs. Checking for Bootloader upgrade...
```

Physical Config CLI Attributes

IOS Command Line Interface

```
flashfs[2]: flashfs fsck took 2 seconds.
flashfs[2]: Initialization complete...done Initializing flashfs.
Checking for Bootloader upgrade.
Boot Loader upgrade not required (Stage 2)
POST: CPU MIC register Tests : Begin
POST: CPU MIC register Tests : End, Status Passed
POST: PORTABIC Memory Tests : Begin
POST: FORTABIC Memory Tests : Begin
POST: FORTABIC Memory Tests : End, Status Passed
POST: CPU MIC interface Loopback Tests : Begin
POST: CPU MIC interface Loopback Tests : End, Status Passed
POST: FORTABIC RingLoopback Tests : Begin
POST: FORTABIC RingLoopback Tests : Begin
POST: FORTABIC CAM Subsystem Tests : End, Status Passed
POST: FORTABIC CAM Subsystem Tests : Begin
POST: FORTABIC CAM Subsystem Tests : Begin
POST: FORTABIC PORT Loopback Tests : End, Status Passed
Watting for Fort download...Complete
          This product contains cryptographic features and is subject to United
        This product contains cryptographic features and is subject to United States and local country laws governing import, export, transfer and use. Delivery of Cisco cryptographic products does not imply third-party authority to import, export, distribute or use encryption. Importers, exporters, distributors and users are responsible for compliance with U.S. and local country laws. By using this product you agree to comply with applicable laws and regulations. If you are unable to comply with U.S. and local laws, return this product immediately. A summary of U.S. laws governing Cisco cryptographic products may be found at: http://www.cisco.com/www/rexport/crypto/tool/stqrg.html
If you require further assistance please contact us by sending email to
                export@cisco.com.
cisco WS-C2960-24TT-L (PowerPC405) processor (revision B0) with 65536K bytes of memory.
                  Processor board ID FOC1010X104
Last reset from power-on
l Virtual Ethernet interface
     l Virtual Ethernet interfaces
20 FastEthernet interfaces
20 Gigabit Ethernet interfaces
20 Gigabit Ethernet interfaces
21 Gigabit Ethernet interfaces
22 Gigabit Ethernet interfaces
23 Gigabit Ethernet interfaces
24 Gigabit Ethernet interfaces
25 Gigabit Ethernet interfaces
25 Gigabit Ethernet interfaces
25 Gigabit Ethernet interfaces
25 Gigabit Ethernet interfaces
26 Gigabit Ethernet interfaces
27 Gigabit Ethernet interfaces
27 Gigabit Ethernet interfaces
28 Gi
             24 FastEthernet interfaces
```

nyorear coming our reconstruction

fodel revision number

fodel revision number
dotherboard revision number
fodel number
System serial number
Top Assembly Part Number
Top Assembly Revision Number : B0 : WS-C2960-24TT-L

Switch Ports Model SW Version SW Image * 1 26 WS-C2960-24TT-L 15.0(2)SE4

Disco IOS Software, C2960 Software (C2960-LANBASEK9-M), Version 15.0(2)SE4, RELEASE SOFTWARE Fechnical Support: http://www.cisco.com/techsupport Dopyright (c) 1986-2013 by Cisco Systems, Inc. Compiled Wed 26-Jun-13 02:49 by mnguyen

C2960-LANBASEK9-M

Press RETURN to get started!

&LINK-5-CHANGED: Interface FastEthernet0/2, changed state to up

&LINEPROTO-5-UPDOWN: Line protocol on Interface FastEthernet0/2, changed state to up

 $\LINK-5-CHANGED$: Interface FastEthernet0/3, changed state to up

&LINEPROTO-5-UPDOWN: Line protocol on Interface FastEthernet0/3, changed state to up

&LINK-5-CHANGED: Interface FastEthernet0/4, changed state to up

&LINEPROTO-5-UPDOWN: Line protocol on Interface FastEthernet0/4, changed state to up

&LINK-5-CHANGED: Interface FastEthernet0/5, changed state to up

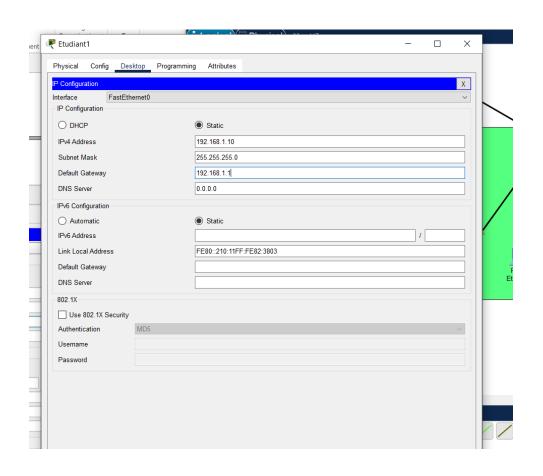
&LINEPROTO-5-UPDOWN: Line protocol on Interface FastEthernet0/5, changed state to up

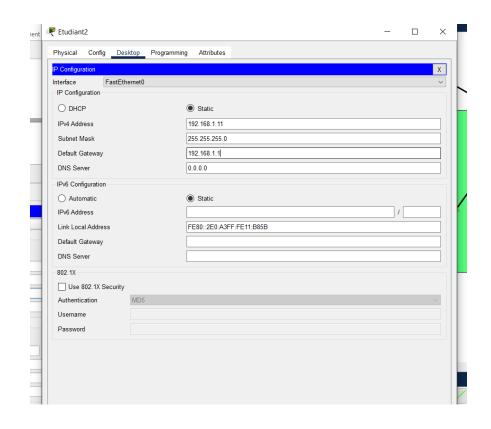
&LINK-5-CHANGED: Interface FastEthernet0/1, changed state to up

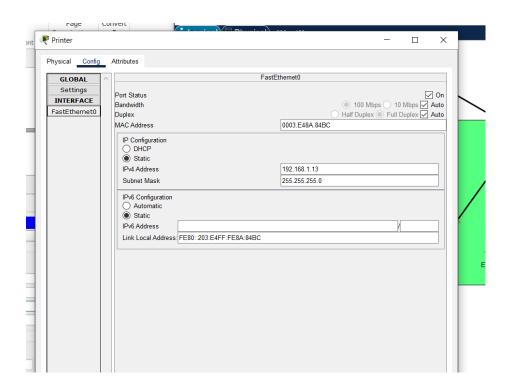
&LINEPROTO-5-UPDOWN: Line protocol on Interface FastEthernetO/1, changed state to up

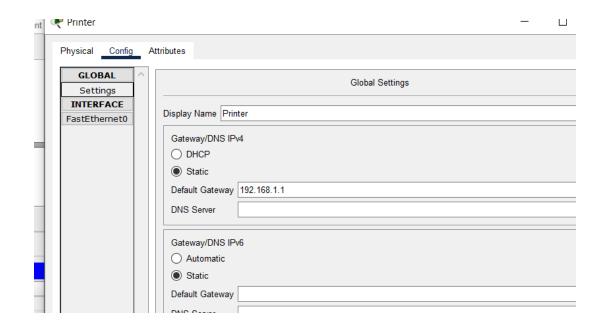
Switch>enable SWitchFood t
Enter configuration commands, one per line. End with CNTL/Z.
SWitch(config) #hostname S2
S2 (config) #interface vlan 1
S2 (config-if) #ip address 192.168.2.2 255.255.255.0

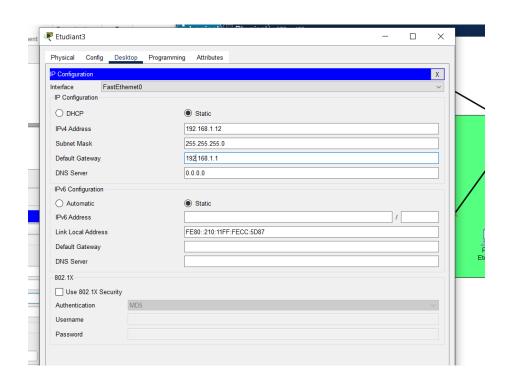
```
%LINK-5-CHANGED: Interface FastEthernet0/2, changed state to up
 LINEPROTO-5-UPDOWN: Line protocol on Interface FastEthernet0/2, changed state to up
%LINK-5-CHANGED: Interface FastEthernet0/3, changed state to up
%LINEPROTO-5-UPDOWN: Line protocol on Interface FastEthernet0/3, changed state to up
%LINK-5-CHANGED: Interface FastEthernet0/4, changed state to up
%LINEPROTO-5-UPDOWN: Line protocol on Interface FastEthernet0/4, changed state to up
%LINK-5-CHANGED: Interface FastEthernet0/5, changed state to up
%LINEPROTO-5-UPDOWN: Line protocol on Interface FastEthernet0/5, changed state to up
%LINK-5-CHANGED: Interface FastEthernet0/1, changed state to up
%LINEPROTO-5-UPDOWN: Line protocol on Interface FastEthernet0/1, changed state to up
Switch#conf t
Enter configuration commands, one per line. End with CNTL/Z. Switch(config) #hostname S2
S2(config) #interface vlan 1
S2(config-if) #ip address 192.168.2.2 255.255.255.0 S2(config-if) #no shutdown
S2(config-if) #
%LINK-5-CHANGED: Interface Vlanl, changed state to up
%LINEPROTO-5-UPDOWN: Line protocol on Interface Vlan1, changed state to up
S2(config-if)#exit
S2(config)#ip default-gateway 192.168.2.1
S2(config)#exit
%SYS-5-CONFIG_I: Configured from console by console
S2#sho arp
Protocol Address
Internet 192.168.2.2
                              Age (min) Hardware Addr Type Inters
- 0001.425B.0814 ARPA Vlanl
                                                                     Interface
```

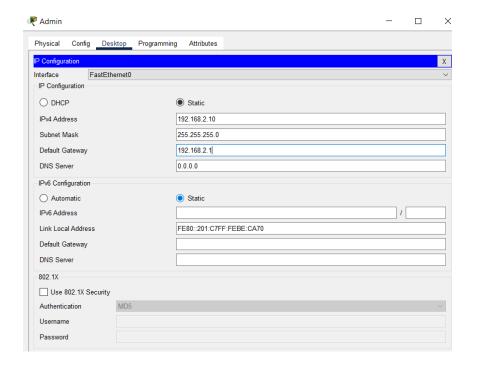


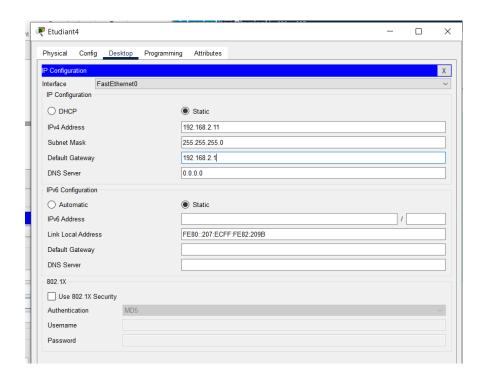


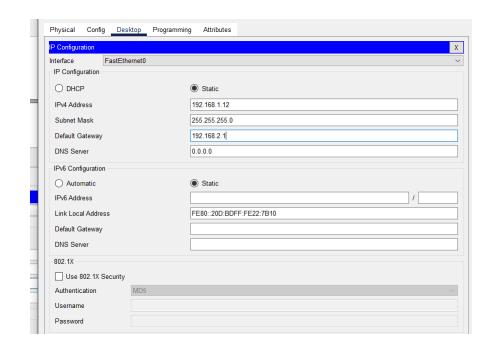


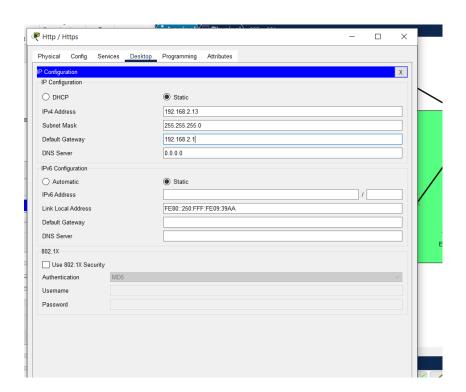


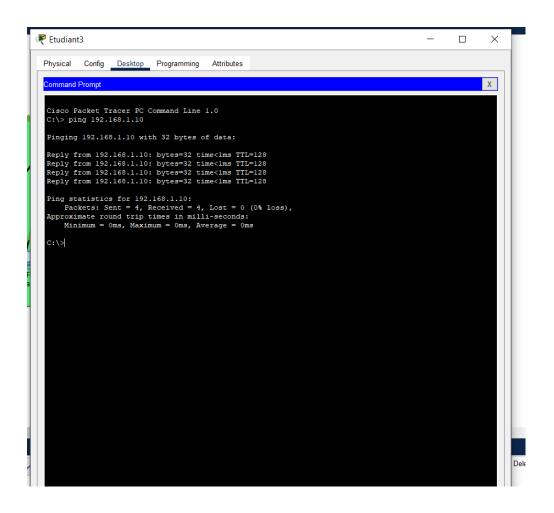


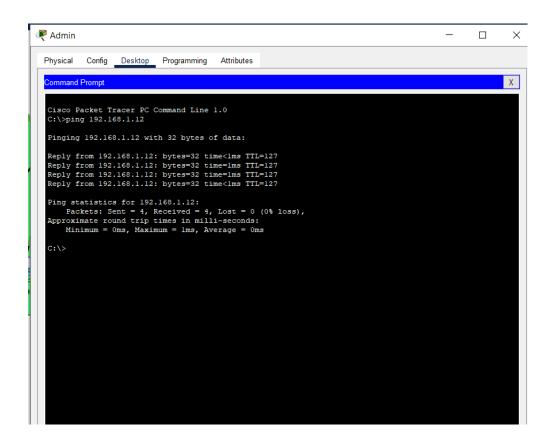






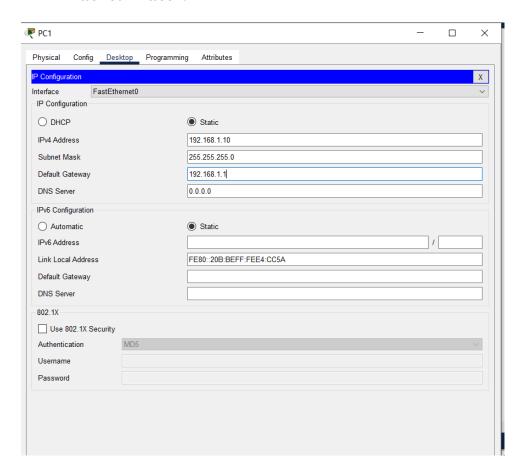


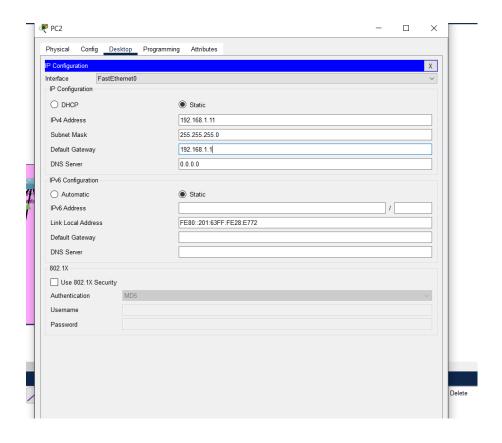


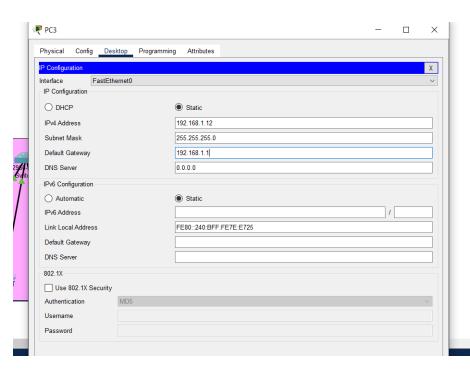


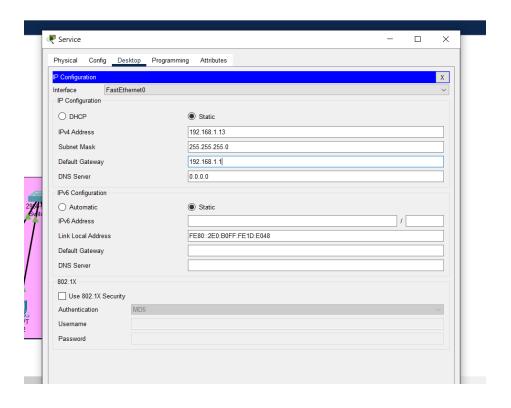
Exécution du TD 3 #2

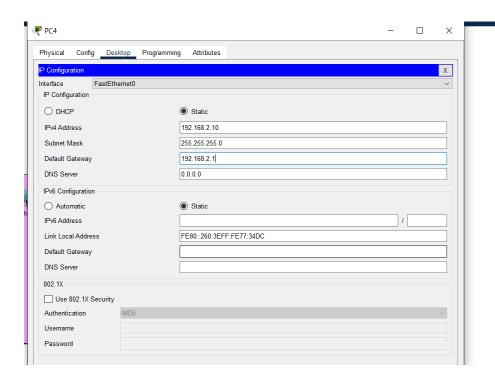
1. Processus sur quelques commandes de base et utilitaires sur Cisco Packet Tracer.

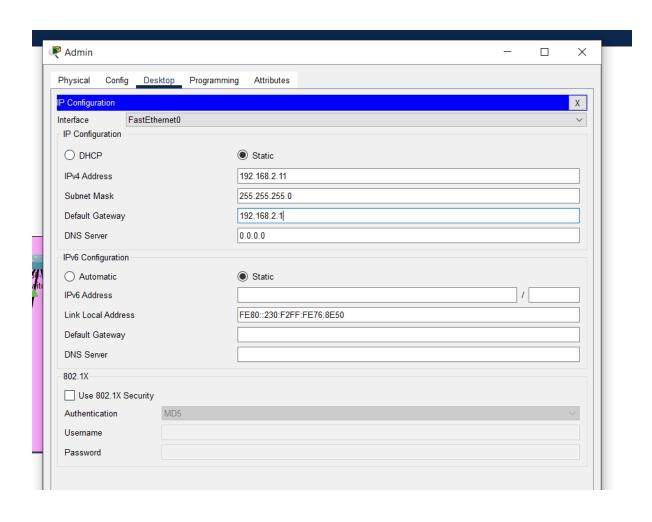


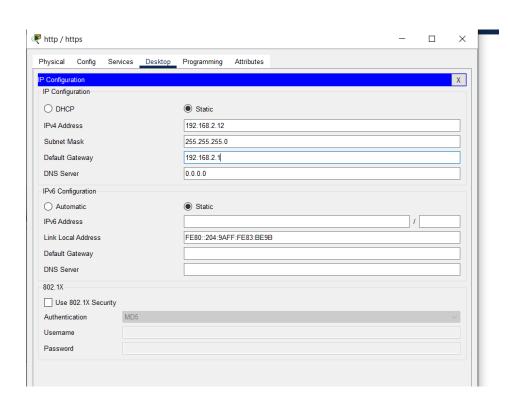


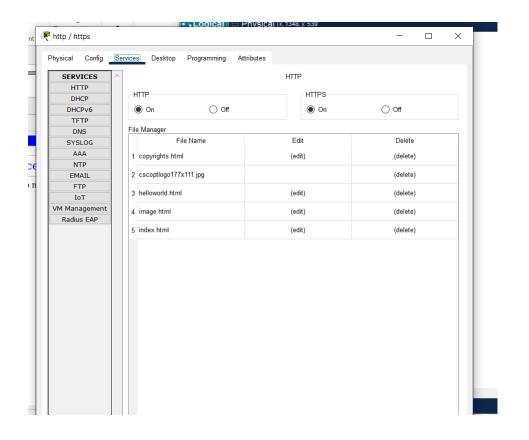


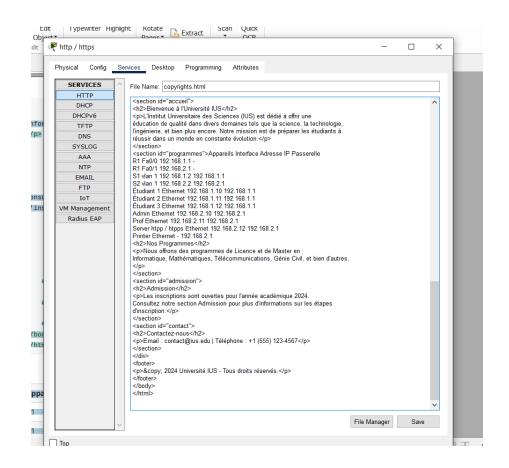




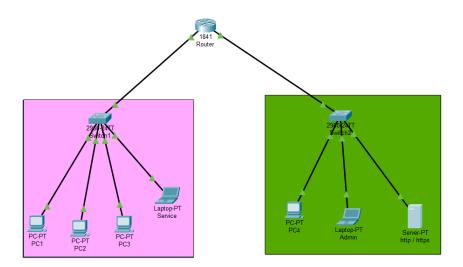






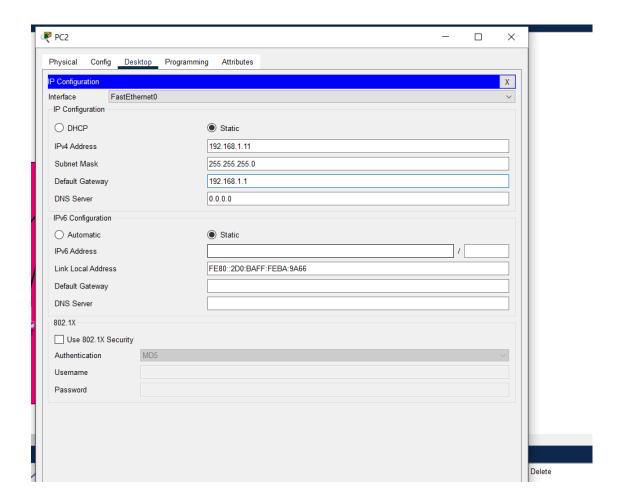


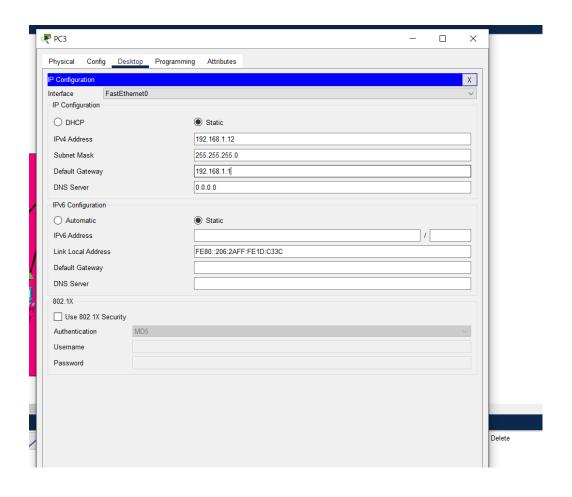


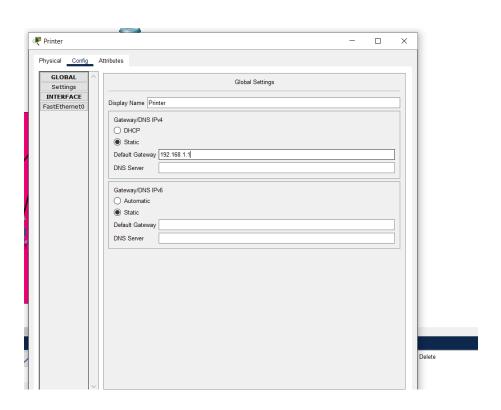


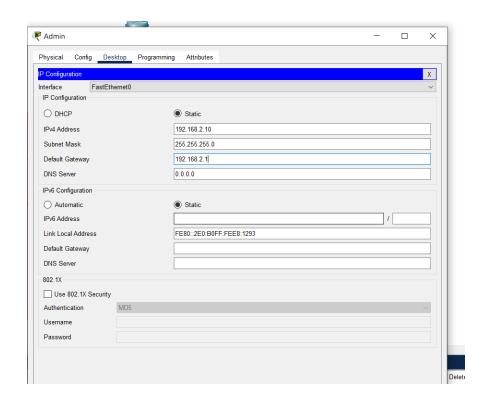
Exécution du TD 3 #1

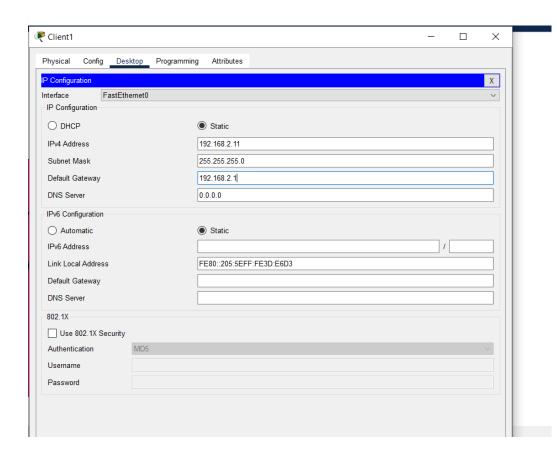
1. Processus sur quelques commandes de base et utilitaires sur Cisco Packet Tracer.

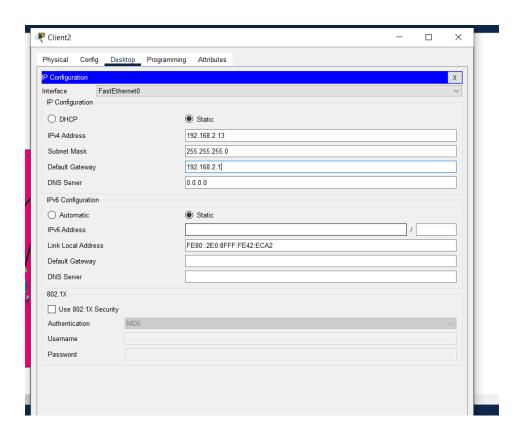


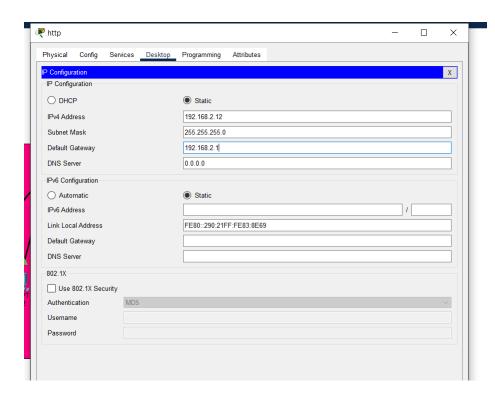


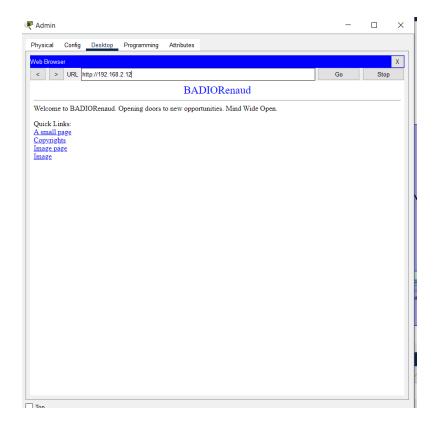


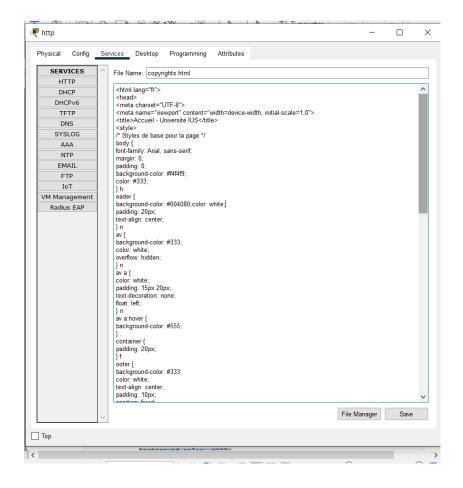




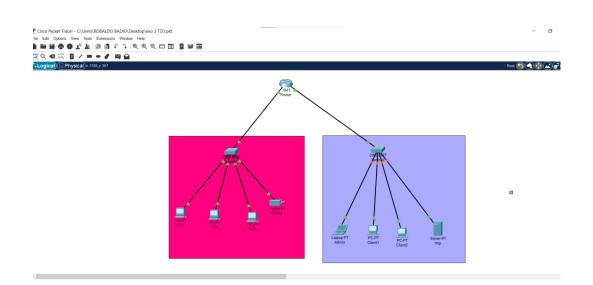


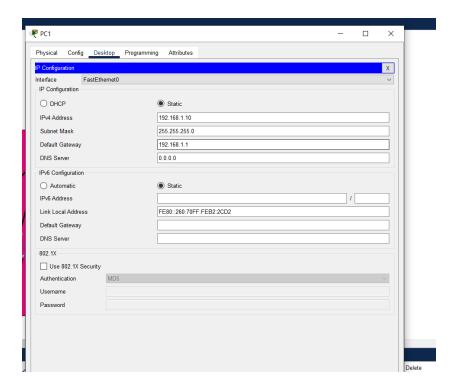












En conclusion, je peux dire que à travers Cisco Packet Tracer j'apprends beaucoup de choses comme faire la configuration d'un rooter, des adresses IP, les commutateurs et tester la connectivité sur quelques commandes de bases. Je peux dire qu'avec la pratique on va apprendre beaucoup plus et maitriser tous les commandes nécessaires.