

ARM AAC Decode Middleware for Linux RCG2ADAACMZ1SL32C

Product Manual

【Preliminary】

RCG2ADAACMZ1SL32E-P01

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ARM AAC Decode Middleware for Linux

1. Overview

1.1. Overview of This Document

This Document is the Product Manual for ARM AAC Decode Middleware for Linux. For function specification, please read related document [1].

1.2. Related Document

Table 1-1 shows the related documents. Table 1-2 shows the processor-related documents.

Table 1-1 List of Related Document

No	Document Name	Description
[1]	AAC Decode Middleware User's Manual	This document shows function specifications.

Table 1-2 List of Processor-related Document

No	Document Name	Title
[2]	WHITE PAPER(Rev.2.0)	big.LITTLE Processing with the Cortex-A15 and Cortex-A7 Processors
[3]	ARM IHI 0042E	Procedure Call Standard for the ARM Architecture (Nov 30, 2012)

2. Product Specification

2.1. Basic Specification

Table 2-1 shows the basic specification of this Middleware.

Table 2-1 Basic Specification

Item	Description
Target CPU	ARM Cortex-A15/A7
Endian	Little Endian
Supported OS	Linux 3.10
Development Environments	gcc version 4.8.3 20140401 (prerelease) (Linaro GCC 4.8-2014.04)

2.2. Memory Requirements

Table 2-2 shows memory requirements of this Middleware.

Table 2-2 Memory Size Requirements

Memory Type	Location	Memory area name		Size (in bytes)	
Instruction	ROM	Instruction area		T.B.D	
Data		Constant table area			
		Other area(Depended on the compiler)			
	Middleware work area		Size	65,896	
	Area breakdown	Static area	breakdown	37,192	
		Scratch area		28,680	
	User work area		Size	10,870	
	Area breakdown	Input buffer	Size breakdown	1,536	
		Output buffer		4,096	
		Structure		630	
	Stack area		2,048		
Other area(Depended on the compiler)		T.B.D			

[Note] Area whose location is shown as ROM in the location column can be included in RAM or ROM.

[Note] Area whose location is shown as RAM in the location column can be included RAM only.

2.3. Performance

Table 2-3 shows processing capability of this Middleware.

Table 2-3 Processing Capability

Item	Description
Average processing load	T.B.D

[Note] The value above varies depending on the frequency, bit rate, and data.

[Note] The value above is measured on R-Car H2 Evaluation Board (ARM Cortex-A15).

[Note] The value above is a reference value which does not indicate a guaranteed level of performance.

2.4. Version information

Table 2-4 shows version information of this Middleware.

Table 2-4 Version Information

Item	Description
Version information	0x00000241 (Version 2.41)

3. List of files

The file list of this Middleware is shown in Table 3-1.

Table 3-1 File list of this Middleware

Directory		Description
RCG2ADAACMZ1SL32C		
document	AACD_UM_J.pdf	User's Manual(Japanese)
	AACD_UM_E.pdf	User's Manual(English)
	RCG2ADAACMZ1SL32J-P01.pdf	Product Manual(Japanese)
	RCG2ADAACMZ1SL32E-P01.pdf	Product Manual(English)
lib	libAACDLA_L.so.2.4	Dynamic link library
	aacd_Lib.h	Header file of library
sample	parser	the sample program of parser
	src	the sample program of application

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		Page	Summary
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