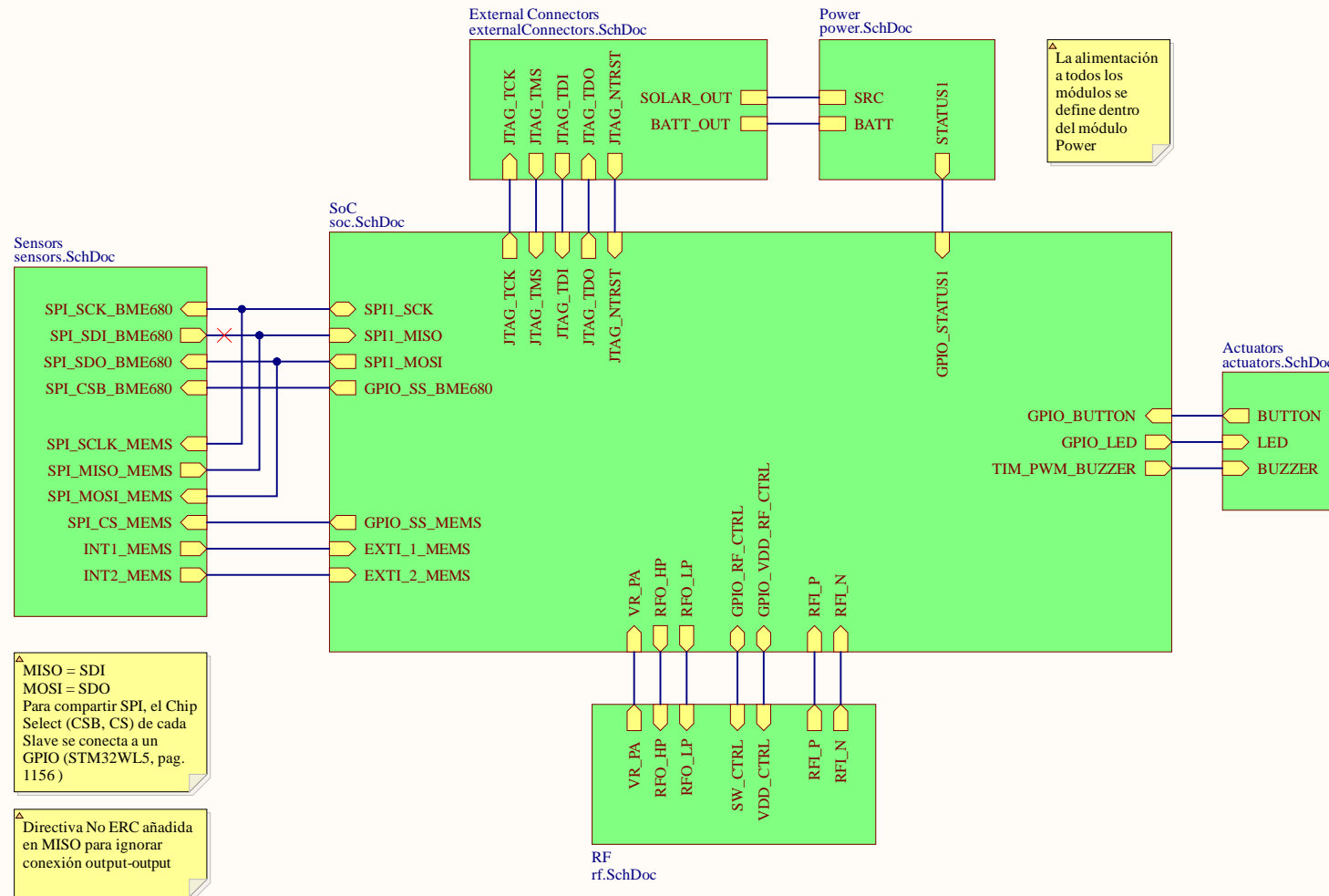

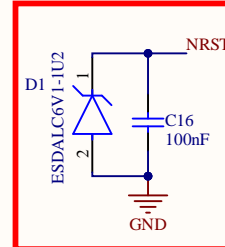
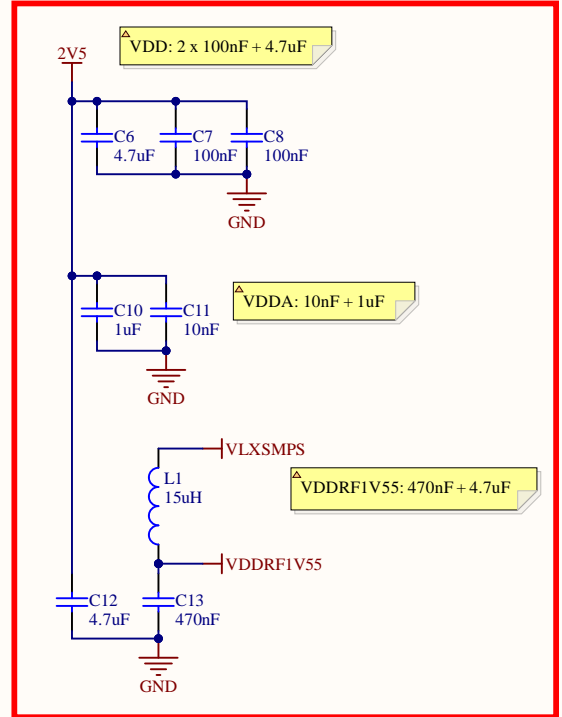
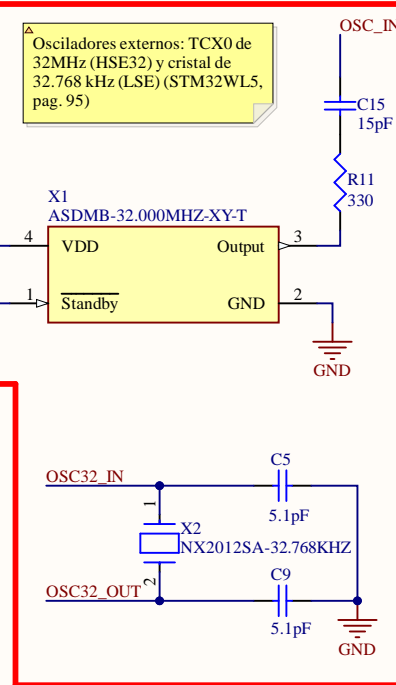
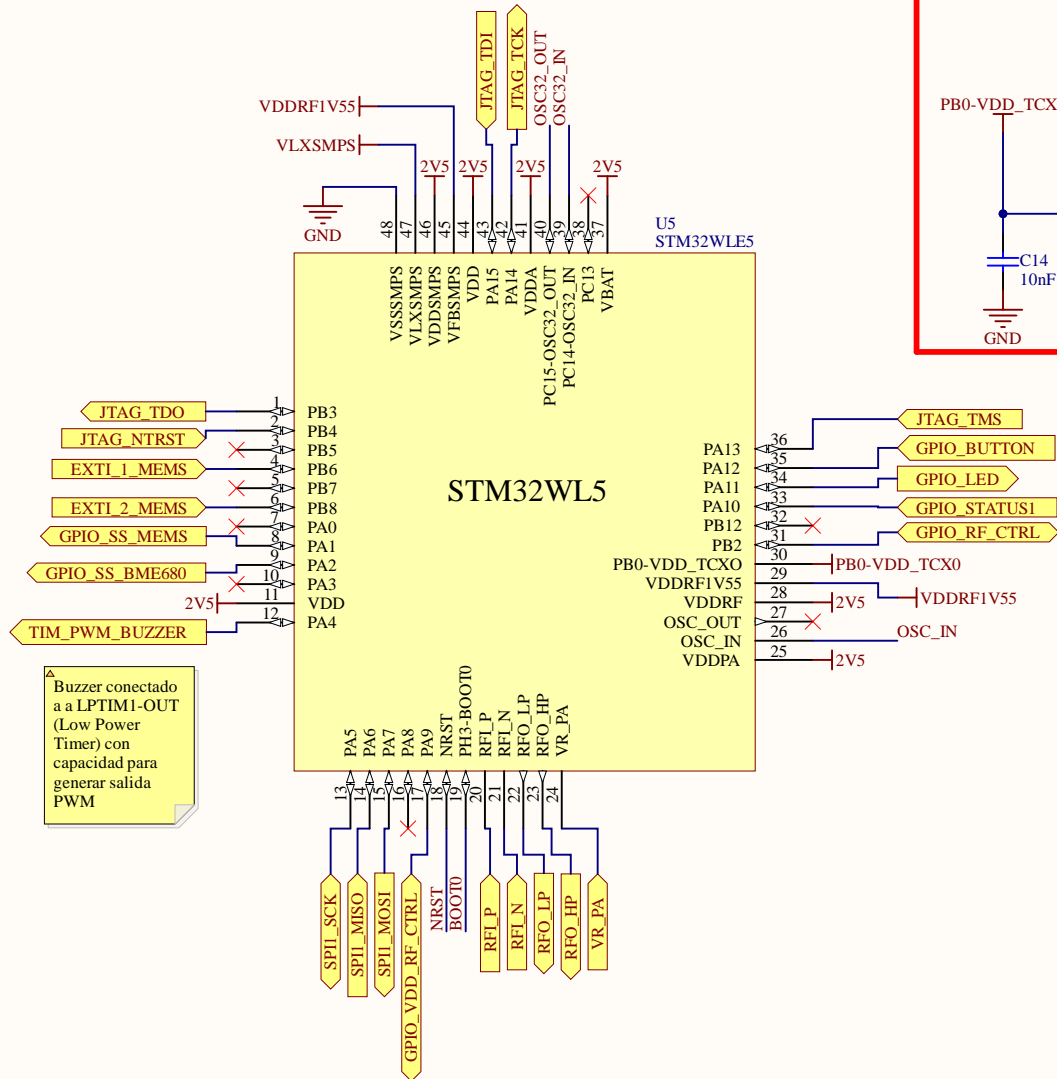


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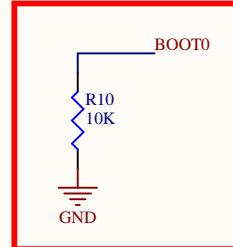


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Drawn By: <b>David, Andrea</b>		Checked By: <b>*</b>		
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Date: <b>26/03/2023</b>	Time: <b>16:47:12</b>	Sheet <b>1</b> of <b>7</b>		
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# SoC



NRST por HW deshabilitado, acondicionamiento del pin con 100nF para evitar resets parásitos (STM32WL5, pag. 113)



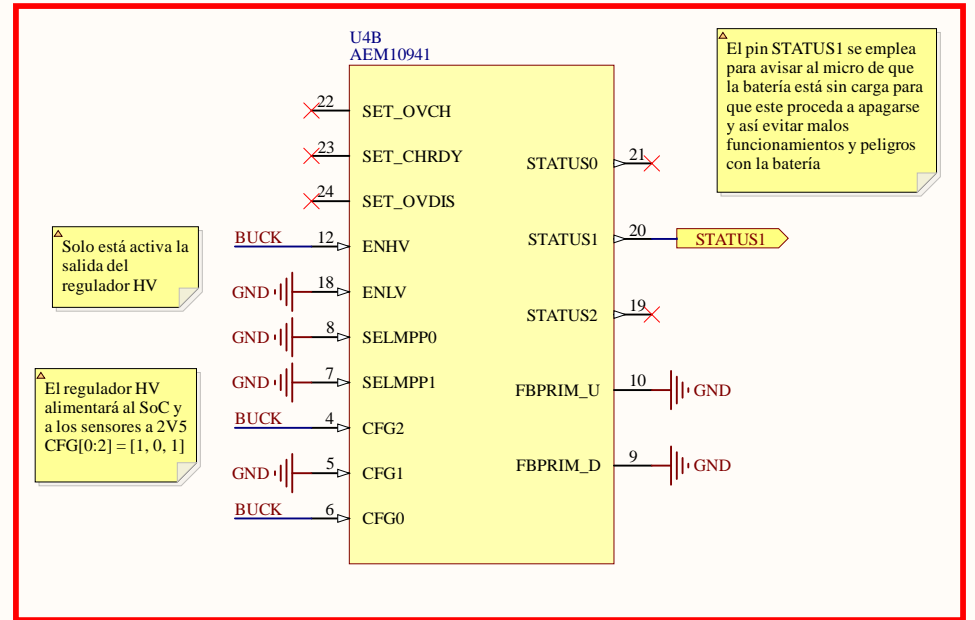
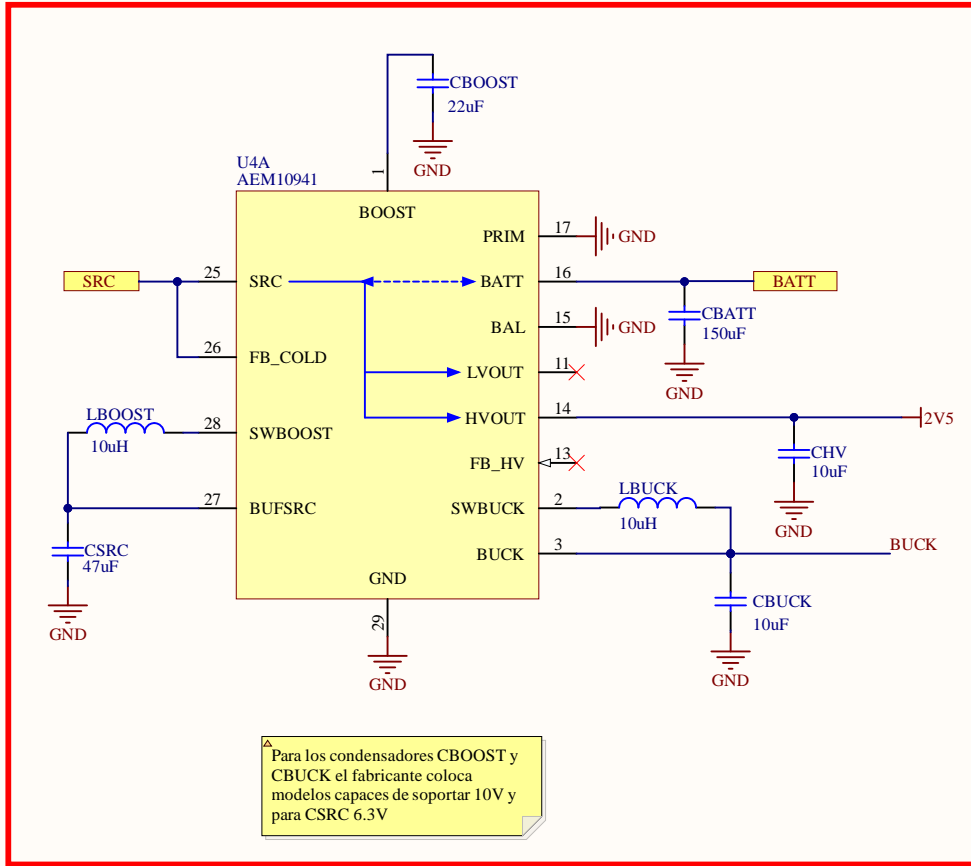
BOOT desde memoria flash (STM32WL5, pag. 60)


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Page Contents: <b>soc.SchDoc</b>			<i>ETSIT</i>	
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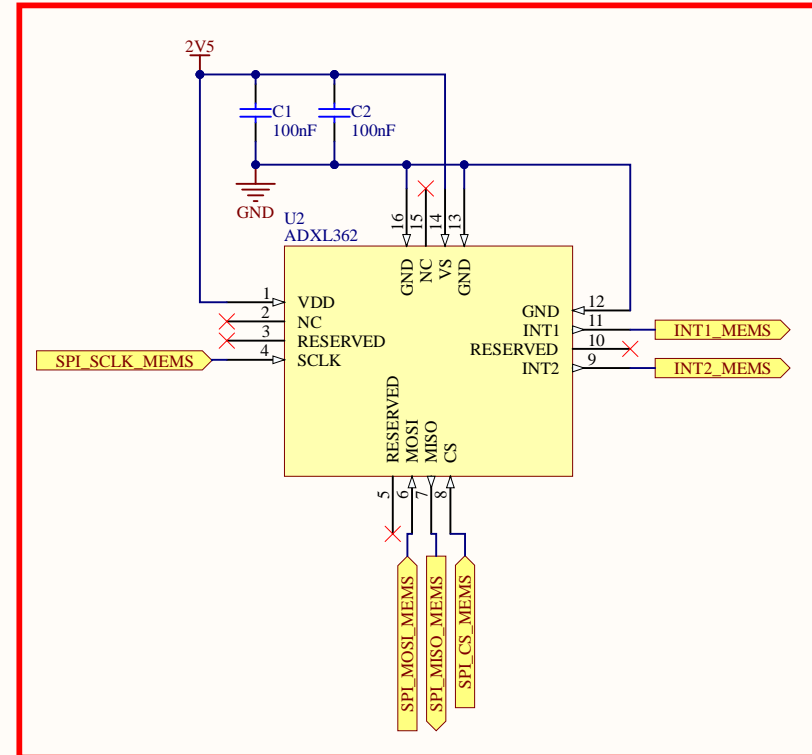
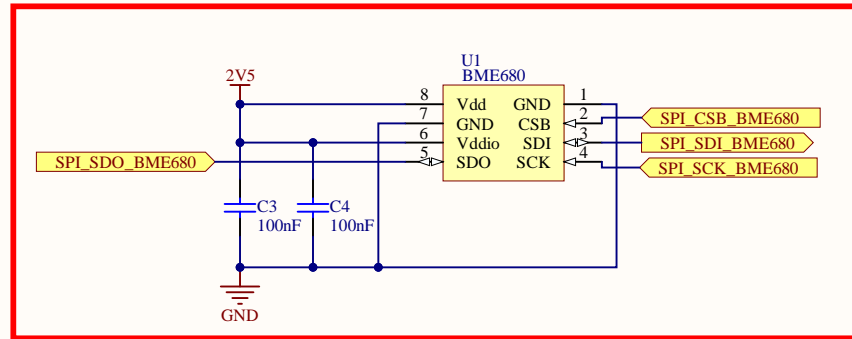
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# Power



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# Sensors

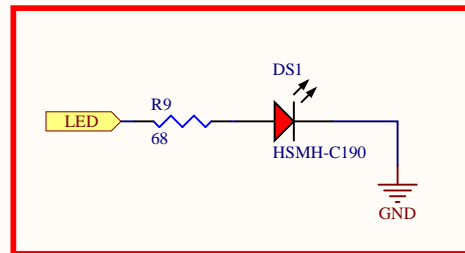
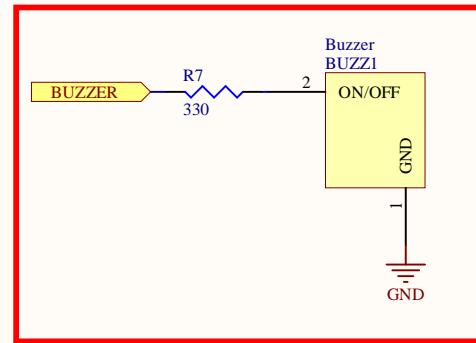
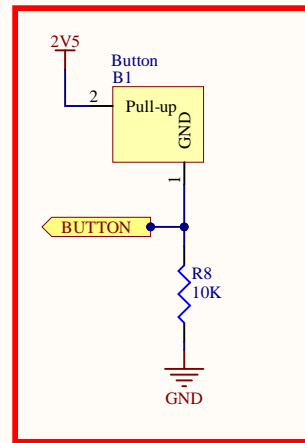



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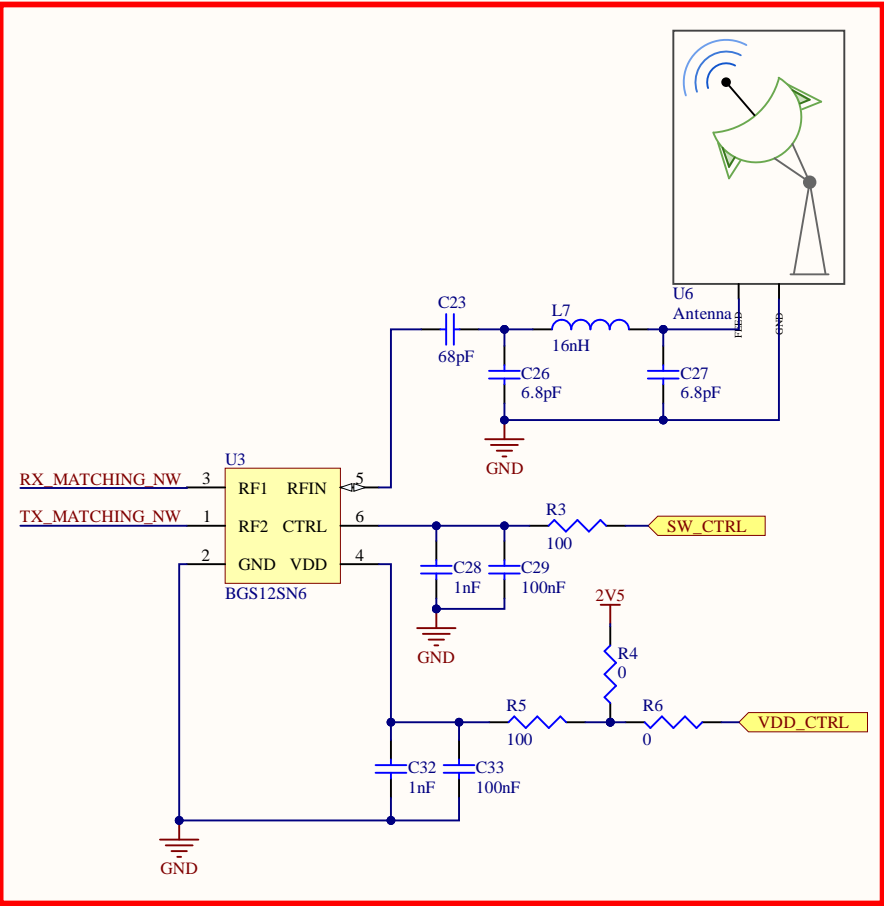
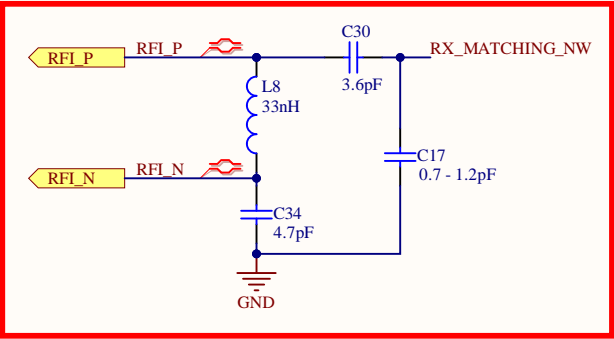
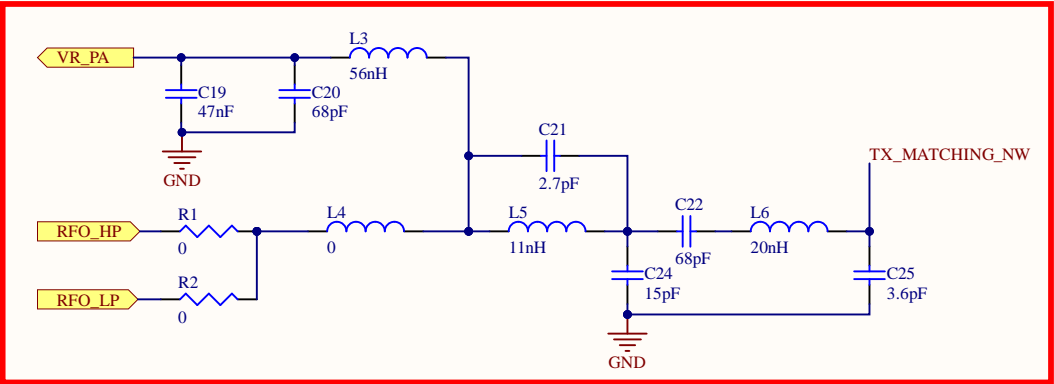
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# Actuators



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# RF



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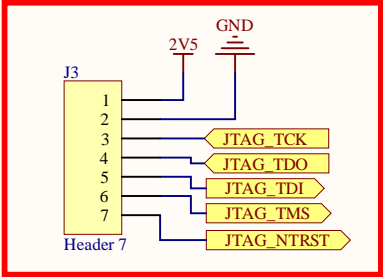
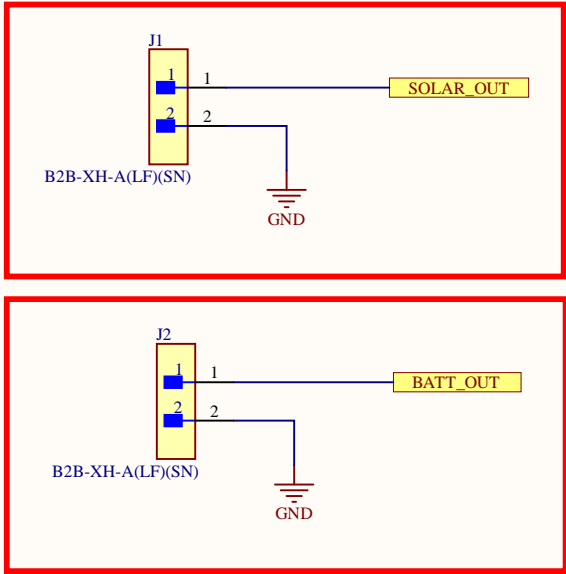
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# External Connectors



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