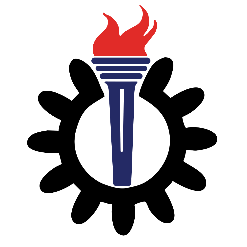


Design of a 32-bit RISC-V Core

Version 0.1

Arvin Delavari – Faraz Ghoreishy





**I U S T**



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