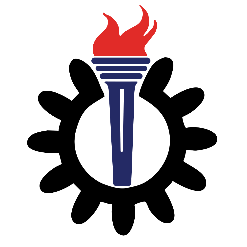


Design of a 32-bit RISC-V Core

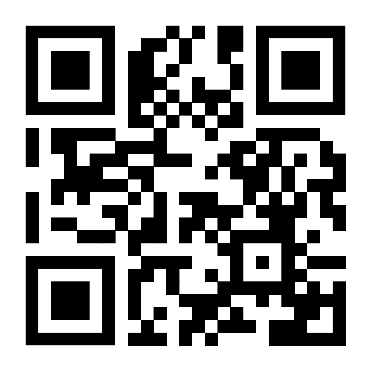
Version 0.1

Arvin Delavari – Faraz Ghoreishy





**I U S T**



GitHub.com/phoeniX-Digital-Design/phoeniX