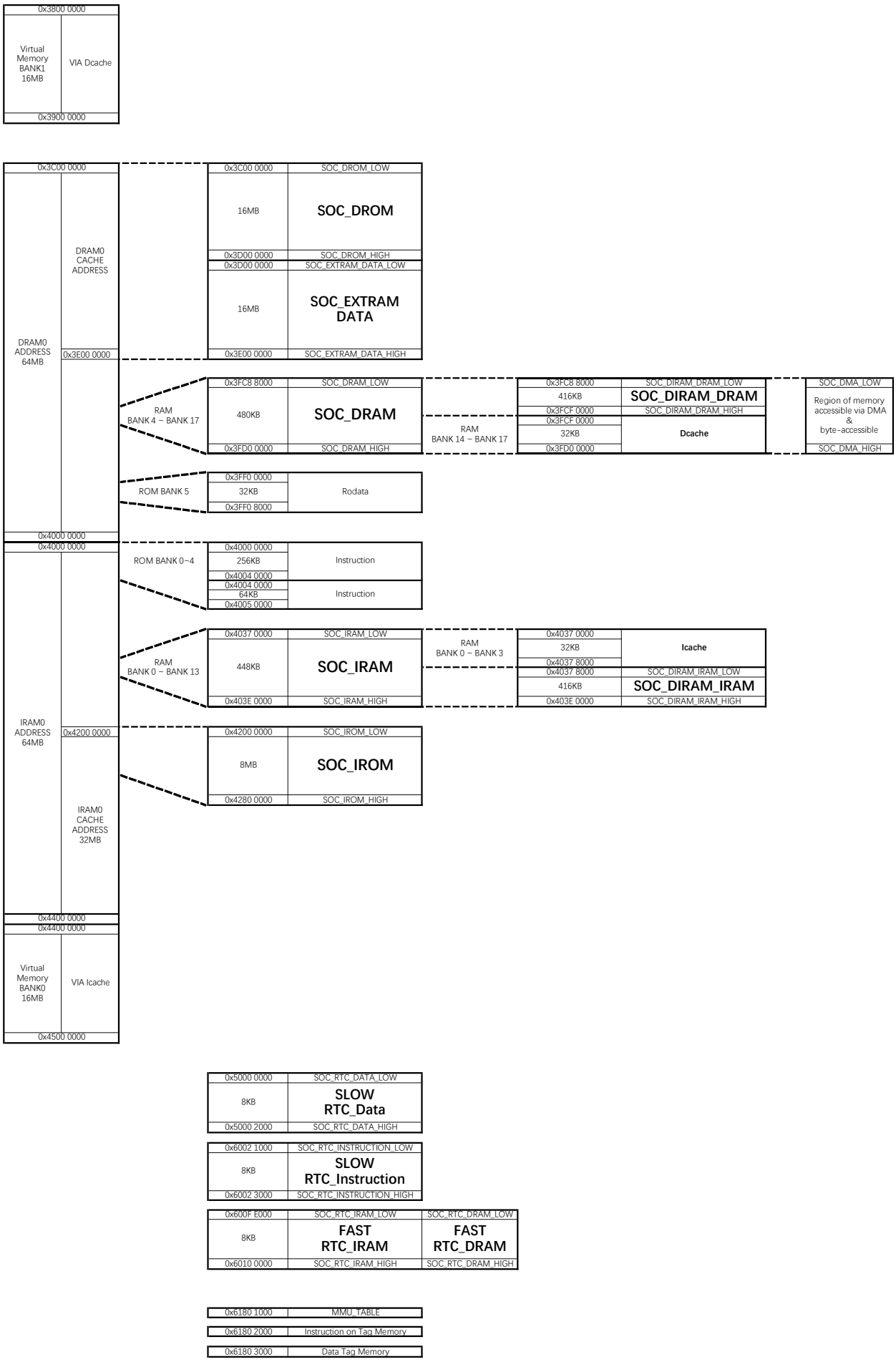
0x3800 0000
Virtual Memory BANK1 16MB
VIA Dcache
0x3900 0000
0x3C00 0000
DRAM0 CACHE ADDRESS
DRAM0 ADDRESS 64MB
0x3E00 0000
0x4000 0000
0x4000 0000
IRAM0 ADDRESS 64MB
0x4200 0000
IRAM0 CACHE ADDRESS 32MB
0x4400 0000
0x4400 0000
Virtual Memory BANK0 16MB
VIA Icache
0x4500 0000
0x3C00 0000
SOC_DROM_LOW
16MB
SOC_DROM
0x3D00 0000
SOC_DROM_HIGH
0x3D00 0000
SOC_EXTRAM_DATA_LOW
16MB
SOC_EXTRAM DATA
0x3E00 0000
SOC_EXTRAM_DATA_HIGH
RAM BANK 4 ~ BANK 17
0x3FC8 8000
SOC_DRAM_LOW
480KB
SOC_DRAM
0x3FD0 0000
SOC_DRAM_HIGH
RAM BANK 14 ~ BANK 17
0x3FC8 8000
SOC_DIRAM_DRAM_LOW
416KB
SOC_DIRAM_DRAM
0x3FCF 0000
SOC_DIRAM_DRAM_HIGH
0x3FCF 0000
32KB
Dcache
0x3FD0 0000
SOC_DMA_LOW
Region of memory accessible via DMA & byte-accessible
SOC_DMA_HIGH
ROM BANK 5
0x3FF0 0000
32KB
0x3FF0 8000
Rodata
ROM BANK 0~4
0x4000 0000
256KB
0x4004 0000
0x4004 0000
64KB
0x4005 0000
Instruction
Instruction
RAM BANK 0 ~ BANK 13
0x4037 0000
SOC_IRAM_LOW
448KB
SOC_IRAM
0x403E 0000
SOC_IRAM_HIGH
RAM BANK 0 ~ BANK 3
0x4037 0000
32KB
0x4037 8000
Icache
0x4037 8000
SOC_DIRAM_IRAM_LOW
416KB
SOC_DIRAM_IRAM
0x403E 0000
SOC_DIRAM_IRAM_HIGH
0x4200 0000
SOC_IROM_LOW
8MB
SOC_IROM
0x4280 0000
SOC_IROM_HIGH
0x5000 0000
SOC_RTC_DATA_LOW
8KB
SLOW RTC_Data
0x5000 2000
SOC_RTC_DATA_HIGH
0x6002 1000
SOC_RTC_INSTRUCTION_LOW
8KB
SLOW RTC_Instruction
0x6002 3000
SOC_RTC_INSTRUCTION_HIGH
0x600F E000
SOC_RTC_IRAM_LOW
SOC_RTC_DRAM_LOW
8KB
FAST RTC_IRAM
FAST RTC_DRAM
0x6010 0000
SOC_RTC_IRAM_HIGH
SOC_RTC_DRAM_HIGH
0x6180 1000
MMU_TABLE
0x6180 2000
Instruction on Tag Memory
0x6180 3000
Data Tag Memory