

The background of the image is a high-magnification, grayscale micrograph of a semiconductor chip. It shows a complex array of circuitry, including various sized rectangular blocks, lines, and patterns, which are typical of a system-on-chip (SoC) or system-on-package (SoP) design. The lighting creates a sense of depth, highlighting the three-dimensional structure of the chip.

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AI-Embedded S<sup>2</sup>oftware-on-Chip (AI-SoC) Lab