

The background of the image is a high-magnification, grayscale micrograph of a semiconductor chip. It shows a complex array of circuitry, including various sized rectangular blocks, lines, and patterns, which are typical of a system-on-chip (SoC) or system-on-package (SoP) design. The lighting creates a sense of depth, highlighting the three-dimensional structure of the chip.

2022 6th Symposium on AI-Embedded System-on-Chip

Kyungpook National University, Daehak-ro 80, No. 724, Daegu, Korea, Oct. 28, 2022

AI-Embedded S²oftware-on-Chip (AI-SoC) Lab