

A detailed, high-magnification grayscale image of a microchip, showing intricate circuit patterns, various sized components, and a central processing area. The image serves as the background for the text overlay.

2024 15th Symposium on AI-Embedded System-on-Chip

Kyungpook National University, Daehak-ro 80, No. 724, Daegu, Korea, Aug. 29, 2024

AI-Embedded S²oftware-on-Chip (AI-SoC) Lab