2-input EX-NOR gate	2-input EX-OR gate	2-input NOR gate	2-input OR gate	2-input NAND gate	2-input AND gate	Inverter (NOT gate)	Buffer	Logic function
Image: Control of the	Image: Control of the	Image: Control of the	Image: Control of the	Image: Control of the	\Box	\$	TOO NI	American (MIL/ANSI) Symbol
<u>"</u>	<u>"</u>	L Ľ	Ľ¥ T	 	*	-	1 OUT	British (BS3939) Symbol
		Height	#	Height		Image: Control of the	Tue out	Common German Symbol
<u>"</u>	<u>"</u>	Ľ\ V	Ľ\ \	 	<u>∞</u>		OUT OUT	International Electrotechnical Commission (IEC) Symbol