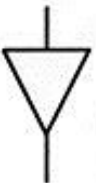
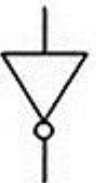
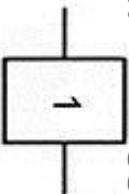
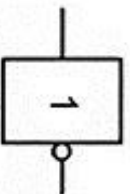






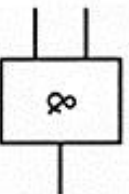
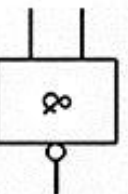






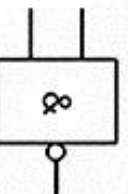
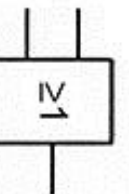






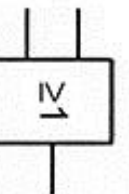
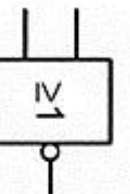






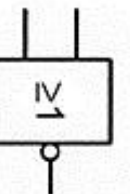







Logic function	American (MIL/ANSI) Symbol		British (BS3939) Symbol		Common German Symbol		International Electrotechnical Commission (IEC) Symbol	
Buffer	IN	OUT	IN	OUT	IN	OUT	IN	OUT
Inverter (NOT gate)								
2-input AND gate								
2-input NAND gate								
2-input OR gate								
2-input NOR gate								
2-input EX-NOR gate	