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.5	Problem Definition	Sahil,Itisha	9/17/21	9/27/21	1	100%		-						+	++	+				++		++	$\pm \pm$		H	+		+	+	++	+	+		H		+	+
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.6	Scope	Sahil, Vedang	9/29/21	9/29/21	1	100%		-						+	+				++	++		++	+					+	_	++	+	_				+	+
7	Existing System/Project	Sahil, Yash, Vedang, Itisha	9/30/21	9/30/21	1	100%			_ '	1 1	' -	+	— L	+	+	_	- 1		+	++	+	+	+	_	1	1 1	$\vdash$	+	$\dashv$	++	+	$\dashv$	1	1 1	1 4	+	+
8	Technology stack	Sahil, Yash, Vedang, Itisha	10/2/21	10/2/21	1	100%		$\rightarrow$	_			$\rightarrow$			+	_			+			+	+	_							+	+			-	_	+
.9	Benefits for environment	Yash,Itisha	10/3/21	10/3/21	1	100%						$\top$		$\pm$	+	_				TT	tt	+	$\top$					$\top$			+					_	†
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	Project Design		_								,									• •	•										•	, i					
.1	Proposed System	Sahil, Yash	10/7/21	10/8/21	1	100%																															Т
.2	Design(Flow Of Modules)	Sahil	10/8/21	10/9/21	1	100%																															Т
.3	Data Flow Diagram	Sahil	10/9/21	10/9/21	1	100%																															Ť
.4	Modules	Sahil, Yash, Vedang, Itisha	10/10/21	10/10/21	1	100%																															Т
.4.1	Module-1	Sahil	10/10/21	10/10/21	1	100%																															Т
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.5	Preparation Of Report	Itisha, Sahil, Vedang	11/15/21	11/25/21	1	100%			_																						П						Т
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.1	Module-1	Sahil, Itisha	11/10/21	11/14/21	0	100%																															Т
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.1	Design of Test Cases	Sahil, Yash, Vedang, Itisha	11/20/21	11/21/21	0	100%																Ш															Т
.2	Testing	Sahil, Yash, Vedang, Itisha	11/21/21	11/22/21	0	100%									$\top$																						Ť
	Results and Analysis																																				
.1	Analysis Of Results	Sahil, Yash, Vedang, Itisha	11/27/21	11/29/21	0	100%																									11						Т
.2	Graphical Representation	Sahil, Yash, Vedang, Itisha	11/30/21	11/30/21	0	100%						$\pm$	$\pm$	++	+					TT		+	$\top$					$\dashv$	_		$\dagger \dagger$	_				1	Ť
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