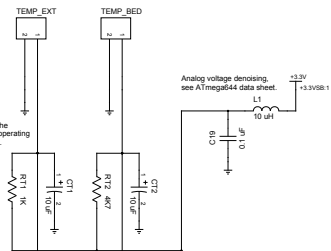
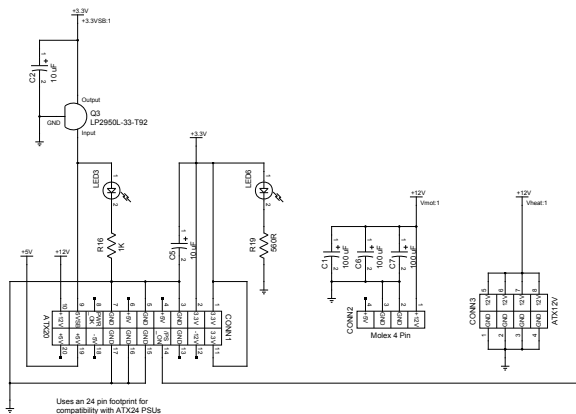
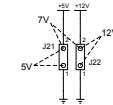
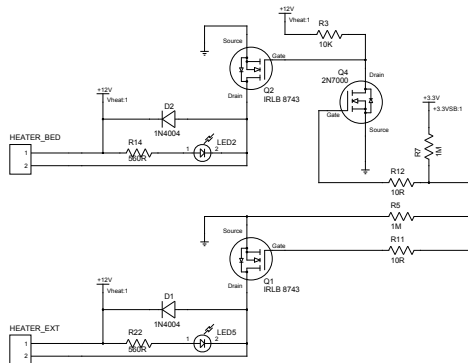
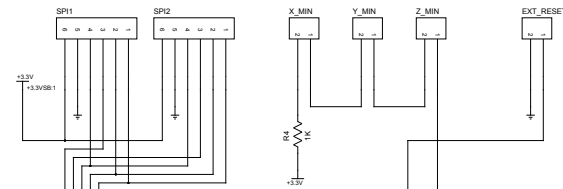
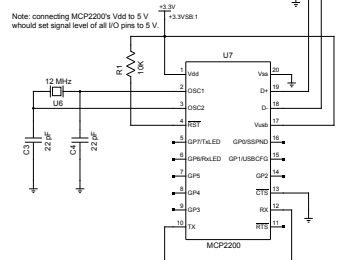


RT1 and RT2 should match the Thermistor's resistance in it's operating point for maximum resolution.

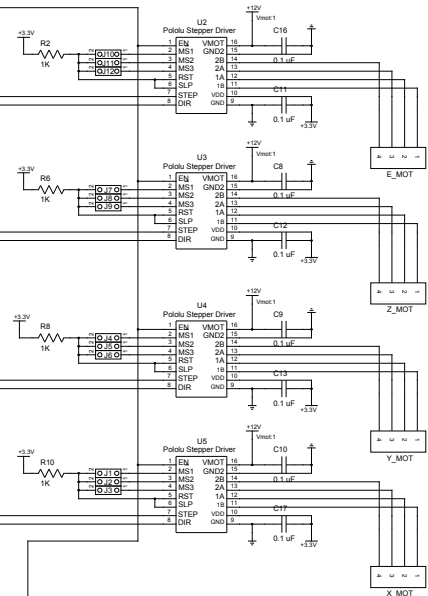


Analog voltage divider, see ATmega644 data sheet.

Note: connecting MCP2200's Vdd to 5 V would set signal level of all I/O pins to 5 V.



Uses an 24 pin footprint for compatibility with ATX24 PSUs



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