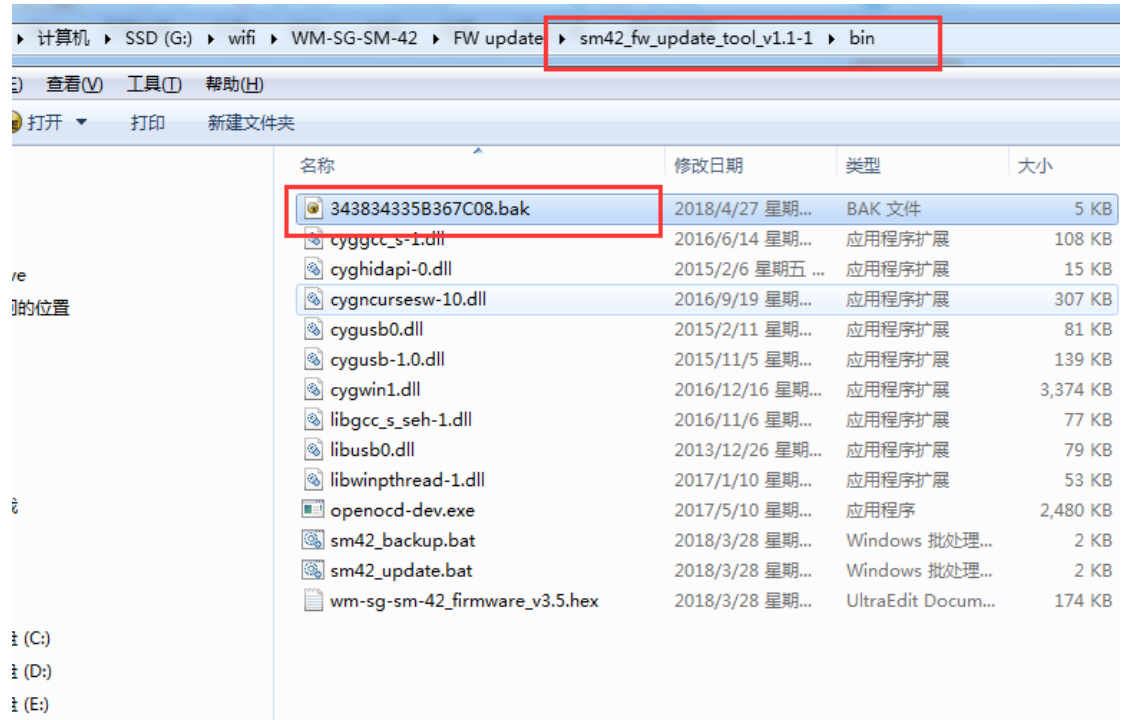1、Connect the EVB to PC through the st-link(swd).
Connect the EVB to PC through the uart.

2、The attachments are placed in the following directory

3、Open the ST-Link Utility to set level 1 and turn off the ST-Link Utility.

4、Open the cmd.exe and exe cmd: sm42_update.bat 3 wm-sg-sm-42_firmware_v3.5.hex
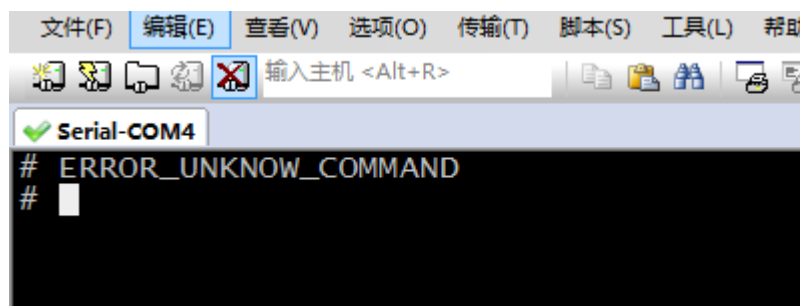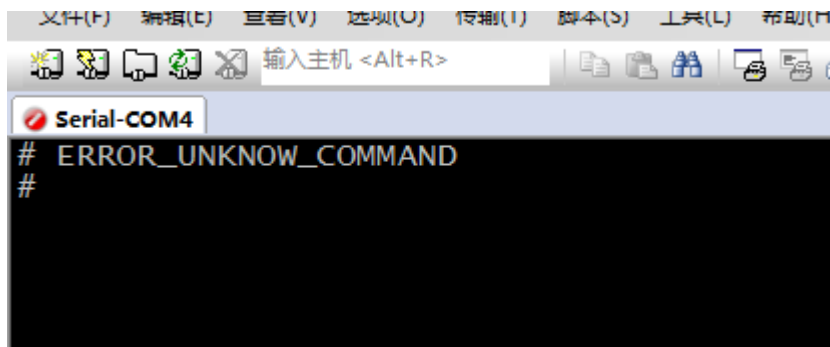
```
G:\wifi\WM-SG-SM-42\FW update\sm42_fw_update_tool_v1.1-1\bin>
G:\wifi\WM-SG-SM-42\FW update\sm42_fw_update_tool_v1.1-1\bin>
G:\wifi\WM-SG-SM-42\FW update\sm42_fw_update_tool_v1.1-1\bin>sm42_update.bat 3 w
m-sg-sm-42_firmware_v3.5.hex
"0) stlink-v1"
"1) stlink_v2"
"2) stlink_v2-1"
select a profile no. from above? 1
going to run using 'stlink_v2'
Open On-Chip Debugger 0.9.0-dev-v1.2 (2017-02-27-12:14)
Licensed under GNU GPL v2
For bug reports, read
        http://openocd.org/doc/doxygen/bugs.html
Info : starting to backup nvm ...
..
Info : target responses abnormal ...
Info : auto-selecting first available session transport "hla_swd". To override u
se 'transport select <transport>'.
adapter speed: 300 kHz
adapter_nsrst_delay: 100
Info : The selected transport took over low-level target control. The results mi
ght differ compared to plain JTAG/SWD
none separate
Info : Unable to match requested speed 300 kHz, using 240 kHz
Info : Unable to match requested speed 300 kHz, using 240 kHz
Info : clock speed 240 kHz
Info : STLINK v2 JTAG v24 API v2 SWIM v4 VID 0x0483 PID 0x3748
Info : using stlink api v2
Info : Target voltage: 3.409948
Info : stm32l0.cpu: hardware has 4 breakpoints, 2 watchpoints
Info : STM32L flash size is 64kb, base address is 0x8000000
Info : found unique device id = 343834335B367C08
Info : atcmd responses abnormal, will use previous nvm backup
Info : backup nvm is valid
Info : backup loaded
```

When you see this information: "then please hit ENTER key while ready to continue ....", Disconnect the EVB power supply to remove st-link and then re power the EVB.

open the Serial port software to try AT cmd and close the Serial port software.

5、Back to the cmd and hit the Enter