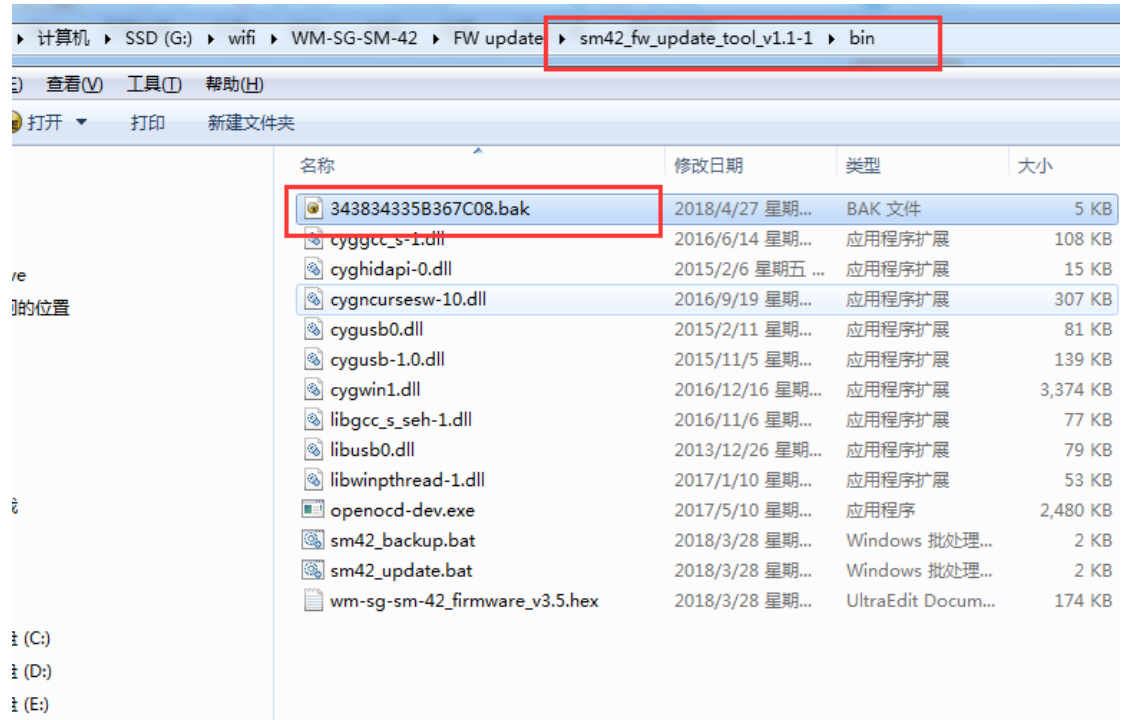


- 1、Connect the EVB to PC through the st-link(swd).
Connect the EVB to PC through the uart.
- 2、The attachments are placed in the following directory



- 3、Open the ST-Link Utility to set level 1 and turn off the ST-Link Utility.

Option Bytes

Read Out Protection
 Level 1

BOR Level
 OFF

User configuration option byte

| | | |
|--|---|--|
| <input checked="" type="checkbox"/> WDG_SW | <input type="checkbox"/> IWDG_STDBY | <input type="checkbox"/> nBoot0 |
| <input type="checkbox"/> IWDG_STOP | <input type="checkbox"/> IWDG_ULP | <input type="checkbox"/> nBOOT0 |
| <input type="checkbox"/> WWDG_SW | <input type="checkbox"/> FZ_IWDG_STOP | <input checked="" type="checkbox"/> nBoot1 |
| <input type="checkbox"/> nSRAM_Parity | <input type="checkbox"/> FZ_IWDG_STDBY | <input type="checkbox"/> BOOT1 |
| <input type="checkbox"/> SRAM2_RST | <input type="checkbox"/> PCROP_RDP | <input type="checkbox"/> nDBOOT |
| <input type="checkbox"/> SRAM2_PE | <input type="checkbox"/> nBoot0_SW_Cfg | <input type="checkbox"/> nBOOT_SEL |
| <input type="checkbox"/> nRST_SHDW | <input type="checkbox"/> BOOT0 nSW Config | <input type="checkbox"/> nBFB2 |
| <input checked="" type="checkbox"/> nRST_STOP | <input type="checkbox"/> VDDA_Monitor | <input type="checkbox"/> DUALBANK |
| <input checked="" type="checkbox"/> nRST_STDBY | | <input type="checkbox"/> nDBANK |
| <input type="checkbox"/> SDADC12_VDD_Monitor | | <input type="checkbox"/> DB1M |

Boot address option bytes

| | | | |
|---------------|--|---------------|--|
| BOOT_ADD0 (H) | | Boot from (H) | |
| BOOT_ADD1 (H) | | Boot from (H) | |

User data storage option bytes

| | | | |
|------------|--|------------|--|
| Data 0 (H) | | Data 1 (H) | |
|------------|--|------------|--|

Flash sectors protection

Flash protection mode: Read/Write protection

| Page | Start address | Size | Protection |
|---------------------------------|---------------|---------|---------------|
| <input type="checkbox"/> Page 0 | 0x08000000 | 0.12... | No Protection |
| <input type="checkbox"/> Page 1 | 0x08000080 | 0.12... | No Protection |
| <input type="checkbox"/> Page 2 | 0x08000100 | 0.12... | No Protection |
| <input type="checkbox"/> Page 3 | 0x08000180 | 0.12... | No Protection |
| <input type="checkbox"/> Page 4 | 0x08000200 | 0.12... | No Protection |
| <input type="checkbox"/> Page 5 | 0x08000280 | 0.12... | No Protection |
| <input type="checkbox"/> Page 6 | 0x08000300 | 0.12... | No Protection |
| <input type="checkbox"/> Page 7 | 0x08000380 | 0.12... | No Protection |
| <input type="checkbox"/> Page 8 | 0x08000400 | 0.12... | No Protection |

Unselect all Select all

Apply Cancel

4、Open the cmd.exe and exe cmd: sm42_update.bat 3 wm-sg-sm-42_firmware_v3.5.hex

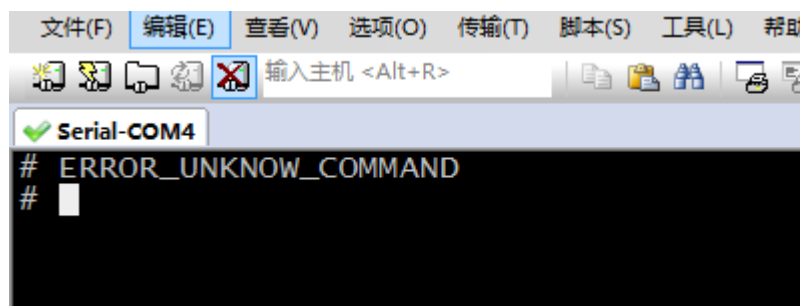
```

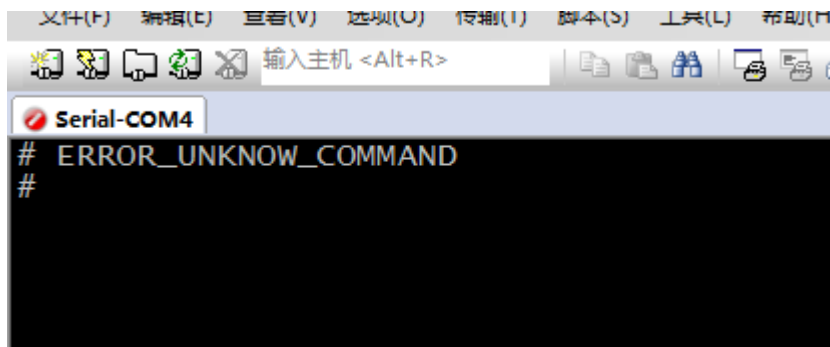
G:\wifi\WM-SG-SM-42\FW update\sm42_fw_update_tool_v1.1-1\bin>
G:\wifi\WM-SG-SM-42\FW update\sm42_fw_update_tool_v1.1-1\bin>
G:\wifi\WM-SG-SM-42\FW update\sm42_fw_update_tool_v1.1-1\bin>sm42_update.bat 3 w
m-sg-sm-42_firmware_v3.5.hex
"0> stlink_v1"
"1> stlink_v2"
"2> stlink_v2-1"
select a profile no. from above? 1
going to run using 'stlink_v2'
Open On-Chip Debugger 0.9.0-dev-v1.2 (2017-02-27-12:14)
Licensed under GNU GPL v2
For bug reports, read
    http://openocd.org/doc/doxygen/bugs.html
Info : starting to backup nvram ...
..
Info : target responses abnormal ...
Info : auto-selecting first available session transport "hla_swd". To override use
'se 'transport select <transport>'.
adapter speed: 300 kHz
adapter_nsrst_delay: 100
Info : The selected transport took over low-level target control. The results mi
ght differ compared to plain JTAG/SWD
none separate
Info : Unable to match requested speed 300 kHz, using 240 kHz
Info : Unable to match requested speed 300 kHz, using 240 kHz
Info : clock speed 240 kHz
Info : STLINK v2 JTAG v24 API v2 SWIM v4 VID 0x0483 PID 0x3748
Info : using stlink api v2
Info : Target voltage: 3.409948
Info : stm32l0.cpu: hardware has 4 breakpoints, 2 watchpoints
Info : STM32L flash size is 64kb, base address is 0x8000000
Info : found unique device id = 343834335B367C08
Info : atcmd responses abnormal, will use previous nvram backup
Info : backup nvram is valid
Info : backup loaded

```

When you see this information: "then please hit ENTER key while ready to continue ...", Disconnect the EVB power supply to remove st-link and then re power the EVB.

open the Serial port software to try AT cmd and close the Serial port software.





5、Back to the cmd and hit the Enter

```
adapter speed: 240 KHz
shutdown command invoked
Info : backup nvram is valid

NOTE!! nvram must be restored ...
DO NOT TERMINATE the process or the window.
to ensure target is in running state, please unplug SWD wire and REPOWER target
then please hit ENTER key while ready to continue ....

Info : restoring nvram ...
.....
.....
.....
.....
fw updated successfully!please takes a reset for effect the new fw.
G:\wifi\WM-SG-SM-42\FW update\sm42_fw_update_tool_v1.1-1\bin>
```