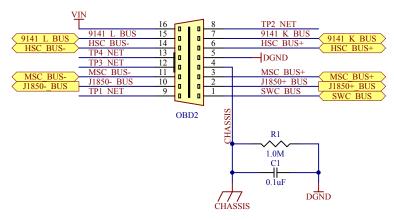
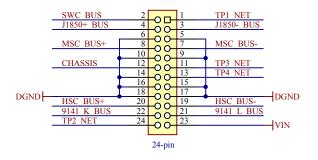


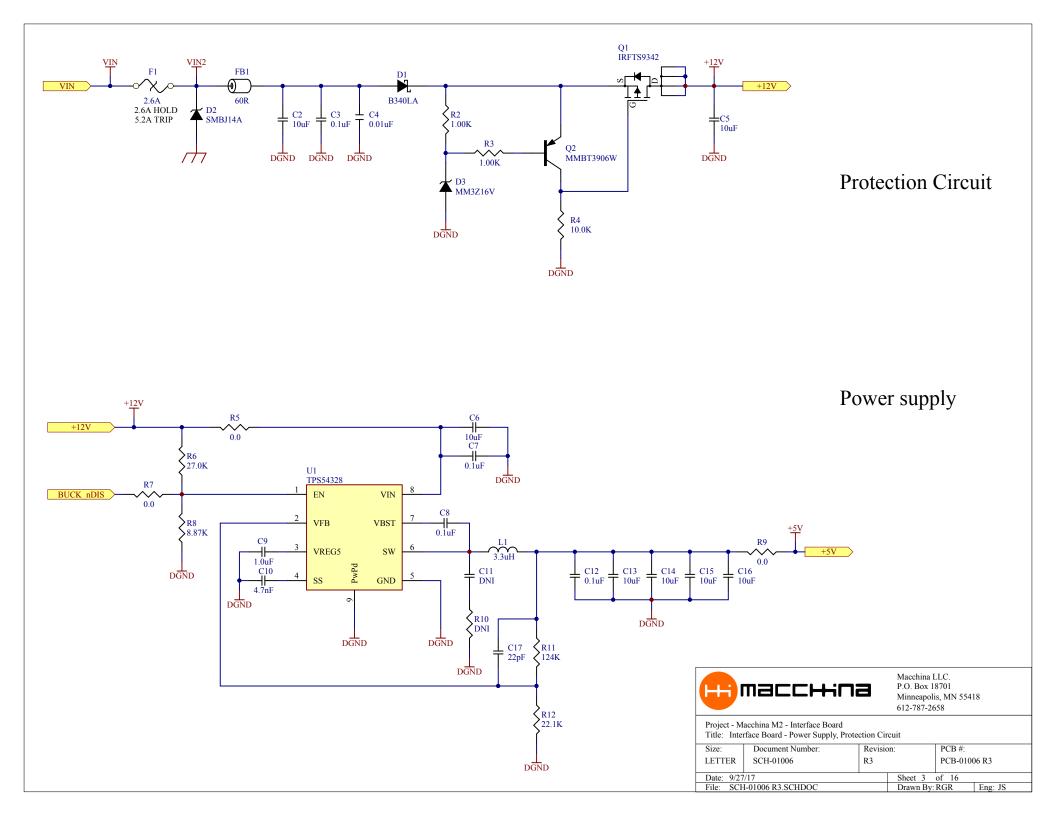
J1 (Under-the-Dash)

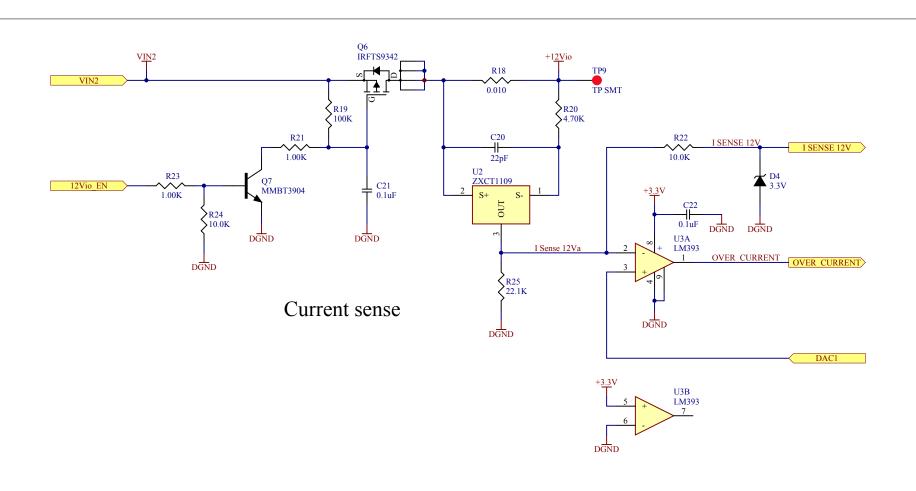


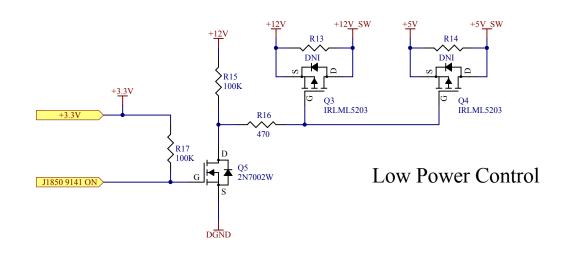
J2 (Under-the-Hood)













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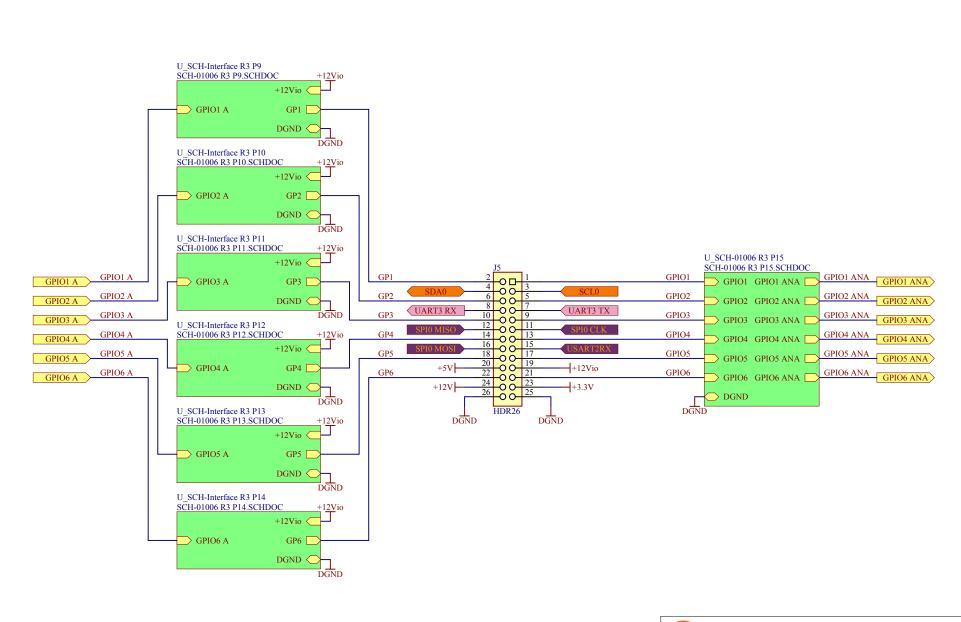
Project - Macchina M2 - Interface Board
Title: Interface Board - Low Power Control, Current Sense

 Size:
 Document Number:
 Revision:
 PCB #:

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 SCH-01006
 R3
 PCB-01006 R3

 Date:
 9/27/17
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 File:
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 Drawn By: RGR
 Eng: JS





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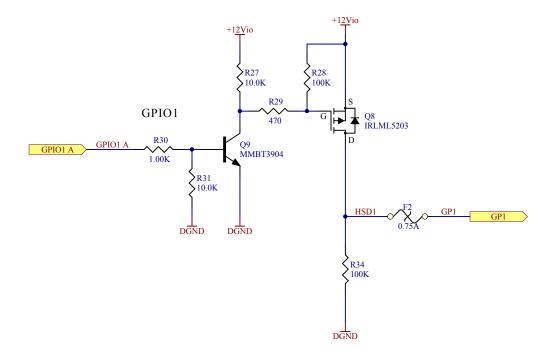
Project - Macchina M2 - Interface Board Title: Interface Board - 12V IO Circuit Driver Overview

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12V IO Channel 1 (High Side)

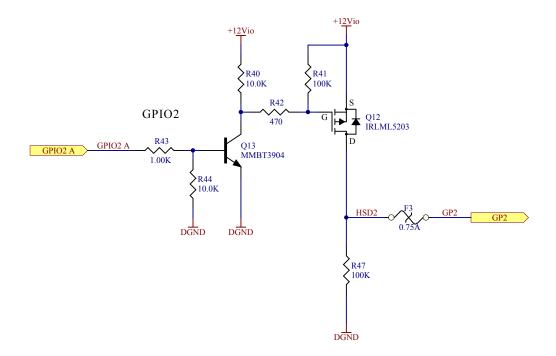




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12V IO Channel 2 (High Side)

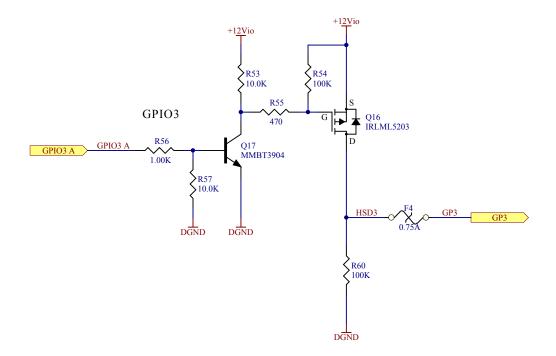




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Date: 9/27	7/17			Sheet 7	of 16	
File: SCF	I-01006 R3.SCHDOC			Drawn By:	RGR	Eng: JS

12V IO Channel 3 (High Side)

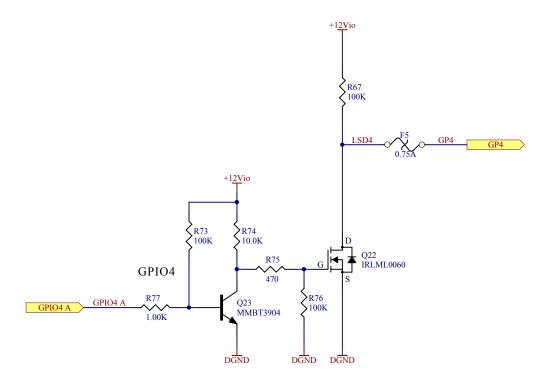




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File: SCF	I-01006 R3.SCHDOC		Drawn By:	RGR	Eng: JS

12V IO Channel 4 (Low Side)

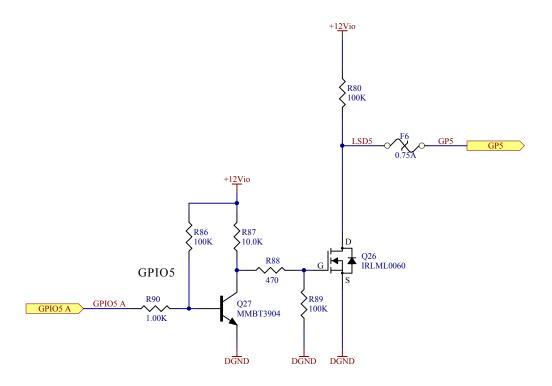




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12V IO Channel 5 (Low Side)

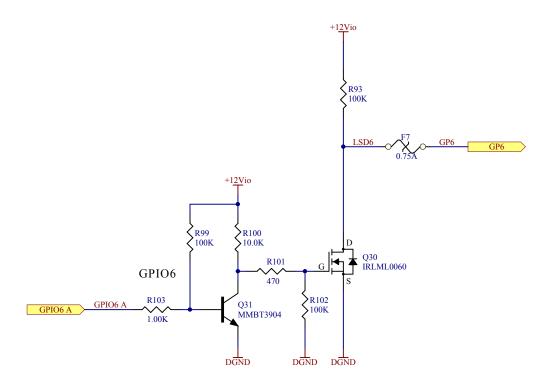




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File: SCI	I-01006 R3.SCHDOC		Drawn By:	RGR	Eng: JS

12V IO Channel 6 (Low Side)

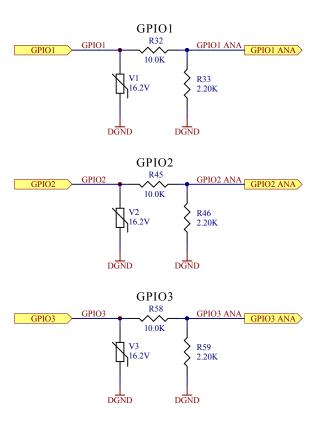


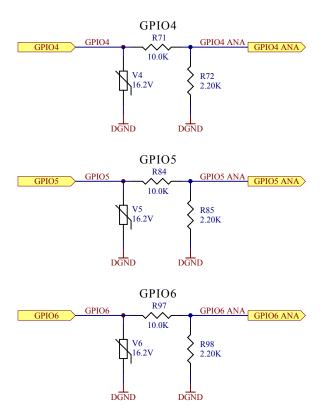


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Analog Inputs







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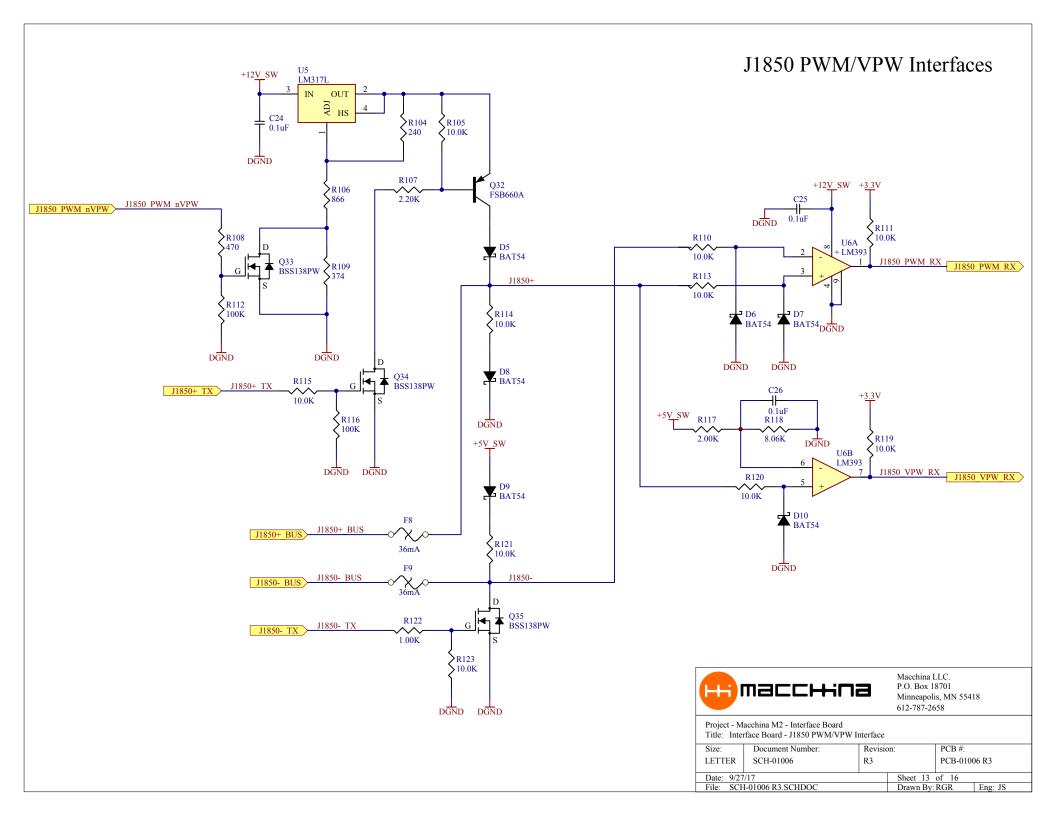
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Project - Macchina M2 - Interface Board Title: Interface Board - Analog Inputs

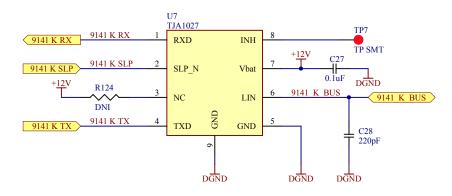
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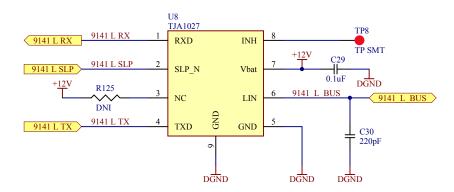
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ISO 9141 / LIN Interface





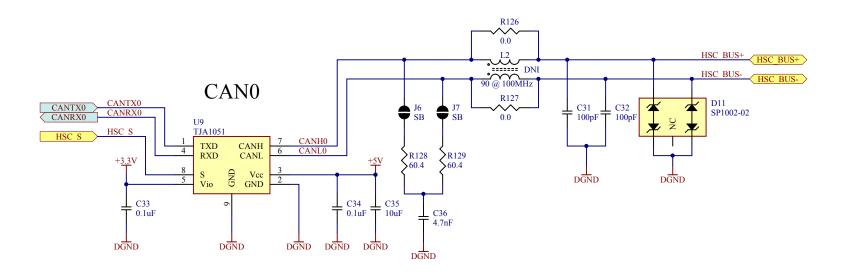


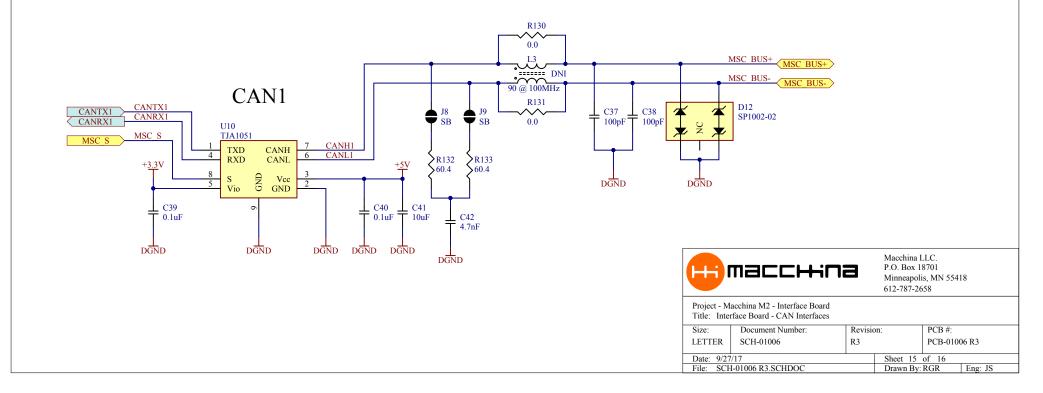
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Project - Macchina M2 - Interface Board Title: Interface Board - ISO9141/LIN Interfaces

Title: Interface Board - ISO9141/LIN Interfaces						
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File: SCF	I-01006 R3.SCHDOC		Drawn By:	RGR	Eng: JS	

CAN Interfaces





Single Wire CAN

