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Eng: JS

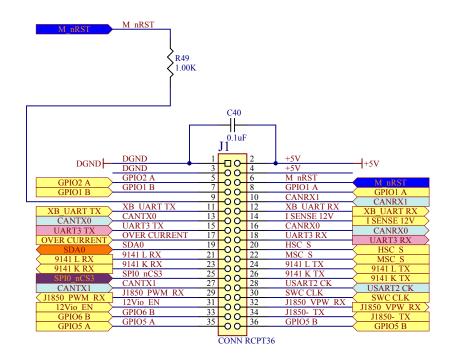
Project - Macchina M2 - Processor Board Title: Processor Board - Overview

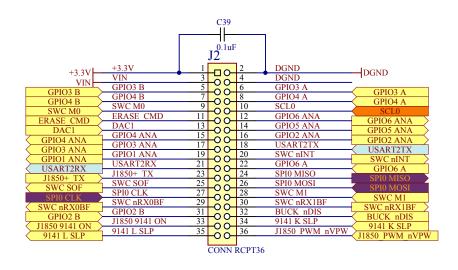
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 Document Number:
 Revision:
 PCB #:

 LETTER
 SCH-01005
 R3
 PCB-01005 R3

 Date:
 9/27/17
 Sheet 1 of 6

 File:
 SCH-01005 R3.SCHDOC
 Drawn Bv. RGR







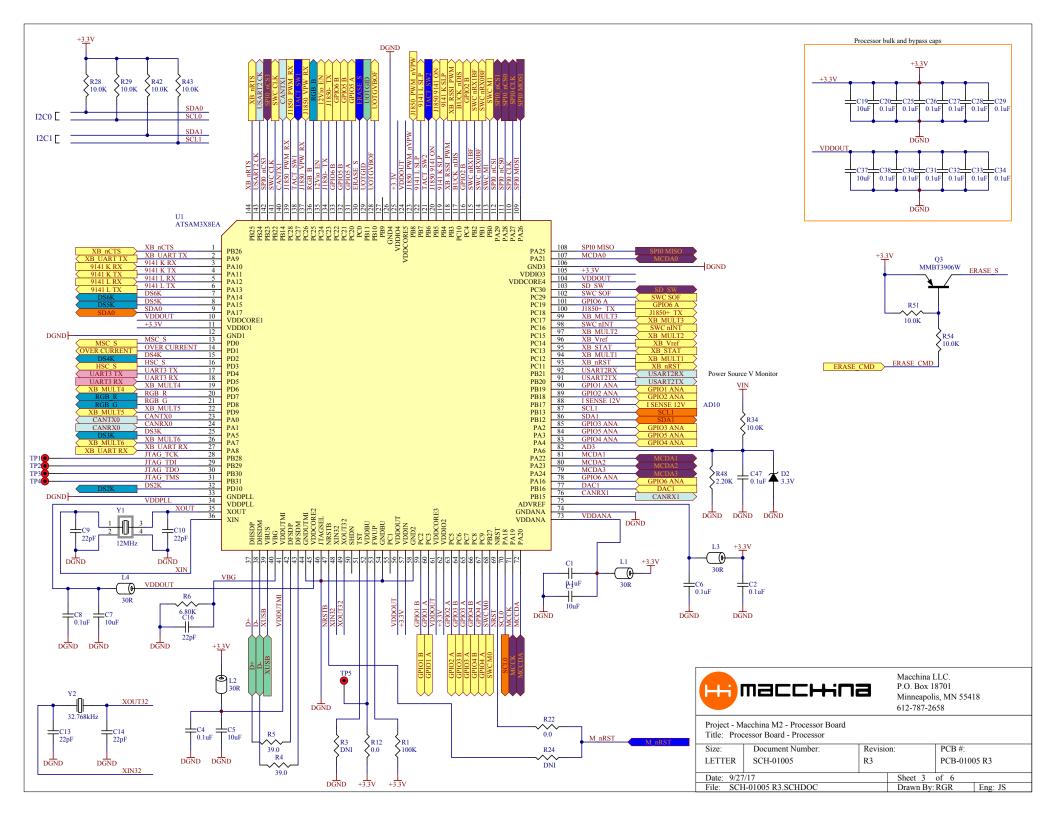
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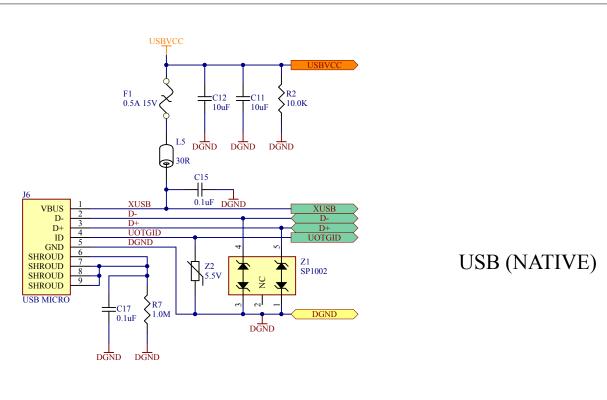
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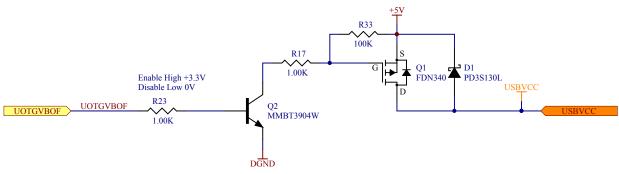
Project - Macchina M2 - Processor Board

Title: Processor Board - Bridge Connectors to Interface Board

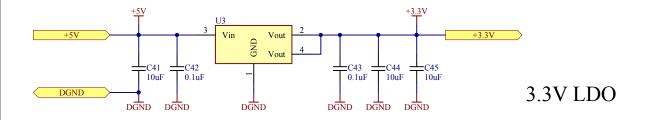
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LETTER	SCH-01005	R3		PCB-01	005 R3
Date: 9/27	7/17		Sheet 2	of 6	
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OTG ENABLE





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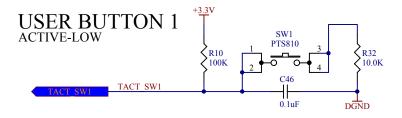
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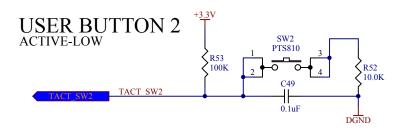
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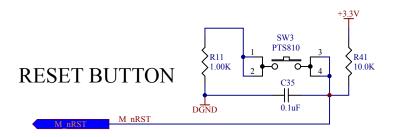
Project - Macchina M2 - Processor Board Title: Processor Board - USB, OTG and LDO Circuits

Size: Document Number: Revision LETTER R3 SCH-01005

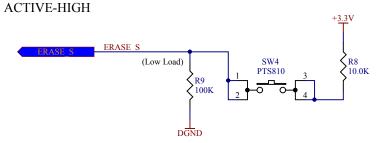
PCB-01005 R3 Sheet 4 of 6 Drawn By: RGR Date: 9/27/17 File: SCH-01005 R3.SCHDOC

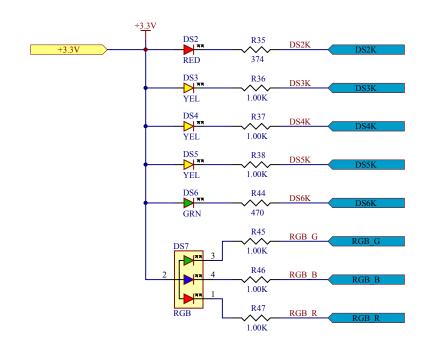






ERASE BUTTON







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Project - Macchina M2 - Processor Board Title: Processor Board - Switches, LEDs

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LETTER	SCH-01005	R3	PCB-01005 R3

