



Fixed pins: GSCLK (PB0), SIN (PD1), SCLK (PD4)

Remappable pins: BLANK (PD6), XLAT (PD5), DCPRG (PC3), VPRG (PC5)

Optional pins (see documentation): PC3 (DCPRG), PC5 (VPRG)

Pins marked "remappable" in the above list may be remapped to unused pins, or swapped with other remappable pins in the Makefile configuration.

Makefile configuration:

```
TLC5940_N = 1
TLC5940_ENABLE_MULTIPLEXING = 0
TLC5940_SPI_MODE = 1
BLANK_PIN = PD6
XLAT_PIN = PD5
DCPRG_PIN = PC3
VPRG_PIN = PC5
```

This SPI mode is by far the fastest, and should be preferred over the others.

For optimal performance, BLANK_PIN and XLAT_PIN should belong to the same PORT.

R1 is optional, but protects against accidental resets due to noise.

D1 is optional, but provides ESD protection for the RESET pin.

C2 is optional, but provides additional noise protection for the RESET pin. (If debugWire or PDI is being used, C2 cannot be present.)

Q1, C3, and C4 are optional if the fuse bits are changed appropriately to use the internal oscillator.