

D1 is optional, but provides ESD protection for the RESET pin.

C2 is optional, but provides additional noise protection for the RESET pin. (If debugWire or PDI is being used, C2 cannot be present.)

Q1, C3, and C4 are optional if the fuse bits are changed appropriately to use the internal oscillator

The IRLML9303 is a much better choice for Q2, Q3, and Q4, but it is only available in a surface