. Larva Pradip



Aim -

Theory -

To display the contents of flag registor push f pop instruction. Each bit of flag registers is then masked off with I foll o's live 1000 0000 0000 0000 (16 bit) 8000 n) & based on the regult of making either 0(30h) or, (3,h) is get displayed on the screen. Each bit of the about 16 bit no. gets shifted in right - direction by I position before mosting to obtain the next bit position of flag register. This whole provedre gets repeated 16 times.

Algorithm -

5tep 1 - Stoot

step 0 - Initialize data segment through Ax register in the DS register.

Step 3 - Display the flug bit names as "xxxx ODIT SF 2F X AF XPEX (FI)

Push the contents of flag registers to the stock.

step 5 - Pop the contents of stack to register to any 16-bit register.

5tep 6 - Move the contents of Bx to temporary



Step 7- Move the 8000h no. to Ax. Step8 - Move the count as 16 to cx register. Step 9 - Move the contents of temporery variable t to BX. Step 10 - AND the contents of BX & AX. Step 11 - If zero flag is set then go to the step no. 14 otherwise goto step no. 12 Step 12 - Move the 31 h to DL register Step 13 - Make the unconditional jump to a step Step 14 - Move the 30h to DL register Step 15 - Preserve the nnumber from Ax in + temporary variable. 5tep 16 - Display the contents of DL registers. Step 17 - Move the contents of H & to AX register backo Step 18 - Rotate the contents of Ax by I position in right direction. Step 19 - Repeat step no. 5 to 17 till count CX reaches to '0'. 5tep 20 - 5top