

TensorFlow-ZenDNN Plug-in User Guide

Publication # CSG-1

Revision # 0.2

Issue Date October 2023

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Contents

Revision His	story	
Chapter 1	Installing TensorFlow-ZenDNN Plug-in	
1.1	TensorFlow-ZenDNN Plug-in Setup	
1.	.1.1 Install with Binary Release	
1.	.1.2 Build and Install from Source	
Chapter 2	High-level Overview	
Chapter 3	TensorFlow-ZenDNN Plug-in v0.2	
Chapter 4	Environment Variables	
Chapter 5	Tuning Guidelines	
5.1	System	
5.2	Environment Variables	
Chapter 6	Convolution Algorithm Logic	
Chapter 7	Technical Support	

	List	of	Fig	ures
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List of Tables

Table 1.	ZenDNN Environment Variables-Generic	.11
Table 2.	ZenDNN Environment Variables-Optimization	.12
Table 3.	System Specification	.13
Table 4.	Convolution Algorithm Logic	.15

Revision History

Date	Revision	Description
October 2023	0.2	 Updated supported ZenDNN and TensorFlow versions. Added section 1.1.2 (Build and Install from Source). Removed Chapter 6 (Blocked Format Support).
March 2023	0.1	Initial version.

Chapter 1 Installing TensorFlow-ZenDNN Plugin

Note: Refer to the ZenDNN 4.1 User Guide before starting the installation.

1.1 TensorFlow-ZenDNN Plug-in Setup

This section describes the procedure to setup the TensorFlow-ZenDNN plug-in for TensorFlow v2.14.

1.1.1 Install with Binary Release

Complete the following steps to install the TensorFlow-ZenDNN plug-in binary release:

1. Install TensorFlow v2.14:

```
pip install tensorflow-cpu==2.14.0
```

- 2. Download the TensorFlow-ZenDNN plug-in wheel file from the *Community supported TensorFlow builds*.
- 3. Install TensorFlow-ZenDNN plug-in:

```
$ pip install tensorflow_zendnn_plugin-0.2.0-cp39-cp39-linux_x86_64.whl
```

The release binaries for TensorFlow-ZenDNN plug-in v0.2 are compiled with manylinux2014 and they provide compatibility with some older Linux distributions.

The release binaries are tested with the recent Linux distributions such as:

- Ubuntu 20.04 and later
- RHEL 9.0 and later

1.1.2 Build and Install from Source

To build and install the TensorFlow-ZenDNN plug-in from source:

Clone the repository:

```
$ git clone https://github.com/amd/ZenDNN-tensorflow-plugin.git
$ cd ZenDNN-tensorflow-plugin/
```

The repository defaults to the master development branch. To build the TensorFlow-ZenDNN plug-in v0.2, you must check out the tag v0.2.

Follow the *build and install from source steps* to configure, build, and install the TensorFlow-ZenDNN plug-in.

Enable TensoFlow-ZenDNN Plug-in

Set the following environment variables to enable ZenDNN for inference:

- TF ENABLE ZENDNN OPTS=1
- TF_ENABLE_ONEDNN_OPTS=0

By default, TensorFlow is shipped with oneDNN enabled.

To run a sample with the installed TensorFlow-ZenDNN plug-in, follow the instructions in *Unified Inference Frontend (UIF) 1.1 User Guide - Run a CPU Example*.

Chapter 2 High-level Overview

The following is a high-level block diagram for the TensorFlow-ZenDNN plug-in package which utilizes ZenDNN as the core inference library:

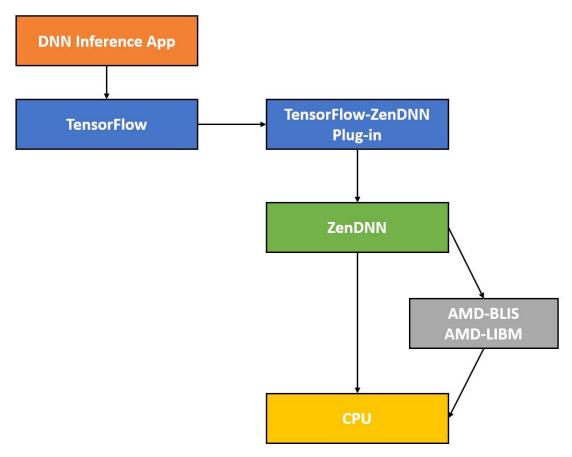


Figure 1. TensorFlow-ZenDNN Plug-in

Chapter 3 TensorFlow-ZenDNN Plug-in v0.2

TensorFlow-ZenDNN plug-in v0.2 is the AMD CPU plug-in release with Pluggable device approach of TensorFlow:

- This plug-in v0.2 is supported for TensorFlow v2.14 and later.
- The TensorFlow-ZenDNN plug-in open source is available at: https://github.com/amd/ZenDNN-tensorflow-plugin
- It is integrated with ZenDNNv4.1 as the core inference library and compiled with GCC v9.3.1.
- As compared to the current TensorFlow-ZenDNN direct integration releases, this release provides:
 - On par performance for models, such as RefineDet, Inception, and VGG variants.
 - Sub-optimal performance for models, such as ResNet, MobileNet and EfficientNet.

Chapter 4 Environment Variables

TensorFlow-ZenDNN plug-in uses the following environment variables to tune performance and control logs:

Table 1. ZenDNN Environment Variables-Generic

Environment Variable	Default Value/User Defined Value
ZENDNN_LOG_OPTS	ALL:0
TF_ZEN_PRIMITIVE_REUSE_DISABLE	False
ZENDNN_ENABLE_MEMPOOL	The default value is set to 1, you can provide the value 0 to disable it. 1 is for Graph-based MEMPOOL and 2 is for Node-based MEMPOOL.
ZENDNN_PRIMITIVE_CACHE_CAPACITY	The default value is set to 1024, you can modify it as required
ZENDNN_TENSOR_BUF_MAXSIZE_ENABLE	0
TF_ENABLE_ZENDNN_OPTS	Default value is set to 0. Set it to 1 along with TF_ENABLE_ONEDNN_OPTS=0 for enabling ZenDNN for inference. You can set it to 0 when you want to enable vanilla training and inference.
TF ENABLE_ONEDNN_OPTS	Default value is set to 1. By default, TensorFlow is shipped with oneDNN optimizations enabled. Hence, set it to 0 when you enable ZenDNN.

Table 2. ZenDNN Environment Variables-Optimization

Environment Variable	Default Value/User Defined Value
OMP_NUM_THREADS	Set it as per the number of cores in the user system ^a .
OMP_DYNAMIC	Set it to FALSE for optimal performance ^a .
OMP_PROC_BIND	Set it to FALSE for optimal performance ^a .
GOMP_CPU_AFFINITY	Set it as per the number of cores in the system being used ^a .
ZENDNN_TENSOR_POOL_LIMIT	The default value is set to 1024. You can modify it to 512 for CNNs for optimal performance.
ZENDNN_CONV_ALGO	The default value is set to 1. Decides the convolution algorithm to be used in execution and the possible values are: • 1 = im2row followed by GEMM • 2 = WinoGrad (fallback to im2row GEMM for unsupported input sizes) • 3 = Direct convolution with blocked inputs and filters • 4 = Direct convolution with blocked filters
ZENDNN_GEMM_ALGO	The default value is 3. You can modify it to one of the following: • 0 = Auto • 1 = AOCL-BLIS path • 2 = Partial AOCL-BLIS • 3 = ZenDNN JIT path • 4 = ZenDNN partial JIT path Note: Auto is an experimental feature and should be used with application warm-up iteration >=15.

a. You must set these environment variables explicitly.

Chapter 5 Tuning Guidelines

The hardware configuration, OS, Kernel, and BIOS settings play an important role in performance. The details for the environment variables used on a 4th Gen AMD EPYCTM server to achieve the optimal performance numbers are as follows:

5.1 System

A system with the following specifications has been used:

Table 3. System Specification

Model name	4 th Gen AMD EPYC TM 9654P 96-Core Processor
DPU MHz	Up to 3.7 GHz
No of Cores	96
1P/2P	1
SMT: Thread(s) per Core	2
Mem-Dims	12x64 GB

OS Used: Ubuntu 20.04.02 LTS

5.2 Environment Variables

The following environment variables have been used:

ZENDNN LOG OPTS=ALL:0

OMP NUM_THREADS=96

OMP_WAIT_POLICY=ACTIVE

OMP PROC BIND=FALSE

OMP DYNAMIC=FALSE

ZENDNN ENABLE MEMPOOL=1

ZENDNN GEMM ALGO=3

Note: For NLP and Recommender models, better performance is observed with ZENDNN GEMM ALGO=4. However, these details should be verified empirically.

ZENDNN TENSOR POOL LIMIT=1024

ZENDNN TENSOR BUF MAXSIZE ENABLE=0

ZENDNN_CONV_ALGO=4

ZENDNN_PRIMITIVE_CACHE_CAPACITY=1024
TF_ENABLE_ZENDNN_OPTS=1
TF_ENABLE_ONEDNN_OPTS=0
GOMP_CPU_AFFINITY=0-95

The environment variables OMP NUM THREADS, OMP WAIT POLICY,

OMP_PROC_BIND, and GOMP_CPU_AFFINITY can be used to tune performance. For optimal performance, the **Batch Size** must be a multiple of the total number of cores (used by the threads). On a 4th Gen AMD EPYCTM server (configuration: AMD EPYCTM 9654P 96-Core, 2P, and **SMT=ON**) with the above environment variable values, **OMP_NUM_THREADS=96** and **GOMP_CPU_AFFINITY=0-95** yield the best throughput numbers for a single socket.

A few of the models (for example, publicly available ResNet50) gain performance with Transparent Huge Pages settings (THP). THP can be enabled as a sudo user using the following command:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

For more information, refer to the Tuning Guidelines section of the ZenDNN 4.1 User Guide.

Chapter 6 Convolution Algorithm Logic

Convolution kernels take Input and Filter/Weights as arguments and return Output. The table below describes the expected Layout for each of the convolution algorithms currently supported by TensorFlow-ZenDNN.

Table 4. Convolution Algorithm Logic

zenConvAlgoType	ZENDNN_CONV_ALGO	Input Layout	Filter Layout	Output Layout
GEMM	1	NHWC	HWIO	NHWC
WINOGRAD	2	NHWC	HWIO	NHWC
DIRECT1	3	nChw8c	Ohwi8o	nChw8c
DIRECT2	4	NHWC	Ohwi8o/ Ohwi16o	NHWC

Note: In the context of Filter Layouts, HWIO is equivalent to HWCN but with I instead of C representing input channels and O instead of N representing output channels.

Chapter 7 Technical Support

Please email *zendnnsupport@amd.com* for questions, issues, and feedback.