

Yükleme için:

https://www.labcenter.com/downloads/

Tutorial için:

https://labcenter.s3.amazonaws.com/downloads/Tutorials.pdf

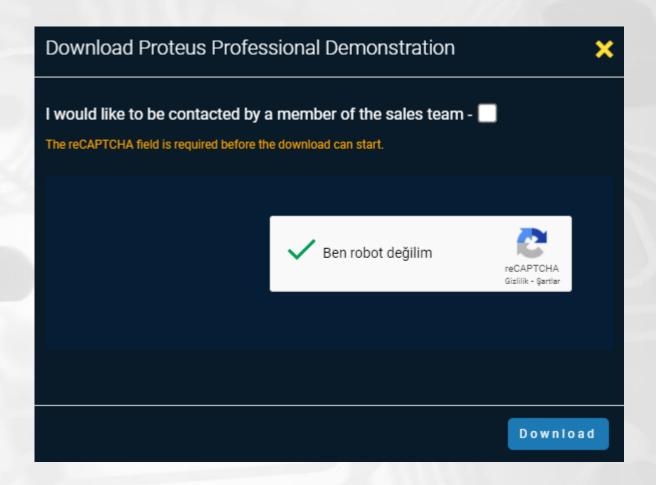


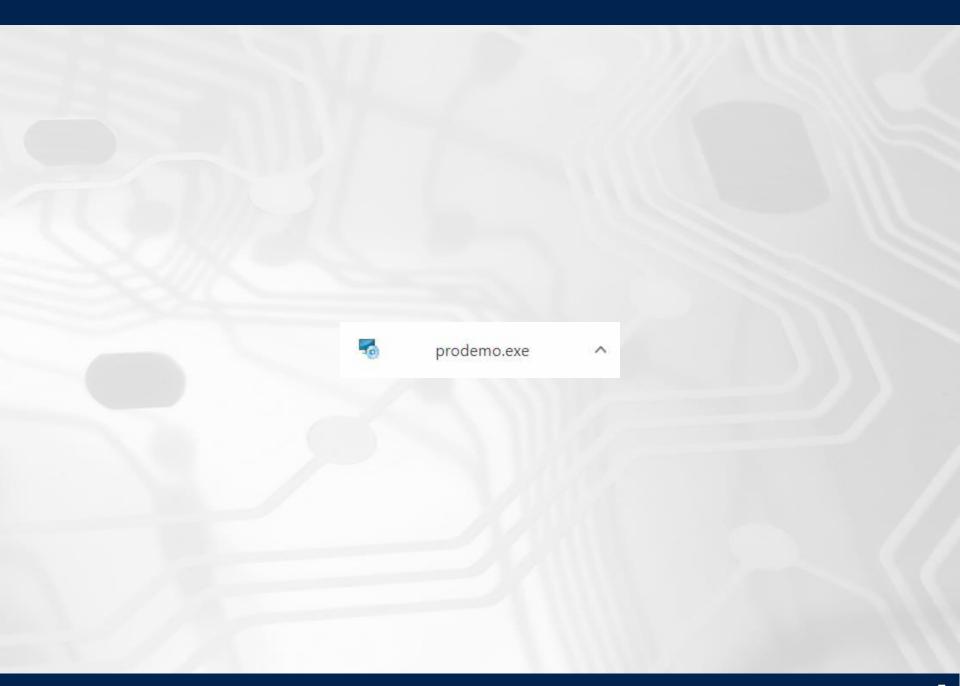
The Limitations of the demonstration version are as follows:

- ✓ Not time limited!
- ✓ Extensive set of sample designs included to help you evaluate all aspects of the software.
- √ You can write your own software to run on existing sample designs for evaluation purposes.
- × You can only print Schematics and Layouts from the sample designs.
- × You cannot save your work.
- × You cannot simulate your own microcontroller designs.

Please note using a download manager or accelerator may corrupt the file.

Download the Demonstration version here.





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PROTEUS PROFESSIONAL LICENCE AGREEMENT

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1. LICENCING

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Choose the installation you want



Typical

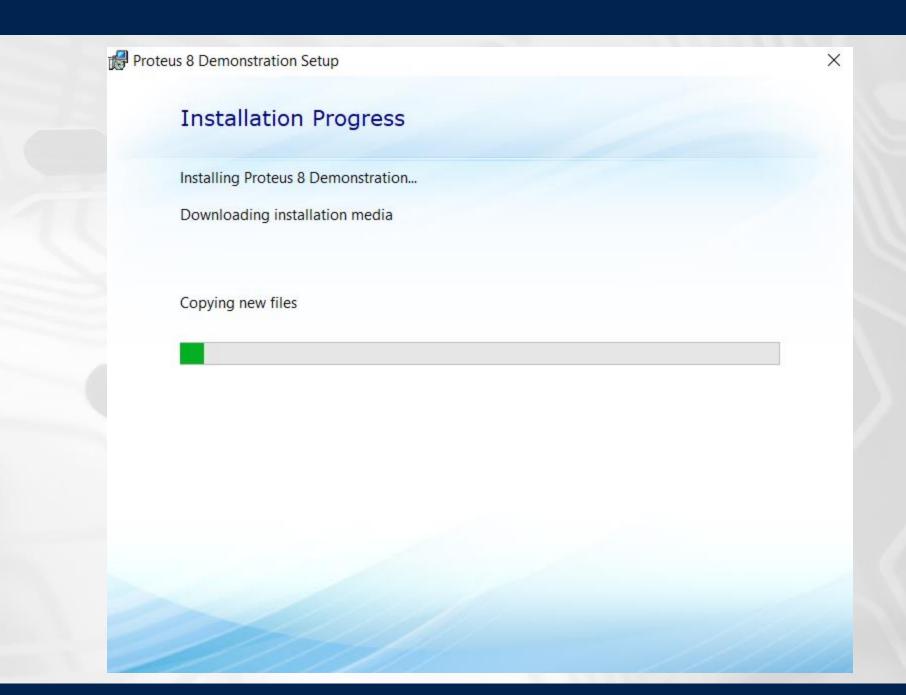
Installs the most common program features. Recommended for most users.

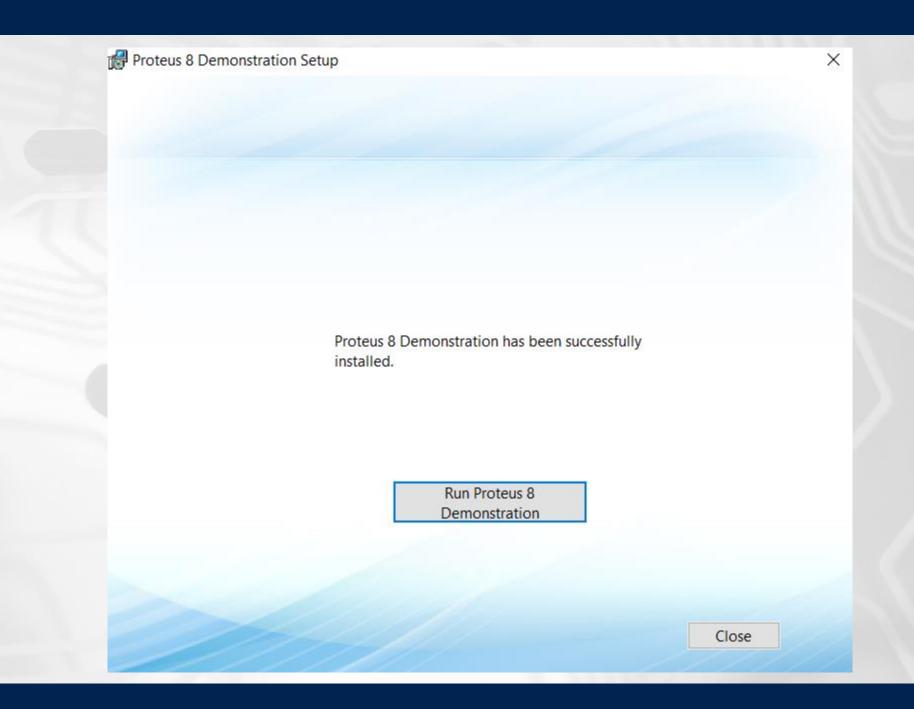


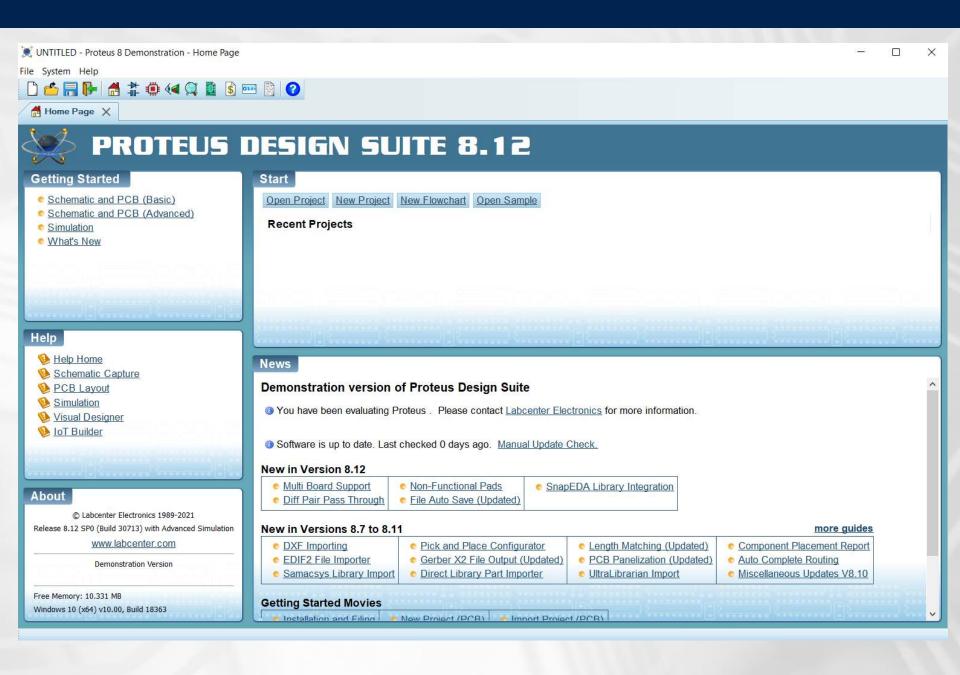
Custom

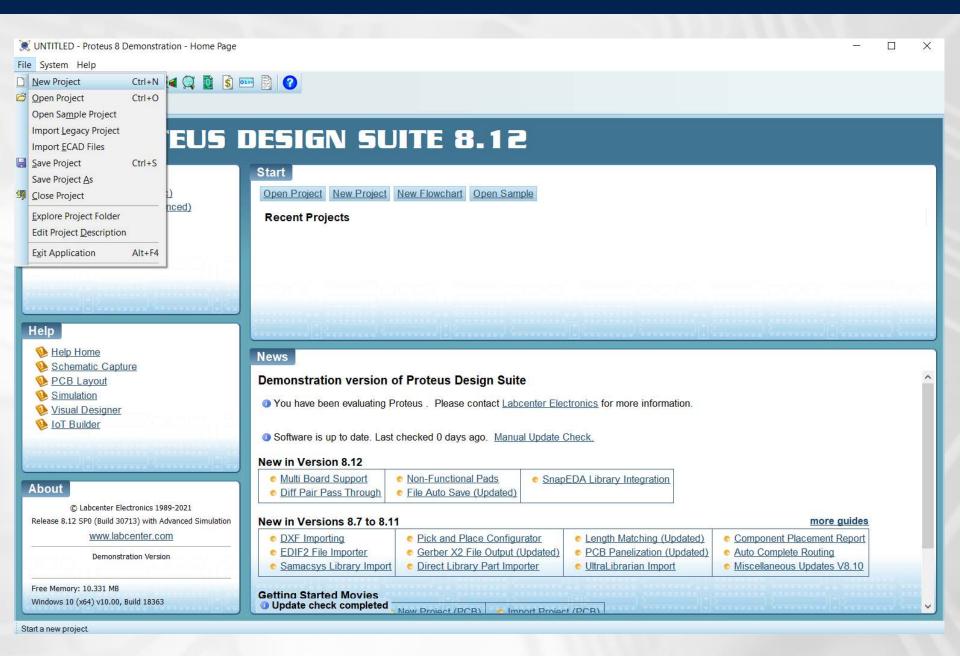
Allows users to choose which program features will be installed and where they will be installed. Recommended for advanced users.

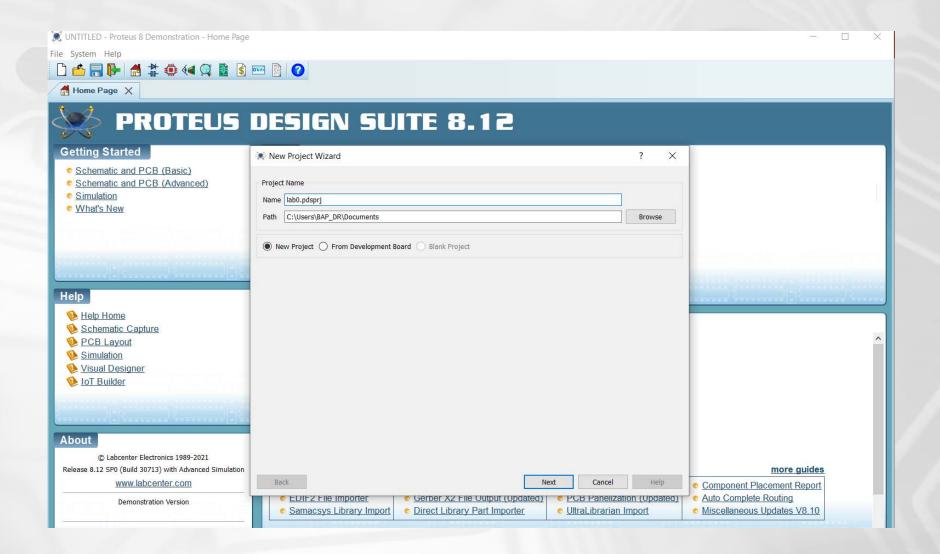
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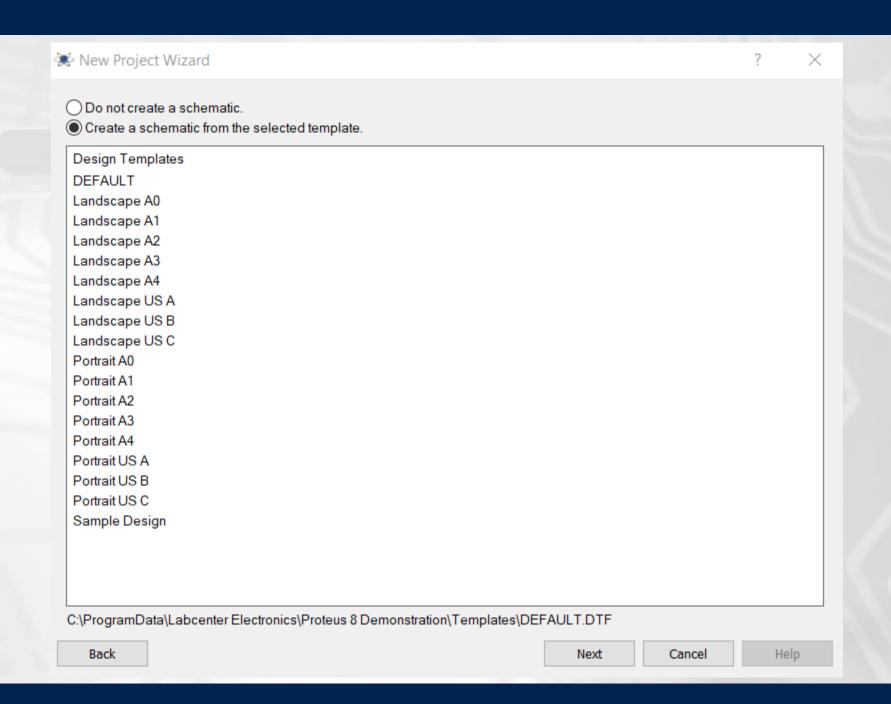












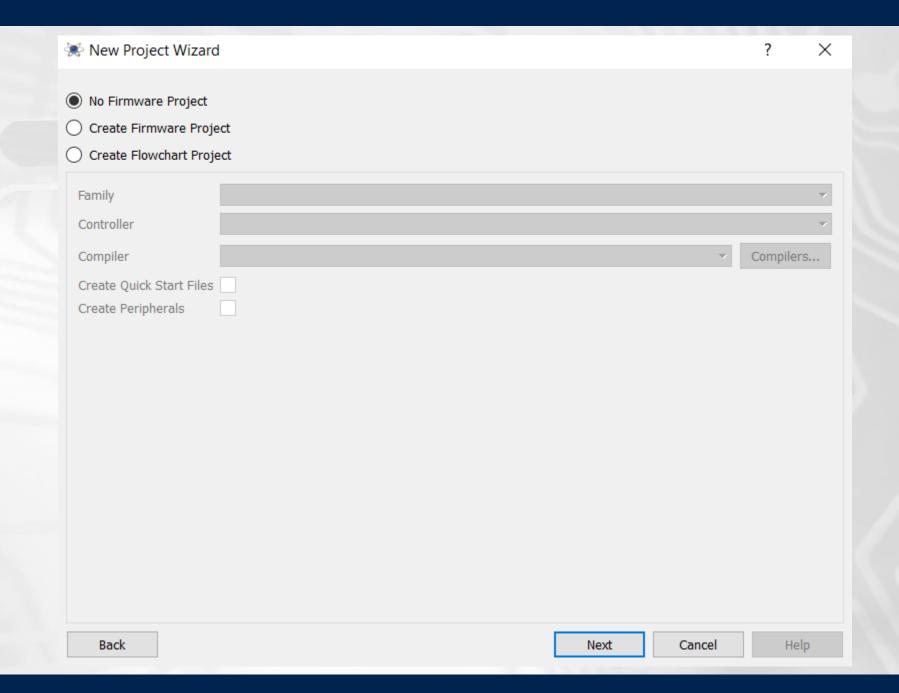
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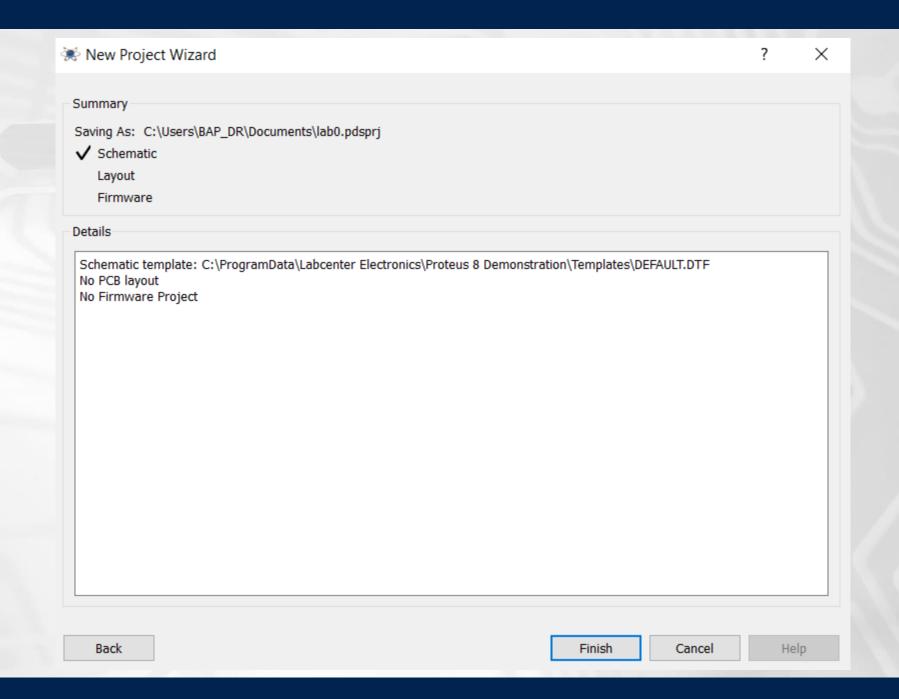
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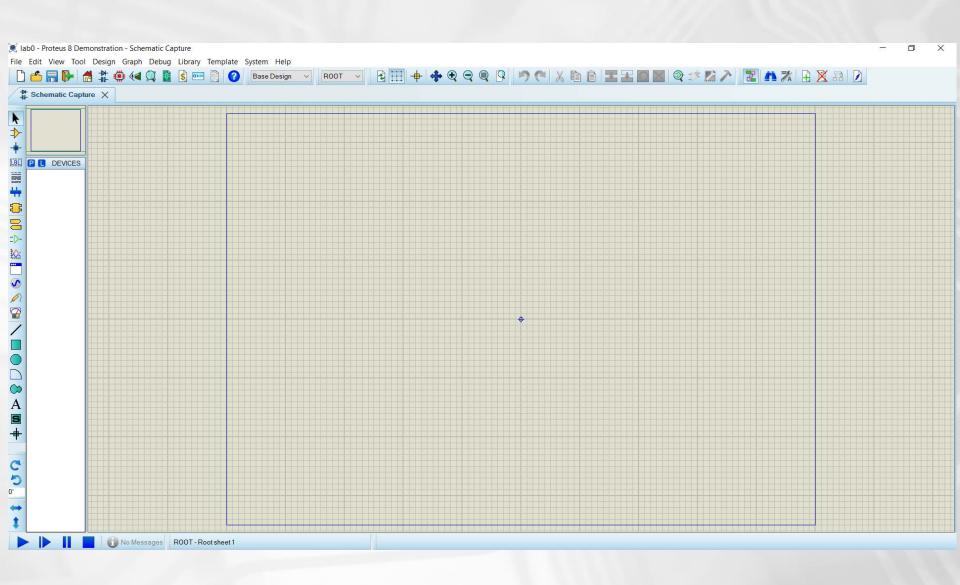
Help

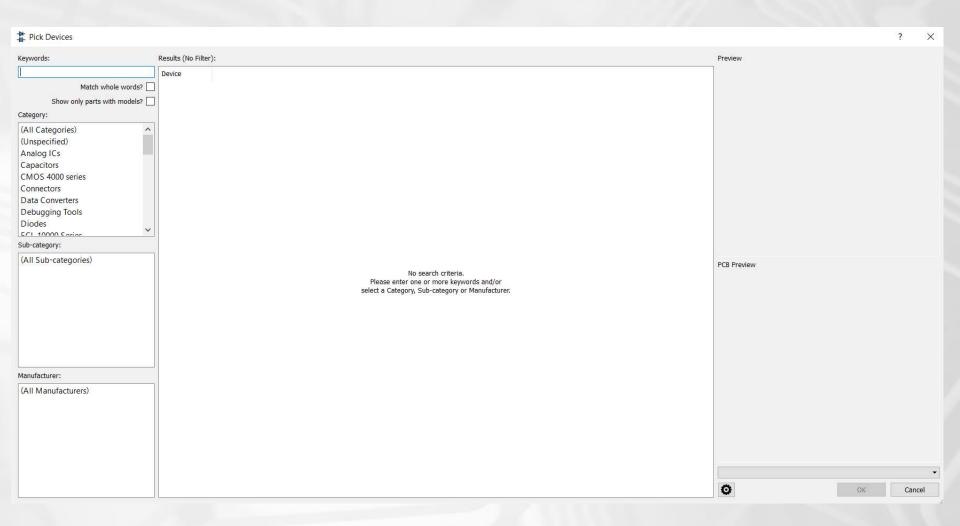
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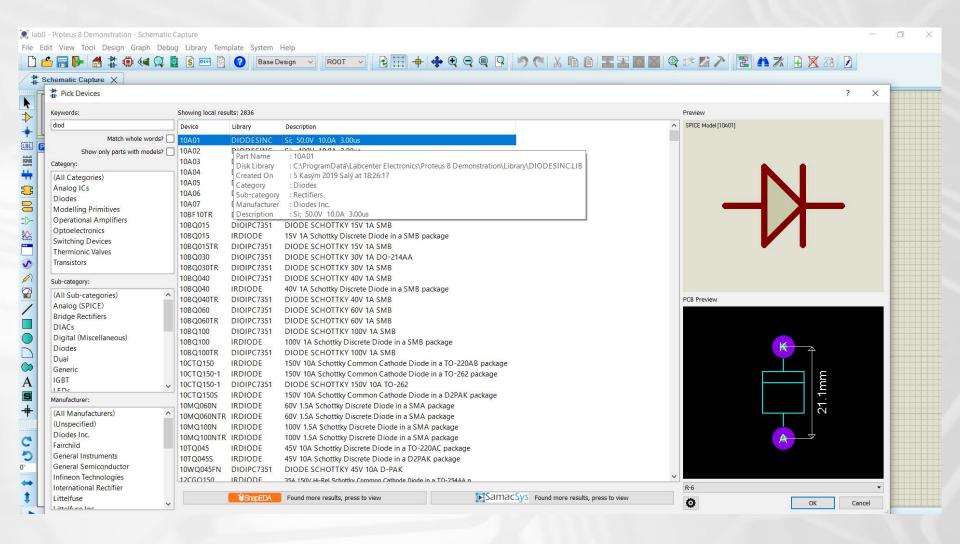
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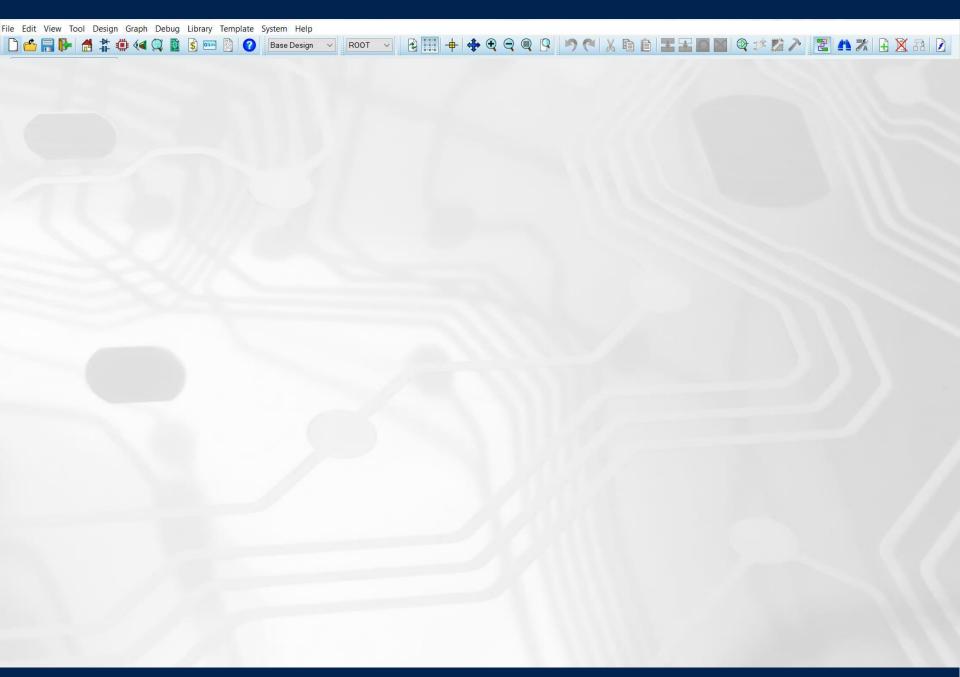


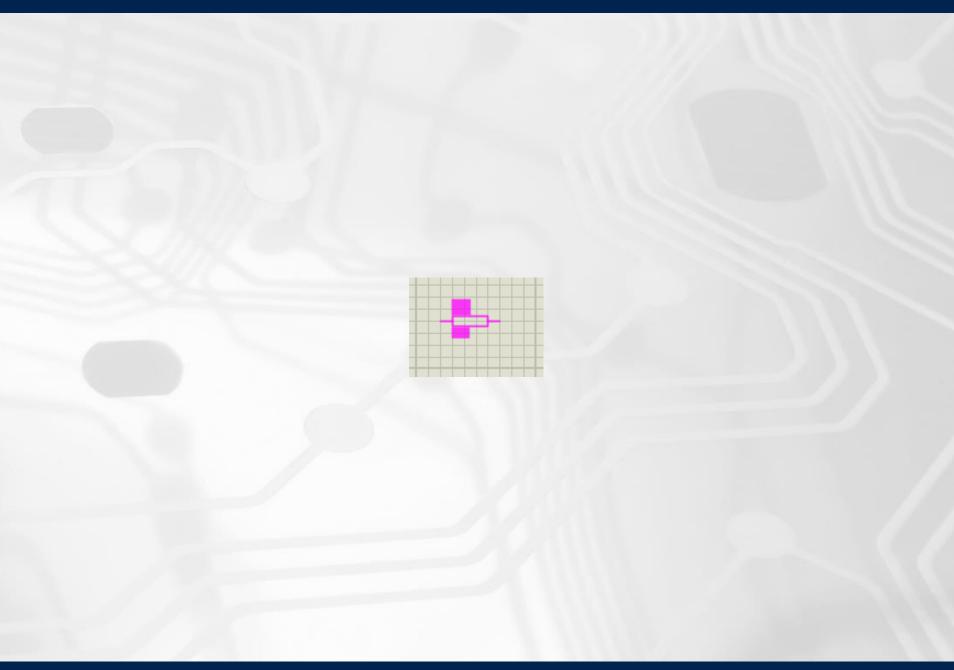


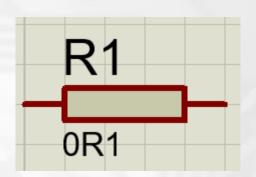


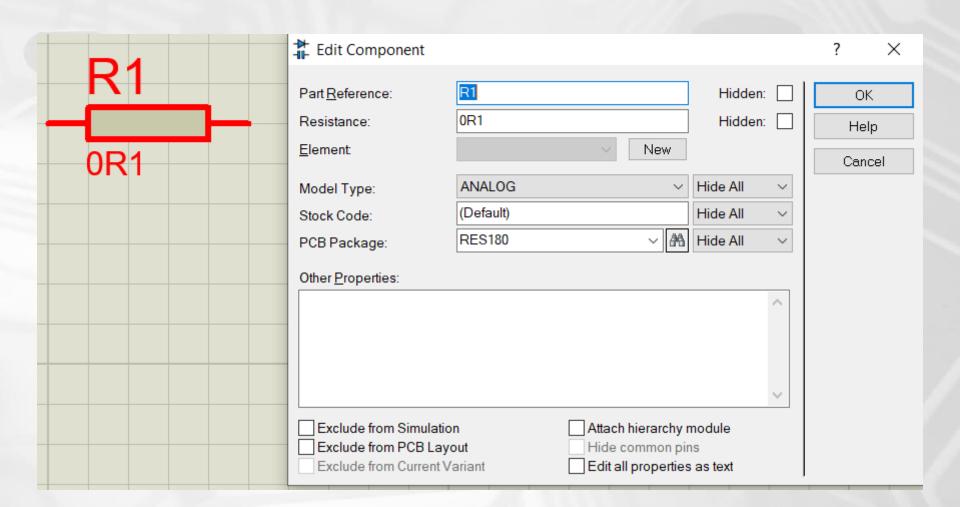


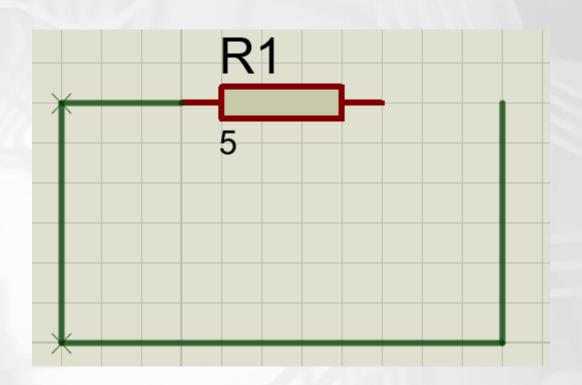


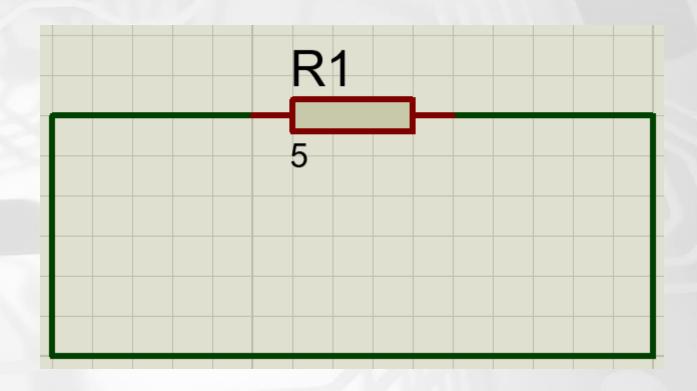


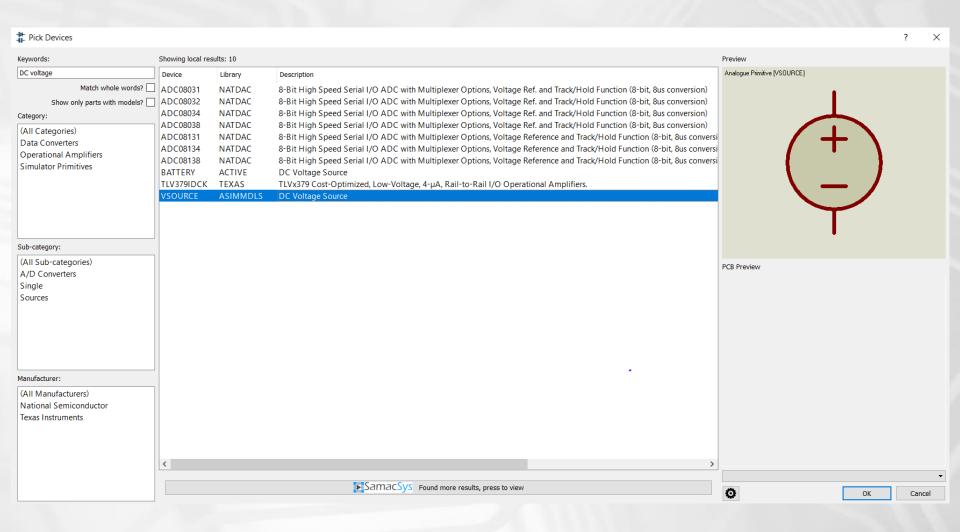


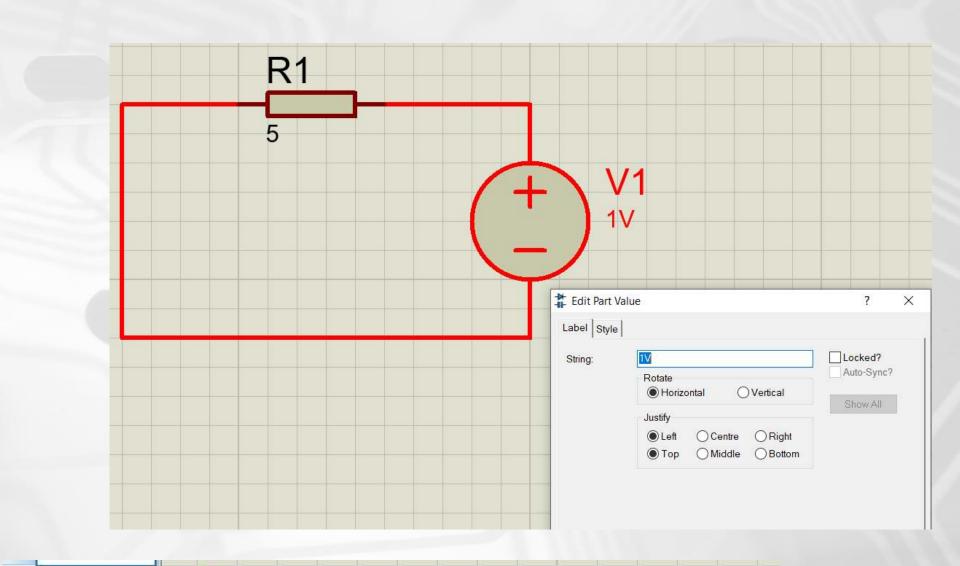








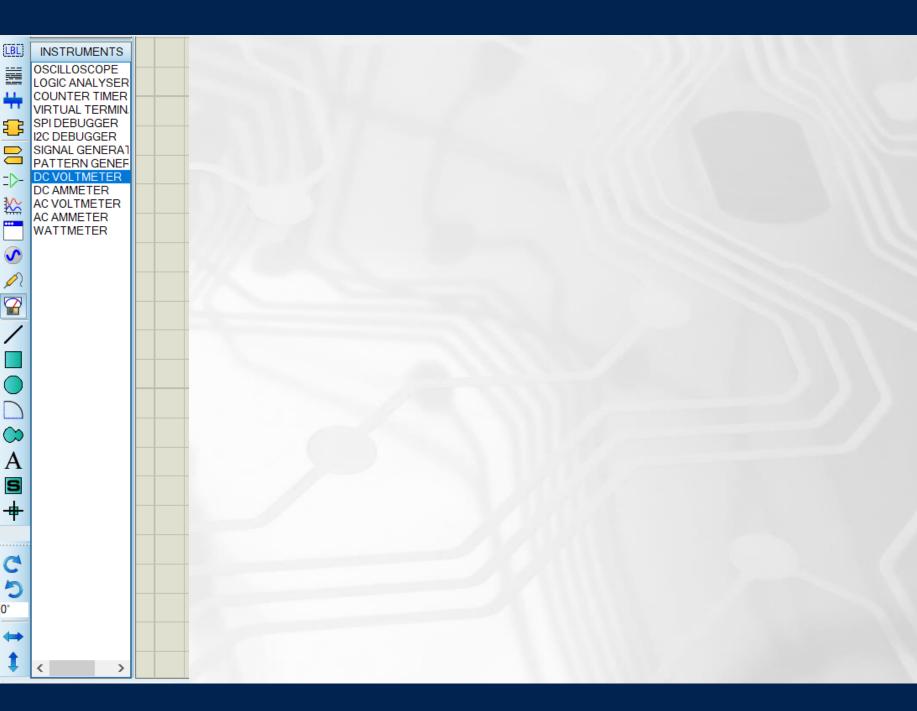


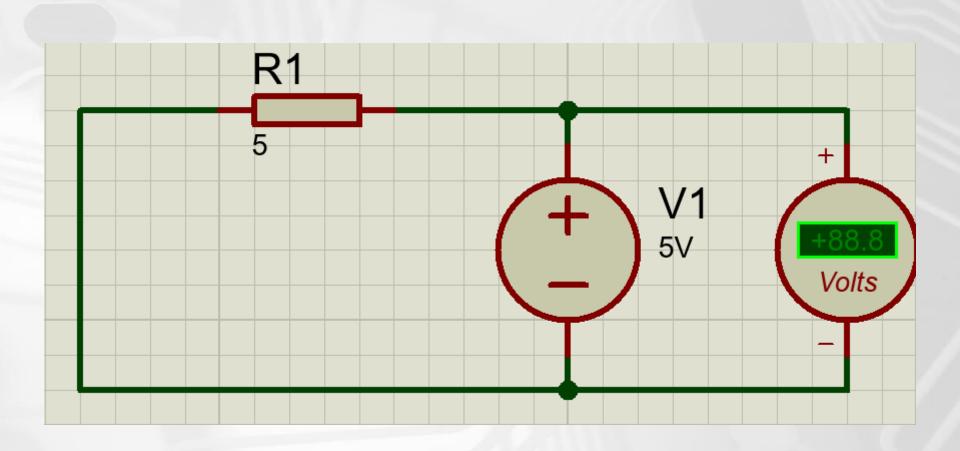


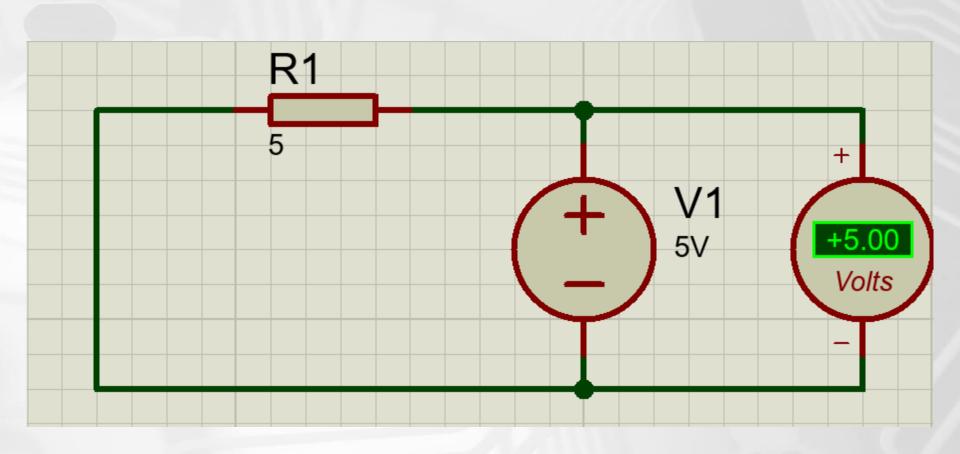
Shows the currently loaded components.

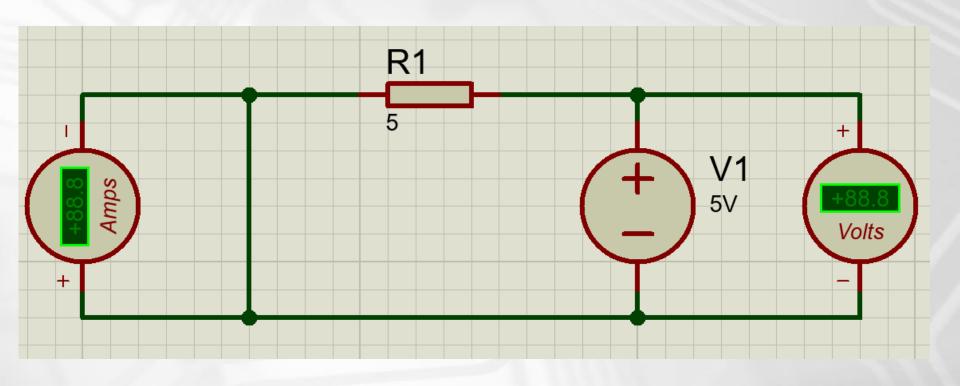
2 Message(s)

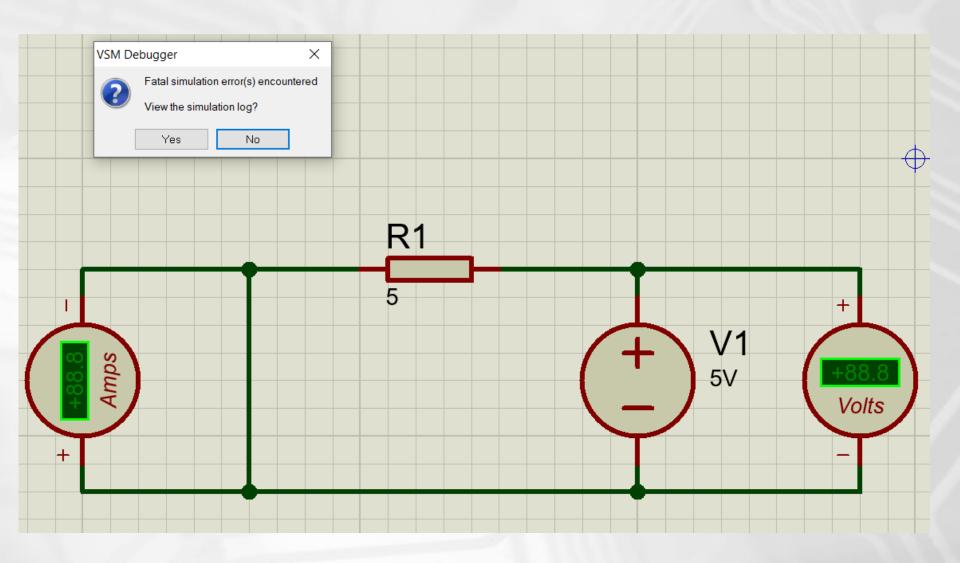












Message

- 🚺 ISIS Release 8.12.00 (Build 30713) (C) Labcenter Electronics 1990-2021.
- Compiling design 'C:\Users\BAP_DR\Documents\lab0.pdsprj'.
- Netlist compilation completed OK.
- Netlist linking completed OK.
- Partition analysis completed OK.
- Simulating partition 1
- PROSPICE 8.08.00 (Build 29194) (C) Labcenter Electronics 1993-2021.
- € Loaded netlist 'C:\Users\BAP_DR\AppData\Local\Temp\LISA4777.SDF' for design 'lab0.pdsprj'
- SPICE] singular matrix: check node #\$IDC AMMETER#0004 IP1#branch.
- [SPICE] singular matrix: check node #\$IDC AMMETER#0004 IP1#branch.
- ▲[SPICE] Gmin stepping failed
- SPICE] singular matrix: check node #\$IDC AMMETER#0004 IP1#branch.
- ▲[SPICE] Source step [0 of 120] failed: source factor = 0.0000
- ▲[SPICE] (null).
- Real Time Simulation failed to start.



