



Certificate no: UC-7ae5f086-05a8-4c1c-a4ba-2941b34604fe

Certificate url: ude.my/UC-7ae5f086-05a8-4c1c-a4ba-2941b34604fe

Reference Number: 0004

CERTIFICATE OF COMPLETION

Verilog HDL: VLSI Hardware Design Comprehensive Masterclass

Instructors **Shepherd Tutorials**

Akshay S S

Date **June 7, 2025**

Length **12.5 total hours**